

ARMY  
NAVY  
AIR FORCE

TM 11-5895-856-34-10  
EE640-CA-MMI-100/E154 CPU  
TO 31W2-2T-122-10

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**TECHNICAL MANUAL**

**DIRECT SUPPORT AND GENERAL SUPPORT  
MAINTENANCE MANUAL  
FOR  
CENTRAL, MESSAGE SWITCHING, AUTOMATIC  
AN/TYC-39(V)1  
AND  
CENTRAL OFFICE, TELEPHONE, AUTOMATIC  
AN/TTC-39(V)2**

**AUTOMATIC DATA PROCESSING  
ASSEMBLIES**

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**DEPARTMENTS OF THE ARMY, NAVY, AND AIR FORCE**

**22 SEPTEMBER 1983**



**5**

**SAFETY STEPS TO FOLLOW IF SOMEONE IS THE VICTIM OF ELECTRICAL SHOCK**

**1**

**DO NOT TRY TO PULL OR GRAB THE INDIVIDUAL**

**2**

**IF POSSIBLE, TURN OFF THE ELECTRICAL POWER**

**3**

**IF YOU CANNOT TURN OFF THE ELECTRICAL POWER, PULL, PUSH, OR LIFT THE PERSON TO SAFETY USING A WOODEN POLE OR A ROPE OR SOME OTHER INSULATING MATERIAL**

**4**

**SEND FOR HELP AS SOON AS POSSIBLE**

**5**

**AFTER THE INJURED PERSON IS FREE OF CONTACT WITH THE SOURCE OF ELECTRICAL SHOCK, MOVE THE PERSON A SHORT DISTANCE AWAY AND IMMEDIATELY START ARTIFICIAL RESUSCITATION**

**WARNING**

**HIGH VOLTAGE**

is used in the operation of this equipment.

**DEATH ON CONTACT**

may result if personnel fail to observe safety precautions. Learn the areas containing high voltage in each piece of equipment. Be careful not to contact high-voltage connections when installing or operating this equipment. Before working inside the equipment, turn power off and ground points of high potential before touching them.

**WARNING**

**USE OF CLEANING SOLVENT**

Adequate ventilation should be provided while using TRICHLOROTRIFLUOROETHANE (NSN 6850-00-105-3084). Prolonged breathing of vapor should be avoided. The solvent should not be used near heat or open flame; the products of decomposition are toxic and irritating. Since TRICHLOROTRIFLUOROETHANE dissolves natural oils, prolonged contact with skin should be avoided. When necessary, use gloves which the solvent cannot penetrate. If the solvent is taken internally, consult a physician immediately.

INSERT LATEST CHANGED PAGES. DESTROY SUPERSEDED PAGES.

**LIST OF EFFECTIVE PAGES**

Dates of issue for original and changed pages are:

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**TOTAL NUMBER OF PAGES IN THIS PUBLICATION IS 432 CONSISTING OF THE FOLLOWING.**

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TECHNICAL MANUAL  
NO. 11-5895-856-34-10  
TECHNICAL MANUAL  
EE640-CA-MMI-100/E154 CPU  
TECHNICAL ORDER  
TO 31W2-2T-122-10

DEPARTMENTS OF THE ARMY  
THE NAVY, AND  
THE AIR FORCE

Washington, DC, 22 September 1983

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**AUTOMATIC DATA PROCESSING  
ASSEMBLIES**

**REPORTING ERRORS AND RECOMMENDING IMPROVEMENTS**

You can help improve this manual. If you find any mistakes or if you know of a way to improve the procedures, please let us know. Mail your letter, DA Form 2028 (Recommended Changes to Publications and Blank Forms), or DA Form 2028-2 located in back of this manual direct to: Commander, US Army Communications-Electronics Command and Fort Monmouth, ATTN: DRSEL-ME-MP, Fort Monmouth, New Jersey 07703.

For Air Force, submit AFTO Form 22 (Technical Order System Publication Improvement Report and Reply) in accordance with paragraph 6-5, Section VI, T.O. 00-5-1. Forward direct to prime ALC/MST.

For Navy, mail comments to the Commander, Naval Electronics Systems Command, ATTN: ELEX 8122, Washington, DC 20360.

In either case, a reply will be furnished direct to you.

## TABLE OF CONTENTS

### VOLUME 10 TM 11-5895-856-34-10

Card Cage Assembly, Interface Control Unit - R.H., Wire List, Logic (149015-860)

This volume is part of a 24-volume set covering the direct support and general support maintenance of the automatic data processing assemblies. This volume contains the logic wire list for the right-hand card cage assembly of the interface control unit, drawing number 149015-860. Refer to volume 1 of this series (TM 11-5895-856-34-1) for an explanation of how to use this wire list as well as other lists contained in the set.

### NOTE

**The pages in volumes 2 through 24 of TM 11-5895-856-34 have been numbered in a special manner. Pages within these volumes are found by keying to two page identifiers: the drawing number and the page number. To find the page that you desire within the volume, follow the steps listed below:**

1. Find the applicable wire run list in the table of contents and note the applicable drawing number.
2. Look through the pages of the volume until you find the particular drawing number of the wire run list that you seek. This will insure that you are in the correct wire run section.
3. Go through these pages until you find the page number you are looking for within this particular wire run list.

Remember, pages in the volumes cited above are found by keying to the drawing number applicable to a particular wire run list, as well as, a page number. Make sure you are on the correct page by checking both page identifiers.

**CARD CAGE ASSEMBLY, INTERFACE CONTROL UNIT  
- RIGHT HAND  
LOGIC WIRE LIST  
149015-860**

**NOTES:** UNLESS OTHERWISE SPECIFIED

1. REFERENCE TO SHEET 3 FOR DEFINITION OF FIELDS.
2. REFERENCE TO SHEET 4 FOR CONFIGURATIONS OF SHIELD AND WIRE TERMINATIONS.
3. REFERENCE TO SHEET 5 OF STRING LIST FOR WIRE CODE DEFINITIONS.
4. REFERENCE TO SHEET 6 OF STRING LIST FOR WIRE PARTS LIST.
5. ALL ABBREVIATIONS PER MIL-STD-12.
6. THROUGHOUT THE BODY OF THIS DOCUMENT THE UNIT NAME IS REFERRED TO AS: CARD CAGE ASSEMBLY IFCU.

**H78 STRING AND CONNECTOR LIST, DEFINITION OF FIELDS**

1. **Record Number** - A unique Data Processing number which associates all information pertaining to a wire "FROM" Connector, "TO" Connector, Wire Code, etc. This number is the wire ID when that field is blank.
2. **Prefix** - An assembly alphanumeric to be used when a wire terminates in two assemblies. This number will be the reference designation as required by USAS Y32.16-1968.
3. **Connector** - Any type of terminating point, plug, receptacle, etc. Designations are in accordance with USAS Y32.16-1968.
4. **Pin** - Exact terminating point of the respective connector. Designations are unique:
  - A. SHXXXX indicates the junction of a shield and a pigtail, the four digits to the right are the wire identity of the shielded wire.
  - B. JCT indicates a common point of two or more shield pigtails.
  - C. Jacket: the terminology used when describing the line that defines the identification of a shielded wire.
5. **Sh Fg** - References a graphic representation showing how a shielded wire or coax is to be terminated. A number in this field indicates the level of automatic wire wrapping.
6. **Multi Group** - Associates wires of a group such as "twisted wire" or "shielded wire". Jacket, pigtails and center conductors will be shown as a common group.
7. **Wire Code** - A three-digit code for wire type and gage or bus bar.
8. **Wire Color** - Standard RETMA color code.
  - A. Base Stripe Tracer.
  - B. Stripe, Tracer 1 and Tracer 2 if the digit to the left is other than 9 and the two positions to the right are not blank and not equal. The Base Color is understood to be white.
9. **Wire Ident** - A number used for reference to differentiate one wire from another. This number will be used to identify the wire when specified in the wire list sleeve code field.
10. **Spc Inst** - A code which indicates that a wire must be given special attention as follows:
  - A. Direct routing, no service loops, no harnessing.
  - B. See general notes or instruction pages.
  - C. See general notes or instruction pages.
  - D. See general notes or instruction pages.
  - E. See general notes or instruction pages.
  - F. See signal description.
  - G. This connection does not go directly to the "TO" connector, but intersects a line going to the "TO" connector.
  - H. See special routing page.
    - I. Junction point for multilayer laminated board (MLB) connection.
  - J. Denotes a bus reference point.
  - K. Blank out "TO" connector and pin.
  - L. Will cause a signal name of three characters or less to be entered in the string list.
  - M. Will cause a record to be omitted from the string list. (This record will print in the connector list.)
  - N. Will suppress printing the wire Identity in the harness string and double entry list.
  - P. Will cause the equation to be used as the signal name for sorting purposes only in the string list.

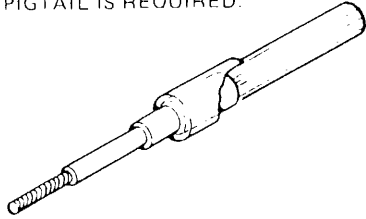
**H78 STRING AND CONNECTOR LIST, DEFINITION OF FIELDS - Continued**

- Q. Will cause an equation record to be omitted from logic listing.
  - R. Will suppress printing the "FROM, TO" pin number in the string and connector lists.
  - S. Do not move record number to the ident field for an ADD transaction in the harness string and double entry only. (Use only when adding a file.)
  - T. Twist wire code.
  - U. Not available.
  - V. See general notes or instruction pages.
  - W. Fixed wire length submitted.
  - X. Sequence of string ls to be left as is.
  - Y. See general notes or instruction pages.
  - Z. Will suppress printing of the 'FROM" pin.
10. **Misc** - Unless otherwise noted, the number 1 indicates a pre-wired connector.
  11. **Signal** - An alphanumeric signal name, mnemonic where feasible, which identifies one specific function from another.
    - SPP - Denotes an available termination.
    - SPF - Denotes an unwired termination which has an assigned function.
    - SPW - Denotes a non-functional wire which is terminated at one or both ends.
    - SPO - Denotes a spare output of a circuit.
    - DNW - Indicates that a termination may not be wired.
    - SPA - Denotes an unassigned circuit as one of a group on a printed circuit board.
    - SPI - Denotes a spare output of a circuit.
    - SPG - Denotes an unassigned logic gate, as one of a group on a printed circuit board.
    - SPR - Denotes a spare resistor.
    - SPD - Denotes an input diode of an assigned logic gate.
  12. **Scq No** - A number which in conjunction with SIGNAL. allows a signal string to be consistently printed in a given order..
  13. **Equation** - A mnemonic, named assigned to each gate of an element.
  14. **Term** - An "OR" function composed of one or more factors
  15. **Factor** - A specified input to a logical gate or active element.
  16. **Ckt or Chip Type** - Denotes a specific board type..
  17. **Group** - Denotes a specific circuit on a printed circuit board.
  18. **Load or Power Plane** - Denotes the current draw in milliamps by a specific circuit or voltage.
  19. **AND Test Point** - Denotes the input test point for a specific circuit on a printed circuit board.
  20. **OR Test Point** - Denotes the output test point for a specific circuit on a circuit board.
  21. **Signal Description** - A written description or name of a signal or voltage.
  22. **ECO No.** - A letter number combination to show the Engineering Change Order level of that particular wire list record.

**Drawing No.**  
**149015-860**  
**Rev. C, sheet 3**

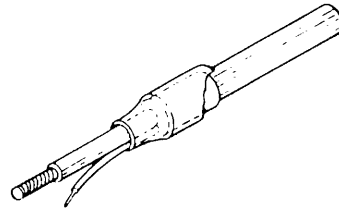
SHIELD FIGURE A

SHIELD TERMINATION FOR SHIELDED, SINGLE AND MULTIPLE CONDUCTORS, NO PIGTAIL IS REQUIRED.



SHIELD FIGURE B

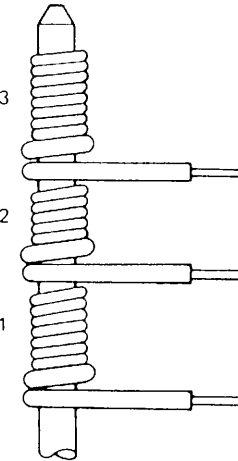
SHIELD TERMINATION FOR SHIELDED, SINGLE AND MULTIPLE CONDUCTORS, FRONT PIGTAIL IS REQUIRED.



SHIELD FIGURE NO. 3

SHIELD FIGURE NO. 2

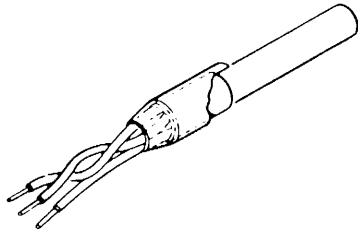
SHIELD FIGURE NO. 1



SHIELD FIGURE NUMBER INDICATES LEVEL OF WIRE WRAP

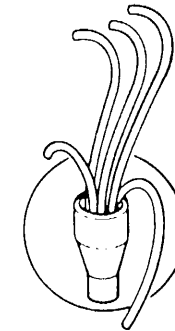
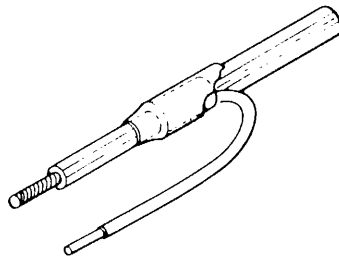
SHIELD FIGURE G

SHIELDED CABLE TERMINATION WITH DRAIN WIRE



SHIELD FIGURE C

SHIELD TERMINATION FOR SHIELDED, SINGLE AND MULTIPLE CONDUCTORS, A REAR PIGTAIL IS REQUIRED.

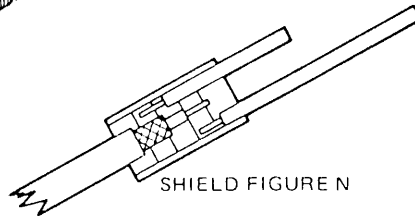
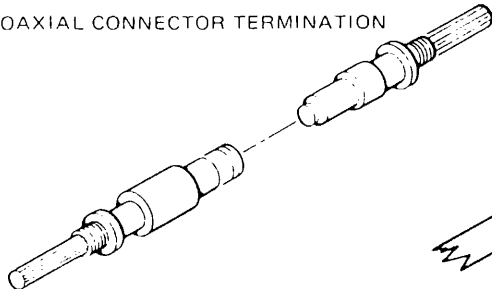


SHIELD FIGURE X

SHIELDED WIRE PIGTAILS—HYBRID

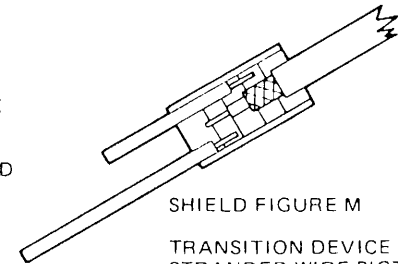
SHIELD FIGURE Z

COAXIAL CONNECTOR TERMINATION



SHIELD FIGURE N

TRANSITION DEVICE WITH SOLID WIRE PIGTAILS



SHIELD FIGURE M

TRANSITION DEVICE WITH STRANDED WIRE PIGTAILS

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	LOGIC FACTOR	FACTOR	COMMENT
			AND	OR					
XA213	TS8	A1	05B		JAEBRA	00	=		RESET OUTPUT COMMA
XA213	TS8	A1	02B		(11)	01		JK1290 JK08C0 JK04F0 JK03F0 JXCP3B SPI0012 SPI0032 SPI0022 01 02B 04 04A 05 03B 06 05A 07 03A 08 06A 10 07A 13 06B	
XA220	TT3	A1	04A		JAE80A	00	=		SET OUTPUT COMMANDEOB F/E
XA220	TT3	A1	05A		(04)	01		J00UTQ JXE81Q JXCP3B 06 05A 08 06A 10 07A	
XA215	T04	B1	11B		JAEN0A	00	=		START OUTPUT COMMAND
XA215	T04	B1	12A		(23)	01		J00UTQ JSNC2S JBUSYS JXCP3B 22 12A 24 13A 25 12B 26 14A	
XA225	T04	D2	24B		JAENOR	00	=		
XA225	T04	D2	23B		(45)	01		JAENOS JA0ENS JAEN1A JXRS0B 43 23B 46 21A 48 22A 50 23A	
XA224	TT3	D2	23B		JAENOS	00	=		OUTPUT COUNTER BIT0
XA224	TT3	D2	22B		(43)	01		JAENOR JAEN0A JAEN2A 41 22B 46 21A 48 22A	
XA223	TQ2	D2	21A		JAEN1A	00	=		
XA223	TQ2	D2	22A		(46)	01		JAENIS JXCP3B 48 22A 50 23A	
XA220	TT3	C3	19B		JAEN1R	00	=		
XA220	TT3	C3	16B		(39)	01		JAENIS JAEN3A JXRS0B 33 16B 35 17B 37 18B	
XA221	TQ2	C3	16B		JAENIS	00	=		OUTPUT COUNTER BIT1
XA221	TQ2	C3	14B		(33)	01		JAENIR JAEN4A 29 14B 31 15B	
XA213	TS8	C1	17B		JAEN2A	00	=		
XA213	TS8	C1	15A		(35)	01		J00UTQ JA0ENS JK1290 JKPE00 JXCP3B SPI0022 SPI0012 SPI0032 30 15A 31 15B 34 16A 36 17A 37 18B 38 18A 40 19A 42 20A	
XA220	TT3	D1	23A		JAEN2R	00	=		
XA220	TT3	D1	24A		(50)	01		JAEN2S JAEN5A JXRS0B 52 24A 54 25A 56 26A	
XA221	TQ2	D1	24A		JAEN2S	00	=		OUTPUT COUNTER BIT2
XA221	TQ2	D1	25A		(52)	01		JAEN2R JXED0A 54 25A 56 26A	
XA222	TQ2	C1	18A		JAEN3A	00	=		
XA222	TQ2	C1	19A		(38)	01		JAENOR JXCP1B 40 19A 42 20A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERMINAL	DESIGNATOR	FACTOR	COMMENT
XA220	TT3	D2	23B	JAEN3R	00	=		
XA220	TT3	D2	22B	(43)	01	=	JAEN3S JAEN7A JXRS0B 41 22B 46 21A 48 22A	
XA221	TQ2	D2	21A	JAEN3S	00	=		OUTPUT COUNTER BIT3
XA221	TQ2	D2	22A	(46)	01	=	JAEN3R JAEN6A 48 22A 50 23A	
XA222	TQ2	C2	15A	JAEN4A	00	=		
XA222	TQ2	C2	16A	(30)	01	=	JAEN0S JXCP1B 34 16A 36 17A	
XA222	TQ2	E2	28A	JAEN5A	00	=		
XA222	TQ2	E2	29A	(60)	01	=	JAEN3S JXCP3B 62 29A 64 30A	
XA222	TQ2	C3	16B	JAEN6A	00	=		
XA222	TQ2	C3	14B	(33)	01	=	JAEN2S JXCP1B 29 14B 31 15B	
XA222	TQ2	C4	19B	JAEN7A	00	=		
XA222	TQ2	C4	17B	(39)	01	=	JAEN2R JXCP1B 35 17B 37 18B	
XA220	TT3	C1	17A	JAE0BR	00	=		
XA220	TT3	C1	18A	(36)	01	=	JAE0BS JAEBRA JXRS0B 38 18A 40 19A 42 20A	
XA221	TQ2	C1	18A	JAE0BS	00	=		EOB RECEIVED ON OUTPUT
XA221	TQ2	C1	19A	(38)	01	=	JAE0BR JAE0A 40 19A 42 20A	
XA225	TD4	E2	30B	JAIENR	00	=		
XA225	TD4	E2	29B	(57)	01	=	JAIENS JAIERA JETX1A JXRS0B 55 29B 60 28A 62 29A 64 30A	
XA223	TQ2	E3	30B	JAIENS	00	=		AUTO INPUT MODE ENABLE F/F
XA223	TQ2	E3	28B	(57)	01	=	JAIENR JAINOA 53 28B 55 29B	
XA224	TT3	F1	36B	JAIERA	00	=		RESET AUTO INPUT MODE EN F/F
XA224	TT3	F1	37B	(73)	01	=	JDAINQ JXE81Q JXCP3B 75 37B 77 38B 79 39B	
XA225	TD4	F1	37A	JAINOA	00	=		START INPUT COMMAND
XA225	TD4	F1	37B	(76)	01	=	JDAINQ JSNC2S JBUSYS JXCP3B 75 37B 77 38B 78 38A 79 39B	



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	LOGIC	FACTOR	COMMENT
XA220	TT3	C2	15B	JAOENR	00	1		
XA220	TT3	C2	14B	(31)	01		JAOENS JAOERA JXRSOB 29 14B 30 15A 34 16A	
XA221	TQ2	C2	15A	JAOENS	00	=		AUTO OUTPUT MODE ENABLE F/F
XA221	TQ2	C2	16A	(30)	01		JAOENR JAENOA 34 16A 36 17A	
XA213	TS8	B1	11B	JAOERA	00	=		RESET AUTO OUTPUT
XA213	TS8	B1	09A	(23)	01		JAE0BS JK1290 JK08C0 JK04F0 JK03E0 JXCP3B SPI0022 SPI0012 14 09A 18 10A 19 09B 20 11A 22 12A 24 13A 25 12B 26 14A	
XA215	TD4	B2	10B	JBSY0A	00	=		SET HARDWARE BUSY F/F
XA215	TD4	B2	09A	(21)	01		JSNCIS SPI0022 JXCP1B SPI0012 14 09A 18 10A 19 09B 20 11A	
XA223	TQ2	D4	27B	JBUSYA	00	=		HARDWARE BUSY WHEN LOW
XA223	TQ2	D4	25B	(51)	01		JPBZY0 SPI0152 47 25B 49 26B	
XA224	TT3	B3	13B	JBUSYR	00	=		
XA224	TT3	B3	10B	(27)	01		JBUSYS JINT1A JXRSOB 21 10B 23 11B 25 12B	
XA223	TQ2	B3	10B	JBUSYS	00	=		HARDWARE BUSY F/F
XA223	TQ2	B3	08B	(21)	01		JBUSYR JBSY0A 17 08B 19 09B	
XA217	TDD	E1	19A	JCDER1	00	=		
XA217	TDD	E1		( )	01		SPI0012 40 19A	
XA217	TDD	EN	20A	JCDERN	00	=		
XA217	TDD	EN		( )	01		SPI0022 42 20A	
XA217	TDD	EP	17B	JCDERP	00	=		
XA217	TDD	EP	18A	(35)	01		JSRS0A 38 18A	
XA217	TDD	EQ	18B	JCDERQ	00	=		COMPUTER DATA PARITY ERROR F/F
XA217	TDD	EQ	19B	(37)	01		JCDESA 39 19B	
XA221	TQ2	F1	37B	JCDESA	00	=		
XA221	TQ2	F1	38B	(75)	01		JCDESO SPI0132 77 38B 79 39B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERMS	DESIGNATOR	FACTOR	COMMENT
XA222	TQ2	F2	34A	JCDESO	00	=		SET COMPUTER DATA PAR FR F/E
XA222	TQ2	F2	36A	(72)	01		JXDPEA JXODEA 71 36A 73 36B	
XA230	TDD	E1	19A	JDAINI	00	=	JDAINO 40 19A	
XA230	TDD	EN	20A	JDAINN	00	=	JXDV10 42 20A	
XA230	TDD	EP	17B	JDAINP	00	=		
XA230	TDD	EP	18A	(35)	01		JXRS0B 38 18A	
XA230	TDD	EQ	18B	JDAINQ	00	=		INPUT COMMAND F/E
XA230	TDD	EQ	19B	(37)	01		SPI0162 39 19B	
XA226	TQ2	B3	10B	JDAINO	00	=		
XA226	TQ2	B3	08B	(21)	01		JXRAF5T SPI0152 17 08B 19 09B	
XA226	TQ2	E2	28A	JDCP00	00	=		DATA REGISTER CLOCK PO 123
XA226	TQ2	E2	29A	(60)	01		JXED0A JXOD0A 62 29A 64 30A	
XA226	TQ2	E3	30B	JDCP10	00	=		DATA REGISTER CLOCK 4567
XA226	TQ2	E3	28B	(57)	01		JXED0A JXOD0A 53 28B 55 29B	
XA222	TQ2	D3	24B	JDEVIR	00	=		
XA222	TQ2	D3	22B	(45)	01		JDEVIS JXRS0B 41 22B 43 23B	
XA221	TQ2	D3	24B	JDEVIS	00	=		END INPUT COMND REQUEST INHBT
XA221	TQ2	D3	22B	(45)	01		JDEVIR JDEV0A 41 22B 43 23B	
XA219	TQ2	D4	27B	JDEV0A	00	=		
XA219	TQ2	D4	25B	(51)	01		JDEV1Q JXCP3B 47 25B 49 26B	
XA217	TDD	DI	10A	JDEV0I	00	=	JXGN2A 18 10A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERMINAL	DESIGNATOR	FACTOR	COMMENT
XA217	TDD	DN	09A	JDEVON ( )	00 01	=	JDEV0A 14 09A	
XA217	TDD	DP	10B	JDEV0P (21 )	00 01	=	JXRS0B 20 11A	
XA217	TDD	DQ	09B	JDEV0Q (19 )	00 01	=	JXDV3A 17 08B	END INPUT COUNTER BIT 0
XA221	TQ2	D4	27B	JDEV1A (51 )	00 01	=	JDEV3S JXCP3B 47 25B 49 26B	
XA218	TDD	DI	10A	JDEV1I ( )	00 01	=	JDEV0Q 18 10A	
XA218	TDD	DN	09A	JDEV1N ( )	00 01	=	JXCP1B 14 09A	
XA218	TDD	DP	10B	JDEV1P (21 )	00 01	=	JXRS0B 20 11A	
XA218	TDD	DQ	09B	JDEV1Q (19 )	00 01	=	SPT0022 17 08B	END INPUT COUNTER BIT 1
XA224	TT3	G3	19B	JDEV2A (39 )	00 01	=	JDEV2S JXXREP JXCP1B 33 16B 35 17B 37 18B	
XA220	TT3	D3	27B	JDEV2R (51 )	00 01	=	JDEV2S JDEV1A JXRS0B 45 24B 47 25B 49 26B	
XA222	TQ2	D1	24A	JDEV2S (52 )	00 01	=	JDEV2R JDEV0A 54 25A 56 26A	END INPUT COUNTER BIT 2
XA221	TQ2	C4	19B	JDEV3A (39 )	00 01	=	JDEV2R JXCP1B 35 17B 37 18B	

CONNECTOR	CROSS TYPE	CROSS GROUP	TEST POINTS AND OR	EQUATION	TERM DESIG- NATOR	FACTOR	COMMENT
XA220	TT3	E1	30A	JDEV3R	00 =		
XA220	TT3	E1	31A	(64)	01	JDEV3S JDEV3A JXRS0B 66 31A 68 32A 70 33A	
XA222	TQ2	D2	21A	JDEV3S	00 =		END INPUT COUNTER BIT 3
XA222	TQ2	D2	22A	(46)	01	JDEV3R JDEV2A 48 22A 50 23A	
XA214	TSB	B1	11B	JDP7RA	00 =		RESET DATA REG ON
XA214	TSB	B1	09A	(23)	01	JDAINQ JAIENS JKTCCP JK0870 JK0485U JK1200 JXCP3B SPI0012 14 09A 18 10A 19 09B 20 11A 22 12A 24 13A 25 12B 26 14A	INPUT COMND
XA214	TSB	C1	17B	JDP7SA	00 =		LOAD DATA REGISTER
XA214	TSB	C1	15A	(35)	01	JDAINQ JAIENS JEYINO JKTCCP JK0870 JK0485U JXCP3B SPI0012 30 15A 31 15B 34 16A 36 17A 37 18B 38 18A 40 19A 42 20A	
XA222	TQ2	B4	13B	JDP7S0	00 =		
XA222	TQ2	B4	11B	(27)	01	JDP7SA SPI0132 23 11B 25 12B	
XA227	TQ2	F1	37B	JDRS0A	00 =		
XA227	TQ2	F1	38B	(75)	01	JDRS00 SPI0162 77 38B 79 39B	
XA224	TT3	E3	33B	JDRS00	00 =		RESET DATA REGISTR
XA224	TT3	E3	30B	(63)	01	JDP7RA JXODRA JXRS0B 57 30B 59 31B 61 32B	
XA227	TQ2	F2	34A	JDRS1A	00 =		
XA227	TQ2	F2	36A	(72)	01	JDRS00 SPI0162 71 36A 73 36B	
XA230	TDD	KI	29A	JDOPB I	00 =		
XA230	TDD	KI	29A	( )	01	JXRPCS 62 29A	
XA230	TDD	KN	28A	JDOPB N	00 =		
XA230	TDD	KN	28A	( )	01	JDCP00 60 28A	
XA230	TDD	KP	30B	JDOPB P	00 =		
XA230	TDD	KP	30A	(57)	01	JDRS0A 64 30A	
XA230	TDD	KQ	29B	JDOPB Q	00 =		DATA REGISTER BIT P
XA230	TDD	KQ	28B	(55)	01	JDOPSA 53 28B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA221	TQ2	B4	13B	JDCPSA	00	H		
XA221	TQ2	B4	11B	( 27 )	01		JDP750 JKI280 23 11B 25 12B	DATA REGISTER SET BIT P
XA228	TDD	FI	16A	JDOUTI	00	=	JDOUTO 34 16A	
XA228	TDD	FN	15A	JDOUTN	00	=	JXOV10 30 15A	
XA228	TDD	FP	16B	JDOUTP	00	=		
XA228	TDD	FP	17A	( 33 )	01		JXRS0B 36 17A	
XA228	TDD	FQ	15B	JDOUTC	00	=		OUTPUT COMMAND E/E
XA228	TDD	FQ	14B	( 31 )	01		SPI0142 29 14B	
XA226	TQ2	B4	13B	JDOUTO	00	=		
XA226	TQ2	B4	11B	( 27 )	01		JXRAF7T SPI0152 23 11B 25 12B	
XA230	TDD	LI	38B	JD00BI	00	=	JXROCS 77 38B	
XA230	TDD	LN	39B	JD00BN	00	=	JDCP00 79 39B	
XA230	TDD	LP	37A	JD00BP	00	=		
XA230	TDD	LP	37B	( 76 )	01		JDRSOA 75 37B	
XA230	TDD	LQ	38A	JD00BQ	00	=		DATA REGISTER BIT 0
XA230	TDD	LQ	39A	( 78 )	01		SPI0162 80 39A	
XA230	TDD	MI	36A	JD01BI	00	=	JXRICS 71 36A	
XA230	TDD	MN	34A	JD01BN	00	=	JDCP00 72 34A	

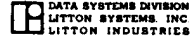
CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATION	FACTOR	COMMENT
XA230	TDD	MP	35A	JD018P	00	=		
XA230	TDD	MP	36B	(69)	01		JDRS0A 73 36B	
XA230	TDD	MQ	35B	JD018Q	00	=		
XA230	TDD	MQ	34B	(74)	01		JD01SA 65 34B	
XA221	TQ2	A1	05A	JD01SA	00	=		
XA221	TQ2	A1	06A	(06)	01		JDP7S0 JK1270 08 06A 10 07A	
				JD02B1	00	=		
XA229	TDD	KI	29A	( )	01		JXR2CS 62 29A	
				JD02BN	00	=		
XA229	TDD	KN	28A	( )	01		JDCP00 60 28A	
XA229	TDD	KP	30B	JD028P	00	=		
XA229	TDD	KP	30A	(57)	01		JDRS0A 64 30A	
XA229	TDD	KQ	29B	JD028Q	00	=		
XA229	TDD	KQ	28B	(55)	01		JD02SA 53 28B	
XA221	TQ2	A2	02B	JD02SA	00	=		
XA221	TQ2	A2	04A	(01)	01		JDP7S0 JK1260 04 04A 05 03B	
				JD03BI	00	=		
XA229	TDD	LI	38B	( )	01		JXR3CS 77 38B	
				JD03BN	00	=		
XA229	TDD	LN	39B	( )	01		JDCP00 79 39B	
XA229	TDD	LP	37A	JD038P	00	=		
XA229	TDD	LP	37B	(76)	01		JDRS0A 75 37B	
XA229	TDD	LQ	38A	JD038Q	00	=		
XA229	TDD	LQ	39A	(78)	01		JD03SA 80 39A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA221	TQ2	A3	04B	JD03SA	00	=		
XA221	TQ2	A3	02A	(09)	01		JDP7S0 JK1250 03 02A 07 03A	
				JD04BI	00	=		
XA229	TDD	MI	36A	( )	01		JXR4CS 71 36A	
				JD04BN	00	=		
XA229	TDD	MN	34A	( )	01		JDCP10 72 34A	
XA229	TDD	MP	35A	JD04BP	00	=		
XA229	TDD	MP	36B	(69)	01		JDRS1A 73 36B	
XA229	TDD	MQ	35B	JD04BQ	00	=		
XA229	TDD	MQ	34B	(74)	01		JD04SA 65 34B	
XA221	TQ2	A4	07B	JD04SA	00	=		
XA221	TQ2	A4	05B	(15)	01		JDP7S0 JK1240 11 05B 13 06B	
				JD05BI	00	=		
XA228	TDD	KI	29A	( )	01		JXR5CS 62 29A	
				JD05BN	00	=		
XA228	TDD	KN	28A	( )	01		JDCP10 60 28A	
XA228	TDD	KP	30B	JD05BP	00	=		
XA228	TDD	KP	30A	(57)	01		JDKS1A 64 30A	
XA228	TDD	KQ	29B	JD05BQ	00	=		
XA228	TDD	KQ	28B	(55)	01		JD05SA 53 28B	
XA221	TQ2	B1	12A	JD05SA	00	=		
XA221	TQ2	B1	13A	(22)	01		JDP7S0 JK1230 24 13A 26 14A	
				JD06BI	00	=		
XA228	TDD	LI	38B	( )	01		JXR6CS 77 38B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
			AND	OR					
					JD06BN	00	=		
XA228	TDD	LN	39B		( )	01		JDCP10 79 39B	
XA228	TDD	LP	37A			00	=		
XA228	TDD	LP	37B		(76 )	01		JDRS1A 75 37B	
XA228	TDD	LQ	38A			00	=		
XA228	TDD	LQ	39A		(78 )	01		JD06SA 80 39A	
XA221	TQ2	B2	09A			00	=		
XA221	TQ2	B2	10A		(14 )	01		JDP750 JK1220 18 10A 20 11A	
					JD07BI	00	=		
XA228	TDD	HI	36A		( )	01		JXR7C5 71 36A	
					JD07BN	00	=		
XA228	TDD	MN	34A		( )	01		JDCP10 72 34A	
XA228	TDD	MP	35A			00	=		
XA228	TDD	MP	36B		(69 )	01		JDRS1A 73 36B	
XA228	TDD	MQ	35B			00	=		
XA228	TDD	MQ	34B		(74 )	01		JD07SA 65 34B	DATA REGISTER BIT 7
XA221	TQ2	B3	10B			00	=		
XA221	TQ2	B3	08B		(21 )	01		JDP750 JK1210 17 08B 19 09B	DATA REGISTER SET BIT 7
XA239	TQ2	B2	09A		JENIN0	00	=		
XA239	TQ2	B2	10A		(14 )	01		JXRAF6T SPI0202 18 10A 20 11A	
XA214	TS8	D1	25B		JETX0A	00	=		
XA214	TS8	D1	23B		(47 )	01		JD0PBQ JD01BP JD02BP JD03BP JD04BP JD05BP JD06BQ JD07BQ 43 23B 46 21A 48 22A 49 26B 50 23A 52 24A 54 25A 56 26A	EXT CHARACTER IS
XA219	TQ2	E1	31A		JETX00	00	=		
XA219	TQ2	E1	32A		(66 )	01		JETX0A SPI0132 68 32A 70 33A	



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LOGIC

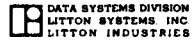
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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA220	TT3	E2	29B	JETX1A	00	=		EXT CHARACTER STROBE
XA220	TT3	E2	28B	(55)	01		J1ENIS JETX00 JXCP3B 53 28B 60 28A 62 29A	
XA243	DCF	D5	38B	JEYINDX	00	=		KEYBOARD INPUT DATA RECEIVER
XA243	DCF	D5	36A	(80)	01		JXGNIA 72 36A	
XA238	TQ2	B3	10B	JEYINO	00	=		
XA238	TQ2	B3	08B	(21)	01		JEYINOX SPI0182 17 08B 19 09B	
XA243	DCF	D6	38A	JEYINOX	00	=		
XA243	DCF	D6	37A	(76)	01		JXGNZA 74 37A	
XA223	TQ2	E4	33B	J1ENRA	00	=		RESET INPUT DATA DETECT CONTR
XA223	TQ2	E4	31B	(63)	01		J1ENRO SPI0152 59 31B 61 32B	
XA222	TQ2	E3	30B	J1ENRO	00	=		
XA222	TQ2	E3	28B	(57)	01		J1E1NS JEYINOX 53 28B 55 29B	
XA224	TT3	E1	30A	J1ENSA	00	=		START MAIN TIME COUNT ON INPT
XA224	TT3	E1	31A	(64)	01		J1E1NS J1EN0Q J1EN1P 66 31A 68 32A 70 33A	
XA214	TS8	F1	37A	J1EN0A	00	=		START INPUT COUNT
XA214	TS8	F1	36A	(76)	01		J0A1NQ J1E1NS JK1290 JK08B3U JXCP3B SPI0022 SPI0012 SPI0032 71 36A 72 34A 73 36B 74 35B 75 37B 77 38B 78 38A 79 39B	
XA217	TDD	KI	29A	J1EN0I	00	=		
XA217	TDD	KI		( )	01		SPI0012 62 29A	
XA217	TDD	KN	28A	J1EN0N	00	=		
XA217	TDD	KN		( )	01		J16M10 60 28A	
XA217	TDD	KP	30B	J1EN0P	00	=		
XA217	TDD	KP	30A	(57)	01		J1ENRA 64 30A	
XA217	TDD	KQ	29B	J1EN0Q	00	=		DATA DETECT COUNTER BIT 0
XA217	TDD	KQ	28B	(55)	01		SPI0022 53 28B	

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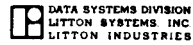
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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA225	TD4	D1	25B	JIENOR	00	=		
XA225	TD4	D1	26B	(47)	01		JIENOS JIEN1A JAIENS JXRSOB 49 26B 52 24A 54 25A 56 26A	
XA223	TQ2	D1	24A	JIENOS	00	=		INPUT COUNTER BIT 0
XA223	TQ2	D1	25A	(52)	01		JIENOR JIENOA 54 25A 56 26A	
XA222	TQ2	D4	27B	JIENIA	00	=		
XA222	TQ2	D4	25B	(51)	01		JIENIS JXCP3B 47 25B 49 26B	
				JIEN1I	00	=		
XA218	TDD	KI	29A	( )	01		JIENIQ 62 29A	
				JIEN1N	00	=		
XA218	TDD	KN	28A	( )	01		J16M10 60 28A	
XA218	TDD	KP	30B	JIEN1P	00	=		
XA218	TDD	KP	30A	(57)	01		JIENRA 64 30A	
XA218	TDD	KQ	29B	JIEN1Q	00	=		DATA DETECT COUNTER BIT 1
XA218	TDD	KQ	28B	(55)	01		SPI0012 53 28B	
XA224	TT3	B1	11A	JIEN1R	00	=		
XA224	TT3	B1	12A	(20)	01		JIENIS JIEN3A JXRSOB 22 12A 24 13A 26 14A	
XA223	TQ2	B1	12A	JIENIS	00	=		INPUT COUNTER BIT 1
XA223	TQ2	B1	13A	(22)	01		JIEN1R JIEN2A 24 13A 26 14A	
XA222	TQ2	A3	04B	JIEN2A	00	=		
XA222	TQ2	A3	02A	(09)	01		JIENOS JXCP1B 03 02A 07 03A	
XA221	TQ2	E4	33B	JIEN3A	00	=		
XA221	TQ2	E4	31B	(63)	01		JIENOR JXCP1B 59 31B 61 32B	
XA222	TQ2	F1	37B	JINT1A	00	=		
XA222	TQ2	F1	38B	(75)	01		JINT10 SPI0132 77 38B 79 39B	

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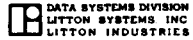
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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA213	TSB	F1	37A	JINT10	00	=		
XA213	TSB	F1	36A	(76)	01	=	JSC11A JETX1A JAIERA JPINTA SPI0012 SPI0022 SPI0032 SPI0042 71 36A 72 34A 73 36B 74 35B 75 37B 77 38B 78 38A 79 39B	END OF DEV COMMAND
XA222	TQ2	B2	09A	JINT2A	00	=		
XA222	TQ2	B2	10A	(14)	01	=	JINT10 SPI0132 1B< 10A 20 11A	
XA231	TSB	F1	37A	J1OTRA	00	=		
XA231	TSB	F1	36A	(76)	01	=	JAIENS JK1290 JK08C0 JK04F0 JK03E0 JXCP3B SPI0162 SPI0142 71 36A 72 34A 73 36B 74 35B 75 37B 77 38B 78 38A 79 39B	
XA224	TT3	E2	29B	J1OUTR	00	=		
XA224	TT3	E2	28B	(55)	01	=	J1OUTS JXEAOA JXRSOB 53 28B 60 28A 62 29A	
XA223	TQ2	E2	28A	J1OUTS	00	=		
XA223	TQ2	E2	29A	(60)	01	=	J1OUTR J1EN1A 62 29A 64 30A	PRINT INHIBIT F/E
XA235	TQ2	F2	34A	JKPE0A	00	=		
XA235	TQ2	F2	36A	(72)	01	=	JK08C0 JK04B5U 71 36A 73 36B	DIVIDE BY 13 PARALLEL ENTRY
XA222	TQ2	E1	31A	JKPE00	00	=		
XA222	TQ2	E1	32A	(66)	01	=	JKPE0A SPI0132 68 32A 70 33A	
XA221	TQ2	E1	31A	JKPE1A	00	=		
XA221	TQ2	E1	32A	(66)	01	=	JK1290 JKPE00 68 32A 70 33A	DIVIDE BY 10 PARRALLEL ENTRY
XA217	TDD	CI	13A	JKTCCI	00	=		
XA217	TDD	CI	13A	( )	01	=	SPI0012 24 13A	
XA217	TDD	CN	14A	JKTCCN	00	=		
XA217	TDD	CN	14A	( )	01	=	JK15QA 26 14A	
XA217	TDD	CP	11B	JKTCCP	00	=		
XA217	TDD	CP	12A	(23)	01	=	JKYCRA 22 12A	
XA217	TDD	CQ	12B	JKTCCQ	00	=		
XA217	TDD	CQ	13B	(25)	01	=	JKTC1A 27 13B	MAIN TIMING COUNTER CONTRL F/E

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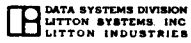
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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM NO	DESIGNATOR	FACTOR	COMMENT
XA227	TQ2	F4	39A	JKTCRA	00	=		START MAIN TIMING COUNTER
XA227	TQ2	F4	37A	(80)	01		JKTCRO SPI0162 76 37A 78 38A	
XA232	TT3	F1	36B	JKTCRO	00	=		
XA232	TT3	F1	37B	(73)	01		JAEN5A JIENSA SPI0172 75 37B 77 38B 79 39B	
XA219	TQ2	A4	07B	JKTC1A	00	=		
XA219	TQ2	A4	05B	(15)	01		JKTC10 SPI0022 11 05B 13 06B	
XA220	TT3	B3	13B	JKTC10	00	=		
XA220	TT3	B3	10B	(27)	01		JIEN0A JXRS0B SPI0022 21 10B 23 11B 25 12B	
XA228	TDD	HI	22A	JK00BI	00	=		
				( )	01		JK0090 48 22A	
XA228	TDD	HN	21A	JK00BN	00	=		
				( )	01		JXCP1B 46 21A	
XA228	TDD	HP	24B	JK00BP	00	=		
XA228	TDD	HP	23A	(45)	01		JKTCCP 50 23A	
XA228	TDD	HQ	23B	JK00BQ	00	=		
XA228	TDD	HQ	22B	(43)	01		SPI0052 41 22B	
XA235	TQ2	F4	39A	JK0090	00	=		
XA235	TQ2	F4	37A	(80)	01		JK009A1 JK00BQ 76 37A 78 38A	
XA233	DBC	C1	18A	JK03B1U	00	=		
XA233	DBC	C1	18B	(38)	01		SPI0142 SPI0182 SPI0052 SPI0042 SPI0172 39 18B 41 19B 43 22B 45 23B 50 24A	
XA233	DBC	C2	19A	JK03B2U	00	=		
XA233	DBC	C2	17B	(40)	01		JXCP1B 37 17B	
XA233	DBC	C3	20A	JK03B3U	00	=		
XA233	DBC	C3	23A	(42)	01		SPI0202 47 23A	

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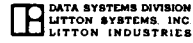
LOGIC

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA233	DBC	C4	21A	JK0384U	00	=		
XA233	DBC	C4	22A	(46)	01		JK008Q 48 22A	
XA233	DBC	C5	17A	JK0385U	00	=		
XA233	DBC	C5	16B	(36)	01		JKTCCP 35 16B	
XA219	TQ2	A1	05A	JK0300	00	=		STATE D OF KK03 BITS
XA219	TQ2	A1	06A	(06)	01		JK0305T SPI0012 08 06A 10 07A	
XA219	TQ2	A2	02B	JK03E0	00	=		STATE E OF KK03 BITS
XA219	TQ2	A2	04A	(01)	01		JK0306T SPI0012 04 04A 05 03B	
XA219	TQ2	A3	04B	JK03F0	00	=		STATE E OF KK03 BITS
XA219	TQ2	A3	02A	(09)	01		JK0307T SPI0012 03 02A 07 03A	
XA222	TQ2	E4	33B	JK03QA	00	=		
XA222	TQ2	E4	31B	(63)	01		JK03B4U SPI0132 59 31B 61 32B	
XA234	DBC	B1	12A	JK0300T	00	=		KK03 BITS STATE DECODER
XA234	DBC	B1	10A	(24)	01		JK03B1U 20 10A	
XA234	DBC	B2	13A	JK0301T	00	=		
XA234	DBC	B2	11A	(26)	01		JK03B2U 22 11A	
XA234	DBC	B3	14A	JK0302T	00	=		
XA234	DBC	B3	09B	(27)	01		JK03B3U 19 09B	
XA234	DBC	B4	15A	JK0303T	00	=		
XA234	DBC	B4	10B	(30)	01		JK03QA 21 10B	
XA234	DBC	B5	16A	JK0304T	00	=		
XA234	DBC	B5		(33)	01		SPA 4T	
XA234	DBC	B6	11B	JK0305T	00	=		
XA234	DBC	B6		(23)	01		SPA 5T	

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149015-860  
CARD CAGE ASSEMBLY IFCU

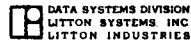
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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA234	DBC	B7	12B	JK0306T	00	=		
XA234	DBC	B7		(25)	01		SPA 6T	
XA234	DBC	B8	13B	JK0307T	00	=		
XA234	DBC	B8		(29)	01		SPA 7T	
XA234	DBC	B9	14B	JK0308T	00	=		
XA234	DBC	B9		(31)	01		SPA 8T	
XA234	DBC	B0	15B	JK0309T	00	=		
XA234	DBC	B0		(34)	01		SPA 9T	
XA234	DBC	C1	18A	JK04B1U	00	=		
XA234	DBC	C1	18B	(38)	01		SPI0182 SPI0052 SPI0042 SPI0142 SPI0172 39 18B 41 19B 43 22B 45 23B 50 24A	BITS 4-5-6-7 OF MAIN TIMER
XA234	DBC	C2	19A	JK04B2U	00	=		
XA234	DBC	C2	17B	(40)	01		JXCP1B 37 17B	
XA234	DBC	C3	20A	JK04B3U	00	=		
XA234	DBC	C3	23A	(42)	01		SPI0192 47 23A	
XA234	DBC	C4	21A	JK04B4U	00	=		
XA234	DBC	C4	22A	(46)	01		JK03B5U 48 22A	
XA234	DBC	C5	17A	JK04B5U	00	=		
XA234	DBC	C5	16B	(36)	01		JKTCCP 35 16B	
XA215	TD4	A2	04B	JK04FA	00	=		
XA215	TD4	A2	02B	(09)	01		JK04B4U JK04B3U JK04B2U JK04B1U 01 02B 04 04A 05 03B 07 03A	KK04 STATE F
XA223	TQ2	A4	07B	JK04F0	00	=		
XA223	TQ2	A4	05B	(15)	01		JK04FA SPI0152 11 05B 13 06B	
XA233	DBC	D1	26A	JK08B1U	00	=		
XA233	DBC	D1	26B	(54)	01		JXGNIA JXGN2A JXGN3A JXGN4A JKPE0A 53 26B 55 27B 57 29B 59 30B 63 31A	BITS 8-9-10-11 OF MAIN TIMER

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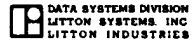
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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	RESISTOR	FACTOR	COMMENT
XA233	DBC	D2	28B	JK08B2U	00	=		
XA233	DBC	D2	25B	(56)	01		JXCP1B 51 25B	
XA233	DBC	D3	28A	JK08B3U	00	=		
XA233	DBC	D3	31B	(60)	01		SPI0182 61 31B	
XA233	DBC	D4	29A	JK08B4U	00	=		
XA233	DBC	D4	30A	(62)	01		JK04B5U 64 30A	
XA233	DBC	D5	25A	JK08B5U	00	=		
XA233	DBC	D5	24B	(52)	01		JKYCCP 49 24B	
XA215	TD4	A1	05B	JK08CA	00	=		KK08 STATE C
XA215	TD4	A1	05A	(11)	01		JK08B4U JK08B3U JK09QA JK08QA 06 05A 08 06A 10 07A 13 06B	
XA222	TQ2	A2	02B	JK08C0	00	=		
XA222	TQ2	A2	04A	(01)	01		JK08CA SPI0132 04 04A 05 03B	
XA219	TQ2	E2	28A	JK08QA	00	=		
XA219	TQ2	E2	29A	(60)	01		JK08B1U SPI0132 62 29A 64 30A	
XA224	TT3	D1	23A	JK087A	00	=		KK08 BITS STATE 7
XA224	TT3	D1	24A	(50)	01		JK08B3U JK08B2U JK08B1U 52 24A 54 25A 56 26A	
XA223	TQ2	E1	31A	JK0870	00	=		
XA223	TQ2	E1	32A	(66)	01		JK087A SPI0152 68 32A 70 33A	
XA219	TQ2	E3	30B	JK09QA	00	=		
XA219	TQ2	E3	28B	(57)	01		JK08B2U SPI0132 53 28B 55 29B	
XA234	DBC	D1	26A	JK12B1U	00	=		BITS 12-13-14-15 OF MAIN TIME
XA234	DBC	D1	26B	(54)	01		JXGN1A JXGN2A JXGN3A JXGN4A JKPE1A 53 26B 55 27B 57 29B 59 30B 63 31A	
XA234	DBC	D2	28B	JK12B2U	00	=		
XA234	DBC	D2	25B	(56)	01		JXCP1B 51 25B	

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
			AND	OR					
XA234	DBC	D3	28A		JK1283U	00	=		
XA234	DBC	D3	31B		(60)	01		SPI0182 61 31B	
XA234	DBC	D4	29A		JK1284U	00	=		
XA234	DBC	D4	30A		(62)	01		JKPE00 64 30A	
XA234	DBC	D5	25A		JK1285U	00	=		
XA234	DBC	D5	24B		(52)	01		JKTCCP 49 24B	
XA219	TQ2	B1	12A		JK1200	00	=		KK12 STATE 0
XA219	TQ2	B1	13A		(22)	01		JK1200T SPI0012 24 13A 26 14A	
XA233	DBC	A1	04A		JK1200T	00	=		KK12 BITS STATE DECODER
XA233	DBC	A1	02A		(08)	01		JK1281U 04 02A	
XA233	DBC	A2	05A		JK1201T	00	=		
XA233	DBC	A2	03A		(10)	01		JK1282U 06 03A	
XA233	DBC	A3	06A		JK1202T	00	=		
XA233	DBC	A3	02B		(14)	01		JK1283U 03 02B	
XA233	DBC	A4	07A		JK1203T	00	=		
XA233	DBC	A4	03B		(13)	01		JK1284U 05 03B	
XA233	DBC	A5	08B		JK1204T	00	=		
XA233	DBC	A5			(17)	01		SPA 4T	
XA233	DBC	A6	04B		JK1205T	00	=		
XA233	DBC	A6			(07)	01		SPA 5T	
XA233	DBC	A7	05B		JK1206T	00	=		
XA233	DBC	A7			(09)	01		SPA 6T	
XA233	DBC	A8	06B		JK1207T	00	=		
XA233	DBC	A8			(11)	01		SPA 7T	



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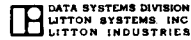
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CONNECTOR	COLU M	GROUP	TEST POINTS AND OR	EQUATION	TERM	LOGIC FUNCTION	FACTOR	COMMENT
XA233	DBC	A9	07B	JK1208T	00	=		
XA233	DBC	A9		(15)	01		SPA 8T	
XA233	DBC	A0	09A	JK1209T	00	=		
XA233	DBC	A0		(18)	01		SPA 9T	
XA219	TQ2	B2	09A	JK1210	00	=		KK12 STATE 1
XA219	TQ2	B2	10A	(14)	01		JK1201T SPI0132 18 10A 20 11A	
XA219	TQ2	B3	10B	JK1220	00	=		KK12 STATE 2
XA219	TQ2	B3	08B	(21)	01		JK1202T SPI0132 17 08B 19 09B	
XA219	TQ2	B4	13B	JK1230	00	=		KK12 STATE 3
XA219	TQ2	B4	11B	(27)	01		JK1203T SPI0132 23 11B 25 12B	
XA219	TQ2	C1	18A	JK1240	00	=		KK12 STATE 4
XA219	TQ2	C1	19A	(38)	01		JK1204T SPI0132 40 19A 42 20A	
XA219	TQ2	C2	15A	JK1250	00	=		KK12 STATE 5
XA219	TQ2	C2	16A	(30)	01		JK1205T SPI0132 34 16A 36 17A	
XA219	TQ2	C3	16B	JK1260	00	=		KK12 STATE 6
XA219	TQ2	C3	14B	(33)	01		JK1206T SPI0132 29 14B 31 15B	
XA219	TQ2	C4	19B	JK1270	00	=		KK12 STATE 7
XA219	TQ2	C4	17B	(39)	01		JK1207T SPI0132 35 17B 37 18B	
XA219	TQ2	D1	24A	JK1280	00	=		KK12 STATE 8
XA219	TQ2	D1	25A	(52)	01		JK1208T SPI0132 54 25A 56 26A	
XA219	TQ2	D2	21A	JK1290	00	=		KK12 STATE 9
XA219	TQ2	D2	22A	(46)	01		JK1209T SPI0132 48 22A 50 23A	
XA219	TQ2	D3	24B	JK15QA	00	=		
XA219	TQ2	D3	22B	(45)	01		JK12B4U SPI0132 41 22B 43 23B	

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
			AND	OR					
XA224	TT3	A2	03A		JLPTBR	00	=		
XA224	TT3	A2	02B		(07)	01		JLPTBS JXXDDP JXRSOB 01 02B 03 02A 05 03B	
XA223	TQ2	A2	02B		JLPTBS	00	=		LOOP TEST BUSY F/E
XA223	TQ2	A2	04A		(01)	01		JLPTBR JXOD0A 04 04A 05 03B	
XA217	TDD	BI	03B		JLPTOI	00	=		
XA217	TDD	BN	02B		( )	01		JXGN2A 05 03B	
XA217	TDD	BP	04B		JLPTON	00	=		
XA217	TDD	BP	04A		( )	01		JLPTIA 01 02B	
XA217	TDD	BQ	03A		JLPTOP	00	=		
XA217	TDD	BQ	02A		(09)	01		JXRSOB 04 04A	
XA217	TDD	BQ	03A		JLPTOQ	00	=		LOOP TEST COUNTER BIT 0
XA217	TDD	BQ	02A		(07)	01		JXOD0A 03 02A	
XA222	TQ2	A1	05A		JLPT1A	00	=		
XA222	TQ2	A1	06A		(06)	01		JLPT1Q JXCP3B 08 06A 10 07A	
XA218	TDD	BI	03B		JLPT1I	00	=		
XA218	TDD	BI	03B		( )	01		JLPTOQ 05 03B	
XA218	TDD	BN	02B		JLPT1N	00	=		
XA218	TDD	BN	02B		( )	01		JXCP1B 01 02B	
XA218	TDD	BP	04B		JLPT1P	00	=		
XA218	TDD	BP	04A		(09)	01		JXRSOB 04 04A	
XA218	TDD	BQ	03A		JLPT1Q	00	=		LOOP TEST COUNTER BIT 1
XA218	TDD	BQ	02A		(07)	01		SP10052 03 02A	
XA224	TT3	A1	04A		JLPT2R	00	=		
XA224	TT3	A1	05A		(04)	01		JLPT2S JXXDDP JXRSOB 06 05A 08 06A 10 07A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA223	TQ2	A1	05A	JLPT2S	00	=		LOOP TEST DATA WAIT FOR ENABLE
XA223	TQ2	A1	06A	(06)	01		JLPT2R JLPT1A 08 06A 10 07A	
XA227	TQ2	F3	35A	JLTE0A	00	=		LOOP TEST START INPUT DATA
XA227	TQ2	F3	34B	(69)	01		JLPT2S JXEA00 65 34B 74 35B	
XA213	TS8	E1	31B	JNULLA	00	=		
XA213	TS8	E1	29B	(59)	01		JD008P JD018P JD028P JD038P JD048P JD058P JD068P JD078P 55 29B 60 28A 61 32B 62 29A 64 30A 66 31A 68 32A 70 33A	
XA225	TD4	B2	10B	JPBZYG	00	=		TTY HARDWARE BUSY WHEN HI
XA225	TD4	B2	09A	(21)	01		JSNCOR JINT2A JXXDIR JLPTBR 14 09A 18 10A 19 09B 20 11A	
XA216	MUX	C1	17A	JPDTX1X	00	=		PARALLEL TO SERIALMULTIPLXER
XA216	MUX	C1	16B	(36)	01		SPI0012 JD078P JD068P JD058P JD048P JD038P JD028P JD018P 35 16B 37 17B 39 18B 41 19B 43 22B 45 23B 47 23A 50 24A	
XA216	MUX	C2	18A	JPDTX2X	00	=		
XA216	MUX	C2	21A	(38)	01		JK1281U JK1282U JK1283U JK1284U 46 21A 42 20A 40 19A 48 22A	
XA224	TT3	B2	09B	JPDT8A	00	=		
XA224	TT3	B2	09A	(19)	01		JPOUT0 JD0P8P JK1280 14 09A 17 08B 18 10A	
XA222	TQ2	B3	10B	JPDT9A	00	=		
XA222	TQ2	B3	08B	(21)	01		JPOUT0 JPDTX1X 17 08B 19 09B	
XA220	TT3	B2	09B	JPDT9C	00	=		SERIAL OUTPUT DATATO TTY
XA220	TT3	B2	09A	(19)	01		JPDT9A JPDT8A JOUTOA 14 09A 17 08B 18 10A	
XA218	TDD	FI	16A	JPETXI	00	=		
XA218	TDD	FI		( )	01		SPI0032 34 16A	
XA218	TDD	FN	15A	JPETXN	00	=		
XA218	TDD	FN		( )	01		SPI0042 30 15A	
XA218	TDD	FP	16B	JPETXP	00	=		
XA218	TDD	FP	17A	(33)	01		JSR50A 36 17A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	FERN	DESIGNATOR	FACTOR	COMMENT
XA218	TDD	FQ	15B	JPETXQ	00	=		
XA218	TDD	FQ	14B	( 31 )	01		JETX1A 29 14B	EXT ON INPUT COMMAND F/F
XA218	TDD	EI	19A	JPE0BI	00	=	SPI0012 40 19A	
XA218	TDD	EN	20A	JPE0BN	00	=	SPI0022 42 20A	
XA218	TDD	EP	17B	JPE0BP	00	=		
XA218	TDD	EP	18A	( 35 )	01		JSR50A 38 18A	
XA218	TDD	EQ	18B	JPE0B0	00	=		
XA218	TDD	EQ	19B	( 37 )	01		JATERA 39 19B	EOB ON INPUT COMMAND F/F
XA231	TS8	E1	31B	JPINTA	00	=		
XA231	TS8	E1	29B	( 59 )	01		JAE0BS JK1290 JK08C0 JK04F0 JK03D0 JXCP38 SPI0162 SPI0142 55 29B 60 28A 61 32B 62 29A 64 30A 66 31A 68 32A 70 33A	EOB INTERRUPT ON OUTPUT COMND
XA221	TQ2	F3	35A	JPRQPA	00	=		
XA221	TQ2	F3	34B	( 69 )	01		JDAINQ JXXREQ 65 34B 74 35B	
XA222	TQ2	F4	39A	JPRQPO	00	=		
XA222	TQ2	F4	37A	( 80 )	01		JPRQPA SPI0132 76 37A 78 38A	REQUEST PENDING CNINPUT
XA215	TD4	C1	17B	JPOUTA	00	=		
XA215	TD4	C1	18B	( 35 )	01		JDOUTQ JAOENS JKTCCP JNULLA 37 18B 38 18A 40 19A 42 20A	
XA243	DCF	D7	33A	JPOUTDX4	00	=		
XA243	DCF	D7	36A	( 61 )	01		JXGN1A 72 36A	OUTPUT DATA TO TTYDRIVER
XA218	TDD	CI	13A	JPOUTI	00	=		
XA218	TDD	CI	13A	( )	01		JPDT90 24 13A	
XA218	TDD	CN	14A	JPOUTN	00	=		
XA218	TDD	CN	14A	( )	01		JXCP3B 26 14A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERMINATION	DESIGNATION	FACTOR	COMMENT
			AND	OR					
XA218	TDD	CP	11B		JPOUTP	00 =			
XA218	TDD	CP	12A		(23)	01	JXRS0B 22 12A		
XA218	TDD	CQ	12B		JPOUTQ	00 =			
XA218	TDD	CQ	13B		(25)	01	SPI0012 27 13B		
XA223	TQ2	C3	16B		JPGUTO	00 =		OUTPUT TO TTY ENABLE CONTROL	
XA223	TQ2	C3	14B		(33)	01	JPOUTA SPI0132 29 14B 31 15B		
XA243	DCF	D8	34A		JPOUTOX	00 =			
XA243	DCF	D8	35A		(68)	01	JPOUTQ 70 35A		
XA220	TT3	F3	39A		JRTDEA	00 =		INPUT COMMAND START DATA INPUT	
XA220	TT3	F3	35A		(80)	01	JDAINQ JAENS JXEA00 69 35A 76 37A 78 38A		
XA232	TT3	F2	35B		JSCI1A	00 =		NOT BUSY COMMAND REJECT INTRP	
XA232	TT3	F2	34B		(74)	01	JSNC2S JBUSYR JXCP3B 65 34B 71 36A 72 34A		
XA214	TS8	E1	31B		JSNCOA	00 =			
XA214	TS8	E1	29B		(59)	01	JSNC0S JSNC2R JBUSYR JINT1A JXCP3B SPI0012 SPI0022 SPI0032 55 29B 60 28A 61 32B 62 29A 64 30A 66 31A 68 32A 70 33A		
XA224	TT3	A3	07B		JSNCOR	00 =			
XA224	TT3	A3	04B		(15)	01	JSNC0S JINT1A JXRS0B 09 04B 11 05B 13 06B		
XA223	TQ2	A3	04B		JSNCOS	00 =		DEVICE COMMAND SYNC BIT 0	
XA223	TQ2	A3	02A		(09)	01	JSNCOR JSYNIA 03 02A 07 03A		
XA222	TQ2	B1	12A		JSNCA1A	00 =			
XA222	TQ2	B1	13A		(22)	01	JSNC2S JXCP3B 24 13A 26 14A		
XA224	TT3	C1	17A		JSNCR1R	00 =			
XA224	TT3	C1	18A		(36)	01	JSNC1S JSNC1A JXRS0B 38 18A 40 19A 42 20A		
XA223	TQ2	C1	18A		JSNCR1S	00 =		DEVICE COMMAND SYNC BIT 1	
XA223	TQ2	C1	19A		(38)	01	JSNCR1R JSNCOA 40 19A 42 20A		

CONNECTOR	GROUP	TEST POINTS AND OR	EQUATION	TERMINATOR	DESIGNATOR	FACTOR	COMMENT
XA226	TQ2	D2 21A	JSNC2A	00 =			
XA226	TQ2	D2 22A	(46)	01		JSNC1S JXCP1B 48 22A 50 23A	
XA224	TT3	C2 15B	JSNC2R	00 =			
XA224	TT3	C2 14B	(31)	01		JSNC2S JSNC3A JXRS0B 29 14B 30 15A 34 16A	
XA223	TQ2	C2 15A	JSNC2S	00 =			DEVICE COMMAND SYNC BIT 2
XA223	TQ2	C2 16A	(30)	01		JSNC2R JSNC2A 34 16A 36 17A	
XA223	TQ2	B4 13B	JSNC3A	00 =			
XA223	TQ2	B4 11B	(27)	01		JSNC1R JXCP1B 23 11B 25 12B	
XA219	TQ2	F2 34A	JRS0A	00 =			
XA219	TQ2	F2 36A	(72)	01		JRS00 SPI0132 71 36A 73 36B	
XA220	TT3	F2 35B	JRS00	00 =			RESET ERROR/STATUS REGISTER
XA220	TT3	F2 34B	(74)	01		JSNC2A JXODRA JXRS0B 65 34B 71 36A 72 34A	
XA227	TQ2	B3 10B	JSYN1A	00 =			STAT NEW INPUT OR OUTPUT COM
XA227	TQ2	B3 08B	(21)	01		JSYN10 JXDV10 17 08B 19 09B	
XA226	TQ2	B2 09A	JSYN10	00 =			
XA226	TQ2	B2 10A	(14)	01		JXRAF5T JXRAF7T 18 10A 20 11A	
			JXACMB4	00 =			
XA240	TLD	F2 34A	( )	01		JXBCMD 72 34A	PORT A/B COMMAND LINE BUS
XA241	TLD	F2 34A	( )	02 +		JXACMD 72 34A	PORT A/B COMMAND LINE BUS
XA241	TLD	F2 34A	JXACMD	00 =			JXACMB4 BUSS
XA241	TLD	F2 36A	(72)	01		JXAIFO JXACMOX 71 36A 73 36B	
XA243	DCF	C1 25B	JXACMDX	00 =			TACMAB BUSS
XA243	DCF	C1 29A	(46)	01		JXA0EA 52 29A	
XA243	DCF	C2 26B	JXACMOX	00 =			
XA243	DCF	C2 27B	(47)	01		JXGN1A 49 27B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA240	TLD	F3	35A	JXAENB4 ( )	00 =		JXBEND 69 35A	PORT A/B ENABLE LINE BU S
XA241	TLD	F3	35A	( )	02 +		JXAEND 69 35A	
XA241	TLD	F3	35A	JXAEND ( )	00 =			
XA241	TLD	F3	34B	(69 )	01		JXAIF0 JXAENOX 65 34B 74 35B	
XA243	DCF	C3	30B	JXAENDX ( )	00 =			TAFNAB BUSS
XA243	DCF	C3	29A	(55 )	01		JXA0EA 52 29A	
XA243	DCF	C4	29B	JXAENOX ( )	00 =			
XA243	DCF	C4	28B	(56 )	01		JXGNIA 51 28B	
XA238	TQ2	A1	05A	JXAIEA ( )	00 =			
XA238	TQ2	A1	06A	(06 )	01		JXASLOX JXXORA 08 06A 10 07A	
XA239	TQ2	A2	02B	JXAIE0 ( )	00 =			PORT A DATA RECEIVE ENABLE
XA239	TQ2	A2	04A	(01 )	01		JXAIEA SPI0182 04 04A 05 03B	
XA239	TQ2	A3	04B	JXAIF0 ( )	00 =			
XA239	TQ2	A3	02A	(09 )	01		JXAIEA SPI0182 03 02A 07 03A	
XA243	DCF	C5	31B	JXAINDX ( )	00 =			TAINAB BUSS
XA243	DCF	C5	29A	(50 )	01		JXA0EA 52 29A	
XA243	DCF	C6	31A	JXAINOX ( )	00 =			
XA243	DCF	C6	30A	(57 )	01		JXDBI0 54 30A	
				JXAPCB4 ( )	00 =			
XA240	TLD	F1	37B	( )	01		JXBPCD 75 37B	PORT A/B PARITY LINE BU S
XA241	TLD	F1	37B	( )	02 +		JXAPCD 75 37B	PORT A/B PARITY LINE BU S
XA241	TLD	F1	37B	JXAPCD ( )	00 =			JXAPCB4 BUSS
XA241	TLD	F1	38B	(75 )	01		JXAIF0 JXAPCOX 77 38B 79 39B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TEST POINTS	DESIGNATION	FACTOR	COMMENT
XA243	DCF	C7	25A	JXAPCDX	00 =			
XA243	DCF	C7	29A	(43 )	01	JXA0EA 52 29A	TAOPAB BUSS	
XA243	DCF	C8	26A	JXAPCOX	00 =			
XA243	DCF	C8	28A	(48 )	01	JXDSBPR 50 28A		
XA237	TD4	E1	31B	JXARQA	00 =			
XA237	TD4	E1	32B	(59 )	01	JXASLOX JXINHR DEVINH JXXROO 61 32B 66 31A 68 32A 70 33A		
XA227	TQ2	E1	31A	JXARQO	00 =			PORT A REQUEST
XA227	TQ2	E1	32A	(66 )	01	JXARQA SPIO162 68 32A 70 33A		
XA232	TT3	B1	11A	JXARSA	00 =			PORT A I/O RESET
XA232	TT3	B1	12A	(20 )	01	JXACMOX JXAENOX JXASLOX 22 12A 24 13A 26 14A		
XA241	TLD	F4	39A	JXAR7D1	00 =			
XA241	TLD	F4	37A	(80 )	01	JXARQO SPIO211 76 37A 78 38A		
XA238	TQ2	B1	12A	JXASLA	00 =			PORT A SELECT
XA238	TQ2	B1	13A	(22 )	01	JXASLOX SPIO182 24 13A 26 14A		
XA242	DCF	D1	32B	JXASLDX	00 =			
XA242	DCF	D1	36A	(65 )	01	SPIO212 72 36A		
XA242	DCF	D2	33B	JXASLOX	00 =			
XA242	DCF	D2	34B	(69 )	01	SPIO192 71 34B		
XA243	DCF	D1	32B	JXASTDX4	00 =			
XA243	DCF	D1	36A	(65 )	01	JXGN1A 72 36A		
XA243	DCF	D2	33B	JXASTOX	00 =			
XA243	DCF	D2	34B	(69 )	01	JXASLOX 71 34B		
				JXAOCB4	00 =			
XA240	TLD	D1	24A	( )	01	JXBODD 52 24A	PORT A/B DATA LINE0 BUS	
XA241	TLD	D1	24A	( )	02 +	JXAODD 52 24A	PORT A/B DATA LINE0 BUS	



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA241	TLD	D1	24A	JXA0CD	00	=		JXA0CB4 BUSS
XA241	TLD	D1	25A	(52)	01		JXA1E0 JXA0COX 54 25A 56 26A	
XA243	DCF	A1	02B	JXA0CDX	00	=		TA00AB BUSS
XA243	DCF	A1	05A	(07)	01		JXA0EA 06 05A	
XA243	DCF	A2	03B	JXA0COX	00	=		
XA243	DCF	A2	04B	(09)	01		JXS031U 11 04B	
XA237	TD4	A1	05B	JXA0EA	00	=		PORT A DATA SEND ENABLE
XA237	TD4	A1	05A	(11)	01		JXASLOX JXINHR DEVINH JXXCS0 06 05A 08 06A 10 07A 13 06B	
				JXA1CB4	00	=		
XA240	TLD	D2	21A	( )	01		JXB1CD 46 21A	
XA241	TLD	D2	21A	( )	02	+	JXA1CD 46 21A	
				JXA1CD	00	=		
XA241	TLD	D2	22A	( )	01		JXA1E0 JXA1COX 48 22A 50 23A	
XA243	DCF	A3	07B	JXA1CDX	00	=		TA01AB BUSS
XA243	DCF	A3	05A	(17)	01		JXA0EA 06 05A	
XA243	DCF	A4	06B	JXA1COX	00	=		
XA243	DCF	A4	05B	(15)	01		JXS032U 13 05B	
				JXA2CB4	00	=		
XA240	TLD	D3	24B	( )	01		JXB2CD 45 24B	
XA241	TLD	D3	24B	( )	02	+	JXA2CD 45 24B	
XA241	TLD	D3	24B	JXA2CD	00	=		JXA2CB4 BUSS
				(45)				
XA241	TLD	D2	21A	JXA2CD	00	=		JXA1CB4 BUSS
XA241	TLD	D3	22B	(46)	01		JXA1E0 JXA2COX 41 22B 43 23B	

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERMIN	DESIGNATOR	FACTOR	COMMENT
XA243	DCF	A5	08B	JXA2CDX	00	=		
XA243	DCF	A5	05A	(14)	01		JXA0EA 06 05A	TA02AB BUSS
XA243	DCF	A6	07A	JXA2COX	00	=		
XA243	DCF	A6	06A	(10)	01		JXS033U 08 06A	
XA240	TLD	D4	27B	JXA3CB4	00	=		
XA241	TLD	D4	27B	( )	01		JXB3CD 51 27B	
XA241	TLD	D4	27B	( )	02	+	JXA3CD 51 27B	
XA241	TLD	D4	27B	JXA3CD	00	=		JXA3CB4 BUSS
XA241	TLD	D4	25B	(51)	01		JXA1E0 JXA3COX 47 25B 49 26B	
XA243	DCF	A7	02A	JXA3CDX	00	=		
XA243	DCF	A7	05A	(01)	01		JXA0EA 06 05A	TA03AB BUSS
XA243	DCF	A8	03A	JXA3COX	00	=		
XA243	DCF	A8	04A	(03)	01		JXS034U 04 04A	
XA240	TLD	E1	31A	JXA4CB4	00	=		
XA241	TLD	E1	31A	( )	01		JXB4CD 66 31A	
XA241	TLD	E1	31A	( )	02	+	JXA4CD 66 31A	
XA241	TLD	E1	31A	JXA4CD	00	=		JXA4CB4 BUSS
XA241	TLD	E1	32A	(66)	01		JXA1E0 JXA4COX 68 32A 70 33A	
XA243	DCF	B1	10B	JXA4CDX	00	=		
XA243	DCF	B1	13A	(27)	01		JXA0EA 36 13A	TA04AB BUSS
XA243	DCF	B2	11B	JXA4COX	00	=		
XA243	DCF	B2	12B	(29)	01		JXS471U 31 12B	
XA240	TLD	E2	28A	JXA5CB4	00	=		
XA241	TLD	E2	28A	( )	01		JXB5CD 60 28A	
XA241	TLD	E2	28A	( )	02	+	JXA5CD 60 28A	

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA241	TLD	E2	28A	JXA5CD	00	=		
XA241	TLD	E2	29A	(60)	01		JXA1E0 JXA5COX 62 29A 64 30A	JXA5CB4 BUSS
XA243	DCF	B3	15B	JXA5CDX	00	=		
XA243	DCF	B3	13A	(37)	01		JXA0EA 36 13A	TA05AB BUSS
XA243	DCF	B4	14B	JXA5COX	00	=		
XA243	DCF	B4	13B	(35)	01		JXS472U 33 13B	
				JXA6CB4	00	=		
XA240	TLD	E3	30B	( )	01		JXB6CD 57 30B	
XA241	TLD	E3	30B	( )	02	+	JXA6CD 57 30B	
XA241	TLD	E3	30B	JXA6CD	00	=		JXA6CB4 BUSS
XA241	TLD	E3	28B	(57)	01		JXA1E0 JXA6COX 53 28B 55 29B	
XA243	DCF	B5	16A	JXA6CDX	00	=		
XA243	DCF	B5	13A	(41)	01		JXA0EA 36 13A	TA06AB BUSS
XA243	DCF	B6	15A	JXA6COX	00	=		
XA243	DCF	B6	14A	(40)	01		JXS473U 38 14A	
				JXA7CB4	00	=		
XA240	TLD	E4	33B	( )	01		JXB7CD 63 33B	PORT A/B DATA LINE7 BUS
XA241	TLD	E4	33B	( )	02	+	JXA7CD 63 33B	PORT A/B DATA LINE7 BUS
XA241	TLD	E4	33B	JXA7CD	00	=		JXA7CB4 BUSS
XA241	TLD	E4	31B	(63)	01		JXA1E0 JXA7COX 59 31B 61 32B	
XA243	DCF	B7	10A	JXA7CDX	00	=		
XA243	DCF	B7	13A	(23)	01		JXA0EA 36 13A	TA07AB BUSS
XA243	DCF	B8	11A	JXA7COX	00	=		
XA243	DCF	B8	12A	(30)	01		JXS474U 34 12A	

CONNECTOR	TEST POINT GROUP	TEST POINT AND OR	EQUATION	TERMINAL	DESIGNATOR	FACTOR	COMMENT
XA240	TLD	F2 34A	JXBCMD	00 =			
XA240	TLD	F2 36A	(72)	01	JXBIFO JXBCMOX 71 36A 73 36B	JXACMB4 BUSS	
XA242	DCF	C1 25B	JXBCMDX	00 =			
XA242	DCF	C1 29A	(46)	01	JXBOEA 52 29A	TACMBB BUSS	
XA242	DCF	C2 26B	JXBCMOX	00 =			
XA242	DCF	C2 27B	(47)	01	JXGN1A 49 27B		
XA240	TLD	F3 35A	JXBEND	00 =			
XA240	TLD	F3 34B	(69)	01	JXBIFO JXBENOX 65 34B 74 35B	JXAENB4 BUSS	
XA242	DCF	C3 30B	JXBENDX	00 =			
XA242	DCF	C3 29A	(55)	01	JXBOEA 52 29A	TAENBB BUSS	
XA242	DCF	C4 29B	JXBENOX	00 =			
XA242	DCF	C4 28B	(56)	01	JXGN1A 51 28B		
XA238	TQ2	A2 02B	JXBIEA	00 =			
XA238	TQ2	A2 04A	(01)	01	JXBSLOX JXXDRA 04 04A 05 03B		
XA239	TQ2	A4 07B	JXBIE0	00 =			
XA239	TQ2	A4 05B	(15)	01	JXBIEA SPI0202 11 05B 13 06B	PORT B DATA RECEIVE ENABLE	
XA239	TQ2	B1 12A	JXBIFO	00 =			
XA239	TQ2	B1 13A	(22)	01	JXBIEA SPI0202 24 13A 26 14A		
XA242	DCF	C5 31B	JXBINDX	00 =			
XA242	DCF	C5 29A	(60)	01	JXBOEA 52 29A	TAINBB BUSS	
XA242	DCF	C6 31A	JXBINOX	00 =			
XA242	DCF	C6 30A	(57)	01	JXDBIO 54 30A		
XA240	TLD	F1 37B	JXBPCD	00 =			
XA240	TLD	F1 38B	(75)	01	JXBIFO JXBPCOX 77 38B 79 39B	JXAPCB4 BUSS	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA242	DCF	C7	25A	JXBPCDX	00	=		
XA242	DCF	C7	29A	(43)	01		JXB0EA 52 29A	TAOPBB BUSS
XA242	DCF	C8	26A	JXBPCOX	00	=		
XA242	DCF	C8	28A	(48)	01		JXDSBPR 50 28A	
XA237	TD4	E2	30B	JXBRQA	00	=		
XA237	TD4	E2	29B	(57)	01		JXBSLOX JXINHR DEVINH JXXRQO 55 29B 60 28A 62 29A 64 30A	
XA227	TQ2	E2	28A	JXBRQO	00	=		PORT B REQUEST
XA227	TQ2	E2	29A	(60)	01		JXBRQA SPI0162 62 29A 64 30A	
XA232	TT3	B2	09B	JXBRSA	00	=		PORT B IOU RESET
XA232	TT3	B2	09A	(19)	01		JXBCMOX JXBENOX JXBSLOX 14 09A 17 08B 18 10A	
XA240	TLD	F4	39A	JXBR7D1	00	=		
XA240	TLD	F4	37A	(80)	01		JXBRQO SPI0201 76 37A 78 38A	
XA238	TQ2	B2	09A	JXBSLA	00	=		PORT B SELECT
XA238	TQ2	B2	10A	(14)	01		JXBSLOX SPI0182 18 10A 20 11A	
XA242	DCF	D3	37B	JXBSDLX	00	=		
XA242	DCF	D3	36A	(78)	01		SPI0212 72 36A	
XA242	DCF	D4	36B	JXBSLOX	00	=		
XA242	DCF	D4	35B	(75)	01		SPI0042 73 35B	
XA243	DCF	D3	37B	JXBSTD4	00	=		
XA243	DCF	D3	36A	(78)	01		JXGN1A 72 36A	
XA243	DCF	D4	36B	JXBSTOX	00	=		
XA243	DCF	D4	35B	(75)	01		JXBSLOX 73 35B	
XA240	TLD	D1	24A	JXB0CD	00	=		JXA0CB4 BUSS
XA240	TLD	D1	25A	(52)	01		JXBIEO JXB0COX 54 25A 56 26A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA242	DCF	A1	02B	JXB0CDX	00	=		
XA242	DCF	A1	05A	(07)	01		JXB0EA 06 05A	TA00BB BUSS
XA242	DCF	A2	03B	JXB0COX	00	=		
XA242	DCF	A2	04B	(09)	01		JXS031U 11 04B	
XA237	TD4	A2	04B	JXB0EA	00	=		
XA237	TD4	A2	02B	(09)	01		JXBSLOX JXINHR DEVINH JXXCSO 01 02B 04 04A 05 03B 07 03A	PORT B DATA SEND ENABLE
XA240	TLD	D2	21B	JXB1CD	00	=		
XA240	TLD	D2	22A	(46)	01		JXB1EO JXB1COX 48 22A 50 23A	JXA1CB4 BUSS
XA242	DCF	A3	07B	JXB1CDX	00	=		
XA242	DCF	A3	05A	(17)	01		JXB0EA 06 05A	TA01BB BUSS
XA242	DCF	A4	06B	JXB1COX	00	=		
XA242	DCF	A4	05B	(15)	01		JXS032U 13 05B	
XA240	TLD	D3	24B	JXB2CD	00	=		
XA240	TLD	D3	22B	(45)	01		JXB1EO JXB2COX 41 22B 43 23B	JXA2CB4 BUSS
XA242	DCF	A5	08B	JXB2CDX	00	=		
XA242	DCF	A5	05A	(14)	01		JXB0EA 06 05A	TA02BB BUSS
XA242	DCF	A6	07A	JXB2COX	00	=		
XA242	DCF	A6	06A	(10)	01		JXS033U 08 06A	
XA240	TLD	D4	27B	JXB3CD	00	=		
XA240	TLD	D4	25B	(51)	01		JXB1EO JXB3COX 47 25B 49 26B	JXA3CB4 BUSS
XA242	DCF	A7	02A	JXB3CDX	00	=		
XA242	DCF	A7	05A	(01)	01		JXB0EA 06 05A	TA03BB BUSS
XA242	DCF	A8	03A	JXB3COX	00	=		
XA242	DCF	A8	04A	(03)	01		JXS034U 04 04A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA240	TLD	E1	31A	JXB4CD	00	=		JXA4CB4 BUSS
XA240	TLD	E1	32A	(66)	01		JXB1E0 JXB4COX 68 32A 70 33A	
XA242	DCF	B1	10B	JXB4CDX	00	=		TA04BB BUSS
XA242	DCF	B1	13A	(27)	01		JXB0EA 36 13A	
XA242	DCF	B2	11B	JXB4COX	00	=		
XA242	DCF	B2	12B	(29)	01		JXS471U 31 12B	
XA240	TLD	E2	28A	JXB5CD	00	=		JXA5CB4 BUSS
XA240	TLD	E2	29A	(60)	01		JXB1E0 JXB5COX 62 29A 64 30A	
XA242	DCF	B3	15B	JXB5CDX	00	=		TA05BB BUSS
XA242	DCF	B3	13A	(37)	01		JXB0EA 36 13A	
XA242	DCF	B4	14B	JXB5COX	00	=		
XA242	DCF	B4	13B	(35)	01		JXS472U 33 13B	
XA240	TLD	E3	30B	JXB6CD	00	=		JXA6CB4 BUSS
XA240	TLD	E3	28B	(57)	01		JXB1E0 JXB6COX 53 28B 55 29B	
XA242	DCF	B5	16A	JXB6CDX	00	=		TA06BB BUSS
XA242	DCF	B5	13A	(41)	01		JXB0EA 36 13A	
XA242	DCF	B6	15A	JXB6COX	00	=		
XA242	DCF	B6	14A	(40)	01		JXS473U 38 14A	
XA240	TLD	E4	33B	JXB7CD	00	=		JXA7CB4 BUSS
XA240	TLD	E4	31B	(63)	01		JXB1E0 JXB7COX 59 31B 61 32B	
XA242	DCF	B7	10A	JXB7CDX	00	=		TA07BB BUSS
XA242	DCF	B7	13A	(23)	01		JXB0EA 36 13A	
XA242	DCF	B8	11A	JXB7COX	00	=		
XA242	DCF	B8	12A	(30)	01		JXS474U 34 12A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERMINAL DESIGNATION	FACTOR	COMMENT
XA237	TD4	F1	37A	JXCA0A	00 =		SET COMMAND ADDRESS F/E
XA237	TD4	F1	37B	(76)	01	JXR7CS JXRCMS JXX050 JXROPA 75 37B 77 38B 78 38A 79 39B	
XA231	TS8	A1	05B	JXCA1A	00 =		DEV COMND BYTE 1 COMMON TERMS
XA231	TS8	A1	02B	(11)	01	JXCMA5 JXXB10 JXXA0P JXXA1Q JXRPCS JXROCS JXR1CR JXR2CR 01 02B 04 04A 05 03B 06 05A 07 03A 08 06A 10 07A 13 06B	
XA235	TQ2	B4	13B	JXCA10	00 =		
XA235	TQ2	B4	11B	(27)	01	JXCA1A SPI0182 23 11B 25 12B	
XA237	TD4	B1	11B	JXCMA5	00 =		
XA237	TD4	B1	12A	(23)	01	JXCMA5 JXX04A JXXB2A JXRS0B 22 12A 24 13A 25 12B 26 14A	
XA235	TQ2	B1	12A	JXCMA5	00 =		COMMON ADDRESS F/E
XA235	TQ2	B1	13A	(22)	01	JXCMA5 JXCA0A 24 13A 26 14A	
XA227	TQ2	A2	02B	JXCP00	00 =		JXCP1B BUSS
XA227	TQ2	A2	04A	(01)	01	JXCP1A SPI0152 04 04A 05 03B	
XA235	TQ2	E4	33B	JXCP1A	00 =		PHASE 1 OF 2 PHASECLOCK
XA235	TQ2	E4	31B	(63)	01	JX1MAP JX1MBQ 59 31B 61 32B	
				JXCP1B	00 =		
XA227	TQ2	A3	04B	( )	01	JXCP10 JXCP20 JXCP00 09 04B 15 07B 01 02B	CLOCK PHASE 1 BUS
XA227	TQ2	A3	04B	JXCP10	00 =		JXCP1B BUSS
XA227	TQ2	A3	02A	(09)	01	JXCP1A SPI0152 03 02A 07 03A	
XA227	TQ2	A4	07B	JXCP20	00 =		JXCP1B BUSS
XA227	TQ2	A4	05B	(15)	01	JXCP1A SPI0152 11 05B 13 06B	
XA227	TQ2	B2	09A	JXCP3A	00 =		PHASE 3 OF 2 PHASECLOCK
XA227	TQ2	B2	10A	(14)	01	JX1MAQ JX1MBP 18 10A 20 11A	
				JXCP3B	00 =		
XA226	TQ2	A2	02B	( )	01	JXCP30 JXCP40 JXCP50 01 02B 09 04B 15 07B	CLOCK PHASE 3 BUS



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERN	DESIGNATOR	FACTOR	COMMENT
XA226	TQ2	A2	02B	JXCP30	00	=	JXCP3B BUSS	
XA226	TQ2	A2	04A	(01)	01		JXCP3A SPI0152 04 04A 05 03B	
XA226	TQ2	A3	04B	JXCP40	00	=	JXCP3B BUSS	
XA226	TQ2	A3	02A	(09)	01		JXCP3A SPI0152 03 02A 07 03A	
XA226	TQ2	A4	07B	JXCP50	00	=	JXCP3B BUSS	
XA226	TQ2	A4	05B	(15)	01		JXCP3A SPI0152 11 05B 13 06B	
XA232	TT3	E3	33B	JXDB10	00	=	INPUT INDICATOR CONTROL	
XA232	TT3	E3	30B	(63)	01		JXXCIP JXXDIP JXXDSP 57 30B 59 31B 61 32B	
XA219	TQ2	F1	37B	JXD8S0	00	=	INPUT MUX STATUS SELECT	
XA219	TQ2	F1	38B	(75)	01		JXXDIP JXXDSP 77 38B 79 39B	
XA216	MUX	D1	27B	JXDB0TA	00	=		
XA216	MUX	D1	25B	(55)	01		JD00BQ JA0ENS JXXC1Q JXD8S0 51 25B 53 26B 52 25A 49 24B	
XA216	MUX	D2	31B	JXDB0TB	00	=	INPUT DATA MUX BITS 0-1-2-3	
XA216	MUX	D2	29B	(61)	01		JD01BQ JATENS 57 29B 59 30B	
XA216	MUX	D3	28A	JXDB0TC	00	=		
XA216	MUX	D3	26A	(60)	01		JD02BQ JPE0BQ 54 26A 56 28B	
XA216	MUX	D4	31A	JXDB0TD	00	=		
XA216	MUX	D4	29A	(63)	01		JD03BQ JPETXQ 62 29A 64 30A	
XA216	MUX	E1	34B	JXDB4TA	00	=	INPUT DATA MUX BITS 4-5-6-7	
XA216	MUX	E1	32A	(73)	01		JD04BQ JPRQPO JXXC1Q JXD8S0 69 32A 71 33B 68 33A 66 32B	
XA216	MUX	E2	37B	JXDB4TB	00	=		
XA216	MUX	E2	35B	(79)	01		JD05BQ JPBZY0 75 35B 77 36B	
XA216	MUX	E3	36A	JXDB4TC	00	=		
XA216	MUX	E3	34A	(74)	01		JD06BQ JK009A1 70 34A 72 35A	

CONNECTOR	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATION	FACTOR	COMMENT
XA216	MUX	E4 38B	JXDB4TD	00 =			
XA216	MUX	E4 37A	(80)	01	J007BQ JCDERQ 76 37A 78 38A		
XA227	TQ2	C1 18A	JXDEVA	00 =			
XA227	TQ2	C1 19A	(38)	01	JXR3CS SPI0162 40 19A 42 20A		
XA237	TD4	C2 16B	JXDEVR	00 =			
XA237	TD4	C2 15A	(33)	01	JXDEVS JXXB0A JXXB3A JXRS0B 30 15A 31 15B 34 16A 36 17A		
XA235	TQ2	C2 15A	JXDEVS	00 =			DEVICE COMMAND F/EMAYBE BSY
XA235	TQ2	C2 16A	(30)	01	JXDEVR JXDVOA 34 16A 36 17A		
XA226	TQ2	B1 12A	JXDEVO	00 =			
XA226	TQ2	B1 13A	(22)	01	JXDEVA SPI0152 24 13A 26 14A		
XA225	TD4	A2 04B	JXDPEA	00 =			AUTO OUTPUT COMND
XA225	TD4	A2 02B	(09)	01	J00UTQ JXENAS JXROPPR JXXA50 01 02B 04 04A 05 03B 07 03A		
XA236	PAR	E1 33A	JXDSPR	00 =			
XA236	PAR	E1 29B	(68)	01	JXS031U JXS032U JXS033U JXS034U JXS471U JXS472U JXS473U JXS474U 55 29B 59 30B 61 31B 63 32B 64 31A 62 30A 60 29A 57 28A		
XA236	PAR	E1 33B	( )	02 +	JXXCIQ 65 33B		
XA227	TQ2	C2 15A	JXDVCO	00 =			
XA227	TQ2	C2 16A	(30)	01	JXR090T SPI0162 34 16A 36 17A		
XA237	TD4	C1 17B	JXDVSR	00 =			
XA237	TD4	C1 18B	(35)	01	JXDVSS JXXB0A JXXB3A JXRS0B 37 18B 38 18A 40 19A 42 20A		
XA235	TQ2	C1 18A	JXDVSS	00 =			DEVICE COMMAND F/ENO BUSY
XA235	TQ2	C1 19A	(38)	01	JXDVSR JXDV5A 40 19A 42 20A		
XA225	TD4	C1 17B	JXDVOA	00 =			SET DEVIC COMMAND F/E
XA225	TD4	C1 18B	(35)	01	JXCA10 JXDEVO JXDVCO JBUSYA 37 18B 38 18A 40 19A 42 20A		
XA231	TS8	C1 17B	JXDV1A	00 =			
XA231	TS8	C1 15A	(35)	01	JXDEVS JXXB20 JXXA0P JXXA1Q JXROPA SPI0142 SPI0162 SPI0032 30 15A 31 15B 34 16A 36 17A 37 18B 38 18A 40 19A 42 20A		

CONNECTOR	CIRCUIT POINT	GROUP	TEST POINTS AND/OR	EQUATION	TERMINAL	DESIGNATOR	FACTOR	COMMENT
XA223	TQ2	F2	34A	JXDV10	00	=		DEV COMMAND DATA BYTE STROBE
XA223	TQ2	F2	36A	(72)	01		JXDV1A SPI0152 71 36A 73 36B	
XA219	TQ2	E4	33B	JXDV2A	00	=		
XA219	TQ2	E4	31B	(63)	01		JXDV2Q JXDV20 59 31B 61 32B	
XA221	TQ2	E3	30B	JXDV20	00	=		
XA221	TQ2	E3	28B	(57)	01		JXDV2R JXDV2I 53 28B 55 29B	
XA213	TSB	D1	25B	JXDV3A	00	=		START END INPUT COMMAND
XA213	TSB	D1	23B	(47)	01		JXDV3S JXDV3Z JXDV3P JXDV3I JENINO JXROPA JXDV2A SPI0012 43 23B 46 21A 48 22A 49 26B 50 23A 52 24A 54 25A 56 26A	
XA232	TT3	A3	07B	JXDV5A	00	=		SET DEV COMND E/F NO BUSY
XA232	TT3	A3	04B	(15)	01		JXDV5I JXDV5V JXDV5C 09 04B 11 05B 13 06B	
XA225	TD4	A1	05B	JXEA0A	00	=		SET ENABLE ADDRESS/E
XA225	TD4	A1	05A	(11)	01		JXEA0S JXEA0N JXEA0O JXROPA 06 05A 08 06A 10 07A 13 06B	
XA223	TQ2	F4	39A	JXEA00	00	=		
XA223	TQ2	F4	37A	(80)	01		JXEA0A SPI0152 76 37A 78 38A	
XA232	TT3	C1	17A	JXEBOA	00	=		COMMAND IS FOR
XA232	TT3	C1	18A	(36)	01		JXCA10 JXEBOB JXDEVA 38 18A 40 19A 42 20A	
XA228	TDD	D1	10A	JXEBOI	00	=		
XA228	TDD	D1	10A	( )	01		JXGN2A 18 10A	
XA228	TDD	DN	09A	JXEBOH	00	=		
XA228	TDD	DN	09A	( )	01		JXEBOI 14 09A	
XA228	TDD	DP	10B	JXEBOP	00	=		
XA228	TDD	DP	11A	(21)	01		JXRSOB 20 11A	
XA228	TDD	DQ	09B	JXEBOQ	00	=		EOB SYNC COUNTER BIT 0
XA228	TDD	DQ	08B	(19)	01		JXEBOA 17 08B	

CONNECTOR	TEST POINT AND/OR	PROB	TEST POINTS AND/OR	EQUATION	TER	DESIGNATION	FACTOR	COMMENT
XA227	TQ2	D3	24B	JXEB1A	00 =			
XA227	TQ2	D3	22B	(45 )	01		JXEB1Q JXCP3B 41 22B 43 23B	
				JXEB1I	00 =			
XA230	TDD	DI	10A	( )	01		JXEBOQ 18 10A	
				JXEB1N	00 =			
XA230	TDD	DN	09A	( )	01		JXCP1B 14 09A	
				JXEB1P	00 =			
XA230	TDD	DP	10B	(21 )	01		JXRSOB 20 11A	
				JXEB1Q	00 =			
XA230	TDD	DQ	09B	(19 )	01		SPI0142 17 08B	EOB SYNC COUNTER BIT 1
				JXE00A	00 =			
XA225	TD4	B1	11B	(23 )	01		JDOU0Q JXENAS JXXB10 JXXA50 22 12A 24 13A 25 12B 26 14A	AUTO OUTPUT DATA STROBE
				JXENAR	00 =			
XA237	TD4	B2	10B	(21 )	01		JXENAS JXX04A JXXB2A JXRSOB 14 09A 18 10A 19 09B 20 11A	
				JXENAS	00 =			
XA235	TQ2	B3	10B	(21 )	01		JXENAR JXEAOA 17 08B 19 09B	ENABLE ADDRESS E/E
				JXE0B0	00 =			
XA227	TQ2	D1	24A	(52 )	01		JXR092T SPI0162 54 25A 56 26A	
				JXGN1A	00 =			
XA238	TQ2	A3	04B	(09 )	01		SPI0182 SPI0142 03 02A 07 03A	SOFT GROUND
				JXGN2A	00 =			
XA235	TQ2	C4	19B	(39 )	01		SPI0142 SPI0182 35 17B 37 18B	
				JXGN3A	00 =			
XA226	TQ2	D4	27B	(51 )	01		SPI0142 SPI0152 47 25B 49 26B	

CONNECTOR	TEST POINT AND/OR	GROUP	TEST POINTS AND/OR	EQUATION	TEST POINT	DESIGNATION	FACTOR	COMMENT
XA239	TQ2	B3	10B	JXGN4A	00 =			
XA239	TQ2	B3	08B	(21)	01		SPI0202 SPI0192 17 08B 19 09B	
XA226	TQ2	D1	24A	JXHSTO	00 =			
XA226	TQ2	D1	25A	(52)	01		JXR091T SPI0152 54 25A 56 26A	
XA232	TT3	C2	15B	JXH50A	00 =			COMMAND IS STOP
XA232	TT3	C2	14B	(31)	01		JXCA10 JXHSTO JXDEVA 29 14B 30 15A 34 16A	
XA235	TQ2	A4	07B	JXINHR	00 =			
XA235	TQ2	A4	05B	(15)	01		JXINHS JXINOA 11 05B 13 06B	
XA232	TT3	A2	03A	JXINHS	00 =			DATA SEND INHIBIT E/E
XA232	TT3	A2	02B	(07)	01		JXINHR JXRS0B JXONLO 01 02B 03 02A 05 03B	
XA232	TT3	D2	23B	JXINOA	00 =			
XA232	TT3	D2	22B	(43)	01		JXRS0B JXRCMS JXONLO 41 22B 46 21A 48 22A	
XA226	TQ2	C4	19B	JXIRCO	00 =			
XA226	TQ2	C4	17B	(39)	01		JXR094T SPI0152 35 17B 37 18B	
XA232	TT3	B3	13B	JXIROA	00 =			COMMAND IS ITR
XA232	TT3	B3	10B	(27)	01		JXCA10 JXIRCO JXDEVA 21 10B 23 11B 25 12B	
XA228	TDD	C1	13A	JXIROI	00 =		JXGN2A 24 13A	
XA228	TDD	CN	14A	JXIRON	00 =		JXIR1A 26 14A	
XA228	TDD	CP	11B	JXIROP	00 =			
XA228	TDD	CP	12A	(23)	01		JXRS0B 22 12A	
XA228	TDD	CQ	12B	JXIROQ	00 =			ITR SYNC COUNTER BIT 0
XA228	TDD	CQ	13B	(25)	01		JXIROA 27 13B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
			AND	OR					
XA227	TQ2	B4	13B		JXIR1A	00	=		
XA227	TQ2	B4	11B		(27)	01		JXIR1Q JXCP3B 23 11B 25 12B	
					JXIR1I	00	=		
XA229	TDD	01	10A		( )	01		JXIROQ 18 10A	
					JXIR1N	00	=		
XA229	TDD	DN	09A		( )	01		JXCP1B 14 09A	
XA229	TDD	DP	10B		JXIR1P	00	=		
XA229	TDD	DP	11A		(21)	01		JXRS0B 20 11A	
XA229	TDD	DQ	09B		JXIR1Q	00	=		
XA229	TDD	DQ	08B		(19)	01		SPI0142 17 08B	IIR SYNC COUNTER BIT 1
XA238	TQ2	A4	07B		JXPRSA	00	=		
XA238	TQ2	A4	05B		(15)	01		JXPRSOX SPI0182 11 05B 13 06B	CAP PANEL RESET
XA242	DCF	05	38B		JXPRSDX	00	=		
XA242	DCF	05	36A		(80)	01		SPI0212 72 36A	
XA242	DCF	06	38A		JXPRSOX	00	=		
XA242	DCF	06	37A		(76)	01		SPI0062 74 37A	
XA234	DBC	A1	04A		JXRAF0T	00	=		
XA234	DBC	A1	02A		(08)	01		JXR7CS 04 02A	DATA BYTE DECODER A TO F
XA234	DBC	A2	05A		JXRAF1T	00	=		
XA234	DBC	A2	03A		(10)	01		JXR6CS 06 03A	
XA234	DBC	A3	06A		JXRAF2T	00	=		
XA234	DBC	A3	02B		(14)	01		JXR5CS 03 02B	
XA234	DBC	A4	07A		JXRAF3T	00	=		
XA234	DBC	A4	03B		(13)	01		JXR4CR 05 03B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA234	DBC	A5	08B	JXRAF4T	00	=		
XA234	DBC	A5		(17)	01		SPA 4T	
XA234	DBC	A6	04B	JXRAF5T	00	=		
XA234	DBC	A6		(07)	01		SPA 5T	
XA234	DBC	A7	05B	JXRAF6T	00	=		
XA234	DBC	A7		(09)	01		SPA 6T	
XA234	DBC	A8	06B	JXRAF7T	00	=		
XA234	DBC	A8		(11)	01		SPA 7T	
XA234	DBC	A9	07B	JXRAF8T	00	=		
XA234	DBC	A9		(15)	01		SPA 8T	
XA234	DBC	A0	09A	JXRAF9T	00	=		
XA234	DBC	A0		(18)	01		SPA 9T	
XA238	TQ2	C1	18A	JXRCMR	00	=		
XA238	TQ2	C1	19A	(38)	01		JXRCMS JXRRSA 40 19A 42 20A	
XA239	TQ2	C1	18A	JXRCMS	00	=		I/O INPUT REG COMMAND BIT
XA239	TQ2	C1	19A	(38)	01		JXRCMR JXACMB4 40 19A 42 20A	
XA238	TQ2	C2	15A	JXREN R	00	=		
XA238	TQ2	C2	16A	(30)	01		JXRENS JXRRSA 34 16A 36 17A	
XA239	TQ2	C2	15A	JXRENS	00	=		I/O INPUT REG ENABLE BIT
XA239	TQ2	C2	16A	(30)	01		JXREN R JXAENB4 34 16A 36 17A	
XA238	TQ2	C3	16B	JXRPCR	00	=		
XA238	TQ2	C3	14B	(33)	01		JXRPCS JXRRSA 29 14B 31 15B	
XA239	TQ2	C3	16B	JXRPCS	00	=		
XA239	TQ2	C3	14B	(33)	01		JXRPCR JXAPCB4 29 14B 31 15B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA226	TQ2	A1	05A	JXRRSA	00	=		
XA226	TQ2	A1	06A	(06)	01		JXRRS0 SPI0152 08 06A 10 07A	RESET I/O INPUT REGISTER
XA227	TQ2	A1	05A	JXRRS0	00	=		
XA227	TQ2	A1	06A	(06)	01		JXA6A JXRS0B 08 06A 10 07A	
XA235	TQ2	A1	05A	JXRS0A	00	=		JXRS0B BUSS
XA235	TQ2	A1	06A	(06)	01		JXRS00 SPI0182 08 06A 10 07A	
				JXRS0B	00	=		
XA235	TQ2	A1	05A	( )	01		JXRS0A JXRS1A JXRS2A 06 05A 01 02B 09 04B	MASTER RESET BUS 0
XA238	TQ2	B4	13B	JXRS00	00	=		
XA238	TQ2	B4	11B	(27)	01		JXST1A SPI0182 23 11B 25 12B	
XA235	TQ2	A2	02B	JXRS1A	00	=		JXRS0B BUSS
XA235	TQ2	A2	04A	(01)	01		JXRS00 SPI0182 04 04A 05 03B	
				JXRS1B	00	=		
XA226	TQ2	C1	18A	( )	01		JXRS3A JXRS4A JXRS5A 38 18A 30 15A 33 16B	MASTER RESET BUS 1
XA235	TQ2	A3	04B	JXRS2A	00	=		JXRS0B BUSS
XA235	TQ2	A3	02A	(09)	01		JXRS00 SPI0182 03 02A 07 03A	
XA226	TQ2	C1	18A	JXRS3A	00	=		JXRS1B BUSS
XA226	TQ2	C1	19A	(38)	01		JXRS00 SPI0152 40 19A 42 20A	
XA226	TQ2	C2	15A	JXRS4A	00	=		JXRS1B BUSS
XA226	TQ2	C2	16A	(30)	01		JXRS00 SPI0152 34 16A 36 17A	
XA226	TQ2	C3	16B	JXRS5A	00	=		JXRS1B BUSS
XA226	TQ2	C3	14B	(33)	01		JXRS00 SPI0152 29 14B 31 15B	
XA238	TQ2	D1	24A	JXROCR	00	=		
XA238	TQ2	D1	25A	(52)	01		JXROCS JXRRSA 54 25A 56 26A	



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIG- NATOR	FACTOR	COMMENT
XA239	TQ2	D1	24A	JXR0CS	00	=		
XA239	TQ2	D1	25A	(52)	01		JXR0CR JXA0CB4 54 25A 56 26A	
XA235	TQ2	F1	37B	JXROPA	00	=		I/O INPUT REG PARITY ERROR
XA235	TQ2	F1	38B	(75)	01		JXR0PPR SPI0182 77 38B 79 39B	
XA236	PAR	F1	39A	JXR0PPR	00	=		
XA236	PAR	F1	35B	(80)	01		JXR0CS JXR1CS JXR2CS JXR3CS JXR4CS JXR5CS JXR6CS JXR7CS 71 35B 73 36B 75 37B 77 38B 76 37A 74 36A 72 35A 70 34A	
XA236	PAR	F1	39B	( )	02	+	JXRPCS 79 39B	
XA233	DBC	B1	12A	JXR090T	00	=		DATA BYTE DECODER 0109
XA233	DBC	B1	10A	(24)	01		JXR7CS 20 10A	
XA233	DBC	B2	13A	JXR091T	00	=		
XA233	DBC	B2	11A	(26)	01		JXR6CS 22 11A	
XA233	DBC	B3	14A	JXR092T	00	=		
XA233	DBC	B3	09B	(27)	01		JXR5CS 19 09B	
XA233	DBC	B4	15A	JXR093T	00	=		
XA233	DBC	B4	10B	(30)	01		JXR4CS 21 10B	
XA233	DBC	B5	16A	JXR094T	00	=		
XA233	DBC	B5		(33)	01		SPA 4T	
XA233	DBC	B6	11B	JXR095T	00	=		
XA233	DBC	B6		(23)	01		SPA 5T	
XA233	DBC	B7	12B	JXR096T	00	=		
XA233	DBC	B7		(25)	01		SPA 6T	
XA233	DBC	B8	13B	JXR097T	00	=		
XA233	DBC	B8		(29)	01		SPA 7T	
XA233	DBC	B9	14B	JXR098T	00	=		
XA233	DBC	B9		(31)	01		SPA 8T	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERN	DESIGNATOR	FACTOR	COMMENT
XA233	DBC	B0	15B	JXR099T	00	=		
XA233	DBC	B0		(34)	01		SPA 9T	
XA238	TQ2	D2	21A	JXR1CR	00	=		
XA238	TQ2	D2	22A	(46)	01		JXR1CS JXR RSA 48 22A 50 23A	
XA239	TQ2	D2	21A	JXR1CS	00	=		
XA239	TQ2	D2	22A	(46)	01		JXR1CR JXA1CB4 48 22A 50 23A	
XA238	TQ2	D3	24B	JXR2CR	00	=		
XA238	TQ2	D3	22B	(45)	01		JXR2CS JXR RSA 41 22B 43 23B	
XA239	TQ2	D3	24B	JXR2CS	00	=		
XA239	TQ2	D3	22B	(45)	01		JXR2CR JXA2CB4 41 22B 43 23B	
XA238	TQ2	D4	27B	JXR3CR	00	=		
XA238	TQ2	D4	25B	(51)	01		JXR3CS JXR RSA 47 25B 49 26B	
XA239	TQ2	D4	27B	JXR3CS	00	=		
XA239	TQ2	D4	25B	(51)	01		JXR3CR JXA3CB4 47 25B 49 26B	
XA238	TQ2	E1	31A	JXR4CR	00	=		
XA238	TQ2	E1	32A	(66)	01		JXR4CS JXR RSA 68 32A 70 33A	
XA239	TQ2	E1	31A	JXR4CS	00	=		
XA239	TQ2	E1	32A	(66)	01		JXR4CR JXA4CB4 68 32A 70 33A	
XA238	TQ2	E2	28A	JXR5CR	00	=		
XA238	TQ2	E2	29A	(60)	01		JXR5CS JXR RSA 62 29A 64 30A	
XA239	TQ2	E2	28A	JXR5CS	00	=		
XA239	TQ2	E2	29A	(60)	01		JXR5CR JXA5CB4 62 29A 64 30A	
XA238	TQ2	E3	30B	JXR6CR	00	=		
XA238	TQ2	E3	28B	(57)	01		JXR6CS JXR RSA 53 28B 55 29B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA239	TQ2	E3	30B	JXR6CS	00	=		
XA239	TQ2	E3	28B	(57)	01		JXR6CR JXA6CB4 53 28B 55 29B	
XA238	TQ2	E4	33B	JXR7CR	00	=		
XA238	TQ2	E4	31B	(63)	01		JXR7CS JXRRSA 59 31B 61 32B	
XA239	TQ2	E4	33B	JXR7CS	00	=		
XA239	TQ2	E4	31B	(63)	01		JXR7CR JXA7CB4 59 31B 61 32B	
XA222	TQ2	F3	35A	JXSK0A	00	=		
XA222	TQ2	F3	34B	(69)	01		JXXC2Q JXXC3P 65 34B 74 35B	
XA227	TQ2	C4	19B	JXSK00	00	=		I/O BUFFER REGISTER CLOCK
XA227	TQ2	C4	17B	(39)	01		JXSK0A SPI0162 35 17B 37 18B	
XA227	TQ2	D2	21A	JXSST0	00	=		
XA227	TQ2	D2	22A	(46)	01		JXR097T SPI0162 48 22A 50 23A	
XA214	TSB	A1	05B	JXSS0A	00	=		SOFTWARE STOP COMM
XA214	TSB	A1	02B	(11)	01		JXBVSS JXXB20 JXXAOP JXXAIQ JXSST0 JXROPA SPI0022 SPI0012 01 02B 04 04A 05 03B 06 05A 07 03A 08 06A 10 07A 13 06B	
				JXST0I	00	=		
XA228	TDD	E1	19A	( )	01		JXGN1A 40 19A	
				JXSTON	00	=		
XA228	TDD	EN	20A	( )	01		JXST1A 42 20A	
XA228	TDD	EP	17B	JXSTOP	00	=		
XA228	TDD	EP	18A	(35)	01		SPI0162 38 18A	
XA228	TDD	EQ	18B	JXST0Q	00	=		STOP SYNC COUNTER BIT 0
XA228	TDD	EQ	19B	(37)	01		JXST2A 39 19B	
XA223	TQ2	D3	24B	JXST1A	00	=		
XA223	TQ2	D3	22B	(45)	01		JXST1Q JXCP3B 41 22B 43 23B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
				JXST11	00	=		
XA229	TDD	E1	19A	( )	01		JXSTOQ 40 19A	
				JXST1N	00	=		
XA229	TDD	EN	20A	( )	01		JXCP1B 42 20A	
XA229	TDD	EP	17B	JXST1P	00	=		
XA229	TDD	EP	18A	(35 )	01		SPI0162 38 18A	
XA229	TDD	EQ	18B	JXST1Q	00	=		
XA229	TDD	EQ	19B	(37 )	01		SPI0142 39 19B	STOP SYNC COUNTER BIT 1
XA226	TQ2	D3	24B	JXST2A	00	=		
XA226	TQ2	D3	22B	(45 )	01		JXST20 SPI0152 41 22B 43 23B	
XA231	TS8	B1	11B	JXST20	00	=		
XA231	TS8	B1	09A	(23 )	01		JXH50A JXSS0A JXARSA JXBRSA JXPRSA JDEVIA DEVINH SPI0162 14 09A 18 10A 19 09B 20 11A 22 12A 24 13A 25 12B 26 14A	START MASTER RESETOR GATE
XA234	DBC	E1	34A	JXS031U	00	=		
XA234	DBC	E1	33B	(70 )	01		JXDB0TA JXDB0TB JXDB0TC JXDB0TD JXGN1A 71 33B 73 34B 75 35B 77 36B 80 38B	
XA234	DBC	E2	35A	JXS032U	00	=		
XA234	DBC	E2	32A	(72 )	01		JXSK00 69 32A	
XA234	DBC	E3	36A	JXS033U	00	=		
XA234	DBC	E3	37B	(74 )	01		JXGN2A 79 37B	
XA234	DBC	E4	37A	JXS034U	00	=		
XA234	DBC	E4	38A	(76 )	01		JXGN3A 78 38A	I/O BUFFER REG BITS 0-1-2-3
XA234	DBC	E5	33A	JXS035U	00	=		
XA234	DBC	E5	32B	(68 )	01		SPI0182 66 32B	
XA233	DBC	E1	34A	JXS471U	00	=		
XA233	DBC	E1	33B	(70 )	01		JXDB4TA JXDB4TB JXDB4TC JXDB4TD JXGN1A 71 33B 73 34B 75 35B 77 36B 80 38B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA233	DBC	E2	35A	JXS472U	00	=		
XA233	DBC	E2	32A	(72)	01		JXSK00 69 32A	
XA233	DBC	E3	36A	JXS473U	00	=		
XA233	DBC	E3	37B	(74)	01		JXGN2A 79 37B	
XA233	DBC	E4	37A	JXS474U	00	=		
XA233	DBC	E4	38A	(76)	01		JXGN3A 78 38A	
XA233	DBC	E5	33A	JXS475U	00	=		I/O BUFFER REG BITS 4-5-6-7
XA233	DBC	E5	32B	(68)	01		SPI0182 66 32B	
XA239	TQ2	C4	19B	JXXACA	00	=		
XA239	TQ2	C4	17B	(39)	01		JXXAC0 SPI0202 35 17B 37 18B	
XA228	TDD	A1	06A	JXXACI	00	=		
XA228	TDD	A1	06A	( )	01		SPI0162 08 06A	
XA228	TDD	AN	07A	JXXACN	00	=		
XA228	TDD	AN	07A	( )	01		JXXA3P 10 07A	
XA228	TDD	AP	05B	JXXACP	00	=		
XA228	TDD	AP	05A	(11)	01		JXXADA 06 05A	
XA228	TDD	AQ	06B	JXXACQ	00	=		I/O STATE COUNTER CONTROL E/F
XA228	TDD	AQ	07B	(13)	01		JXRS0B 15 07B	
XA231	TS8	D1	25B	JXXAC0	00	=		
XA231	TS8	D1	23B	(47)	01		JXR0CR JXR1CR JXR2CR JXR3CR JXR4CR JXR5CR JXR6CR JXR7CR 43 23B 46 21A 48 22A 49 26B 50 23A 52 24A 54 25A 56 26A	
XA235	TQ2	F3	35A	JXXADA	00	=		START I/O STATE COUNTER
XA235	TQ2	F3	34B	(69)	01		JXXAD0 SPI0182 65 34B 74 35B	
XA237	TD4	D1	25B	JXXAD0	00	=		
XA237	TD4	D1	26B	(47)	01		JXR0CR JXR1CR JXR2CR JXR3CR JXR4CR JXR5CR JXR6CR JXR7CR 49 26B 52 24A 54 25A 56 26A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
				JXXAOI	00	=		
XA229	TDD	A1	06A	( )	01		JXXA3P 08 06A	
				JXXA0N	00	=		
XA229	TDD	AN	07A	( )	01		J16MH0 10 07A	
XA229	TDD	AP	05B	JXXA0P	00	=		
XA229	TDD	AP	05A	(11 )	01		JXXACP 06 05A	
XA229	TDD	AQ	06B	JXXA0Q	00	=		I/O STATE COUNTER BIT 0
XA229	TDD	AQ	07B	(13 )	01		SPI0162 15 07B	
				JXXA1I	00	=		
XA230	TDD	A1	06A	( )	01		JXXA0Q 08 06A	
				JXXA1N	00	=		
XA230	TDD	AN	07A	( )	01		J16MH0 10 07A	
XA230	TDD	AP	05B	JXXA1P	00	=		
XA230	TDD	AP	05A	(11 )	01		JXXACP 06 05A	
XA230	TDD	AQ	06B	JXXA1Q	00	=		I/O STATE COUNTER BIT 1
XA230	TDD	AQ	07B	(13 )	01		SPI0162 15 07B	
				JXXA2I	00	=		
XA229	TDD	BI	03B	( )	01		JXXA1Q 05 03B	
				JXXA2N	00	=		
XA229	TDD	BN	02B	( )	01		J16MH0 01 02B	
XA229	TDD	BP	04B	JXXA2P	00	=		
XA229	TDD	BP	04A	(09 )	01		JXXACP 04 04A	
XA229	TDD	BQ	03A	JXXA2Q	00	=		I/O STATE COUNTER BIT 2
XA229	TDD	BQ	02A	(07 )	01		SPI0142 03 02A	

CONNECTOR	GROUP	TEST POINTS AND/OR	EQUATION	TERM DESIGNATOR	FACTOR	COMMENT
XA230	TDD	B1 03B	JXXA3I ( )	00 = 01	JXXA2Q 05 03B	
XA230	TDD	BN 02B	JXXA3N ( )	00 = 01	J16MH0 01 02B	
XA230	TDD	BP 04B	JXXA3P ( )	00 =		
XA230	TDD	BP 04A	JXXA3P (09 )	01	JXXACP 04 04A	
XA230	TDD	BQ 03A	JXXA3Q ( )	00 =		I/O STATE COUNTER BIT 3
XA230	TDD	BQ 02A	JXXA3Q (07 )	01	SPI0142 03 02A	
XA235	TQ2	E1 31A	JXXA4A ( )	00 =		
XA235	TQ2	E1 32A	JXXA4A (66 )	01	JXXA0Q JXXA3Q 68 32A 70 33A	
XA239	TQ2	F4 39A	JXXA40 ( )	00 =		I/O STATE COUNTER STATE 4
XA239	TQ2	F4 37A	JXXA40 (80 )	01	JXXA4A SPI0202 76 37A 78 38A	
XA235	TQ2	E2 28A	JXXA5A ( )	00 =		
XA235	TQ2	E2 29A	JXXA5A (60 )	01	JXXA0P JXXA1Q 62 29A 64 30A	
XA238	TQ2	F1 37B	JXXA50 ( )	00 =		I/O STATE COUNTER STATE 5
XA238	TQ2	F1 38B	JXXA50 (75 )	01	JXXA5A SPI0182 77 38B 79 39B	
XA235	TQ2	E3 30B	JXXA6A ( )	00 =		I/O STATE COUNTER STATE 6
XA235	TQ2	E3 28B	JXXA6A (57 )	01	JXXA1P JXXA2Q 53 28B 55 29B	
XA238	TQ2	C4 19B	JXXBCA ( )	00 =		RESET I/O BYTE COUNTER
XA238	TQ2	C4 17B	JXXBCA (39 )	01	JXXBC0 SPI0182 35 17B 37 18B	
XA232	TT3	D1 23A	JXXBC0 ( )	00 =		
XA232	TT3	D1 24A	JXXBC0 (50 )	01	JXR0MR JXREN R JXRS0B 52 24A 54 25A 56 26A	
XA227	TQ2	B1 12A	JXXBK0 ( )	00 =		I/O BYTE COUNTER CLOCK
XA227	TQ2	B1 13A	JXXBK0 (22 )	01	JXXA3Q SPI0152 24 13A 26 14A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DEFINITION	FACTOR	COMMENT
XA235	TQ2	D1	24A	JXXB0A	00	=		
XA235	TQ2	D1	25A	(52)	01		JXXB0P JXXB2P 54 25A 56 26A	
				JXXB0I	00	=		
XA228	TDD	BI	03B	( )	01		JXXB2P 05 03B	
				JXXB0N	00	=		
XA228	TDD	BN	02B	( )	01		JXXBK0 01 02B	
				JXXB0P	00	=		
XA228	TDD	BP	04A	(09)	01		JXXBCA 04 04A	
				JXXB0Q	00	=		
XA228	TDD	BQ	02A	(07)	01		SPI0142 03 02A	I/O BYTE COUNTER BIT 0
				JXXB00	00	=		
XA239	TQ2	F1	38B	(75)	01		JXXB0A SPI0202 77 38B 79 39B	I/O BYTE COUNTER STATE 0
				JXXB1A	00	=		
XA235	TQ2	D2	21A	(46)	01		JXXB0Q JXXB1P 48 22A 50 23A	
				JXXB1I	00	=		
XA229	TDD	CI	13A	( )	01		JXXB0Q 24 13A	
				JXXB1N	00	=		
XA229	TDD	CN	14A	( )	01		JXXBK0 26 14A	
				JXXB1P	00	=		
XA229	TDD	CP	12A	(23)	01		JXXBCA 22 12A	I/O BYTE COUNTER BIT 1
				JXXB1Q	00	=		
XA229	TDD	CQ	13B	(25)	01		SPI0162 27 13B	
				JXXB10	00	=		
XA239	TQ2	F2	36A	(72)	01		JXXB1A SPI0202 71 36A 73 36B	I/O BYTE COUNTER STATE 1



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA235	TQ2	D3	24B	JXXB2A	00	=		
XA235	TQ2	D3	22B	(45)	01		JXXB1Q JXXB2P 41 22B 43 23B	
				JXXB2I	00	=		
XA230	TDD	CI	13A	( )	01		JXXB1Q 24 13A	
				JXXB2N	00	=		
XA230	TDD	CN	14A	( )	01		JXXBK0 26 14A	
XA230	TDD	CP	11B	JXXB2P	00	=		
XA230	TDD	CP	12A	(23)	01		JXXBCA 22 12A	
				JXXB2Q	00	=		I/O BYTE COUNTER BIT 2
XA230	TDD	CQ	12B	(25)	01		SPI0162 27 13B	
				JXXB20	00	=		I/O BYTE COUNTER STATE 2
XA239	TQ2	F3	35A	JXXB20	00	=		
XA239	TQ2	F3	34B	(69)	01		JXXB2A SPI0202 65 34B 74 35B	
				JXXB3A	00	=		I/O BYTE COUNTER STATE 3
XA235	TQ2	D4	27B	JXXB3A	00	=		
XA235	TQ2	D4	25B	(51)	01		JXXB0Q JXXB2Q 47 25B 49 26B	
				JXXCIA	00	=		
XA226	TQ2	F2	34A	JXXCIA	00	=		
XA226	TQ2	F2	36A	(72)	01		JXXCY0 SPI0152 71 36A 73 36B	
				JXXCII	00	=		
XA229	TDD	GI	25A	( )	01		JXGN3A 54 25A	
				JXXCIN	00	=		
XA229	TDD	GN	26A	( )	01		JXXC4P 56 26A	
				JXXCIP	00	=		
XA229	TDD	GP	25B	JXXCIP	00	=		
XA229	TDD	GP	24A	(47)	01		JXRS0B 52 24A	
				JXXCIG	00	=		
XA229	TDD	GQ	26B	JXXCIG	00	=		
XA229	TDD	GQ	27B	(49)	01		JXXCIA 51 27B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
			AND	OR					
XA215	T04	F2	35A		JXXC10	00	=	INDICATOR INPUT CONTROL	
XA215	T04	F2	36A		(69)	01	JSYN1A JXDV3A JXSS0A JX0D0A 71 36A 72 34A 73 36B 74 35B		
XA225	T04	E1	31B		JXXCRO	00	=	IOU INPUT STROBE COUNT RESET	
XA225	T04	E1	32B		(59)	01	JXXCIP JXXDDP JXXDIP JXXDSP 61 32B 66 31A 68 32A 70 33A		
XA221	TQ2	F2	34A		JXXCSA	00	=	IOU INPUT STROBE COUNT STROBE	
XA221	TQ2	F2	36A		(72)	01	JXXC2Q JXXC4Q 71 36A 73 36B		
XA219	TQ2	F4	39A		JXXCS0	00	=		
XA219	TQ2	F4	37A		(80)	01	JXXCSA SPI0132 76 37A 78 38A		
					JXXCOI	00	=		
XA230	T00	G1	25A		( )	01	JXXC4P 54 25A		
					JXXCON	00	=		
XA230	T00	GN	26A		( )	01	J16M10 56 26A		
XA230	T00	GP	25B		JXXCOP	00	=		
XA230	T00	GP	24A		(47)	01	JXXCRO 52 24A		
XA230	T00	GQ	26B		JXXCOQ	00	=	IOU INPUT STROBE COUNT BIT 0	
XA230	T00	GQ	27B		(49)	01	SPI0162 51 27B		
					JXXC1I	00	=		
XA218	T00	LI	38B		( )	01	JXXCOQ 77 38B		
					JXXC1N	00	=		
XA218	T00	LN	39B		( )	01	J16M10 79 39B		
XA218	T00	LP	37A		JXXC1P	00	=		
XA218	T00	LP	37B		(76)	01	JXXCRO 75 37B		
XA218	T00	LQ	38A		JXXC1Q	00	=		
XA218	T00	LQ	39A		(78)	01	SPI0012 80 39A		

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
				JXXC2I	00 =			
XA230	TDD	HI	22A	( )	01	JXXC1Q	48 22A	
				JXXC2N	00 =			
XA230	TDD	HN	21A	( )	01	J16M10	46 21A	
XA230	TDD	HP	24B	JXXC2P	00 =			
XA230	TDD	HP	23A	(45 )	01	JXXCR0	50 23A	
XA230	TDD	HQ	23B	JXXC2Q	00 =			
XA230	TDD	HQ	22B	(43 )	01	SPI0142	41 22B	
				JXXC3I	00 =			
XA229	TDD	HI	22A	( )	01	JXXC2Q	48 22A	
				JXXC3N	00 =			
XA229	TDD	HN	21A	( )	01	J16M10	46 21A	
XA229	TDD	HP	24B	JXXC3P	00 =			
XA229	TDD	HP	23A	(45 )	01	JXXCR0	50 23A	
XA229	TDD	HQ	23B	JXXC3Q	00 =			
XA229	TDD	HQ	22B	(43 )	01	SPI0162	41 22B	
				JXXC4I	00 =			
XA228	TDD	GI	25A	( )	01	JXXC3Q	54 25A	
				JXXC4N	00 =			
XA228	TDD	GN	26A	( )	01	J16M10	56 26A	
XA228	TDD	GP	25B	JXXC4P	00 =			
XA228	TDD	GP	24A	(47 )	01	JXXCR0	52 24A	
XA228	TDD	GQ	26B	JXXC4Q	00 =			IOU INPUT STROBE COUNT BIT 4
XA228	TDD	GQ	27B	(49 )	01	SPI0162	51 27B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA226	TQ2	F4	39A	JXXDDA	00	=		
XA226	TQ2	F4	37A	( 80 )	01		JXXDDO SPI0152 76 37A 78 38A	
				JXXDDI	00	=		
XA217	TDD	LI	38B	( )	01		JXGN3A 77 38B	
				JXXDDN	00	=		
XA217	TDD	LN	39B	( )	01		JXXC4P 79 39B	
XA217	TDD	LP	37A	JXXDDP	00	=		
XA217	TDD	LP	37B	( 76 )	01		JXRS0B 75 37B	
XA217	TDD	LQ	38A	JXXDDQ	00	=		
XA217	TDD	LQ	39A	( 78 )	01		JXXDDA 80 39A	IOU INPUT DATA CONTROL F/E
XA219	TQ2	F3	35A	JXXDDO	00	=		
XA219	TQ2	F3	34B	( 69 )	01		JRTDEA JLTEOA 65 34B 74 35B	
XA226	TQ2	F3	35A	JXXDIA	00	=		
XA226	TQ2	F3	34B	( 69 )	01		JXXDIS JXEA00 65 34B 74 35B	
				JXXDII	00	=		
XA218	TDD	MI	36A	( )	01		JXGN3A 71 36A	
				JXXDIN	00	=		
XA218	TDD	MN	34A	( )	01		JXXC4P 72 34A	
XA218	TDD	MP	35A	JXXDIP	00	=		
XA218	TDD	MP	36B	( 69 )	01		JXRS0B 73 36B	
XA218	TDD	MQ	35B	JXXDIQ	00	=		
XA218	TDD	MQ	34B	( 74 )	01		JXXDIA 65 34B	IOU INPUT INTERRUPT DATA F/E
XA224	TT3	F3	39A	JXXDIR	00	=		
XA224	TT3	F3	35A	( 80 )	01		JXXDIS JXINHR JXXDIP 69 35A 76 37A 78 38A	

CONNECTOR	TEST POINTS AND/OR	EQUATION	TERM.	DESIGNATION	FACTOR	COMMENT
XA223	TQ2 F3 35A	JXXDIS	00 =			INTERRUPT WAIT FOREENABLE F/F
XA223	TQ2 F3 34B	(69)	01	JXXDIR	JINT2A 65 34B 74 35B	
XA239	TQ2 A1 05A	JXXDRA	00 =			DATA RECEIVE INHIBIT IF SEND
XA239	TQ2 A1 06A	(06)	01	JXXDRO	SPI01B2 08 06A 10 07A	
XA232	TT3 A1 04A	JXXDRO	00 =			
XA232	TT3 A1 05A	(04)	01	JXXDDP	JXXDIP JXXDSP 06 05A 08 06A 10 07A	
XA217	TDD MI 36A	JXXDSI	00 =		JXGN3A 71 36A	
XA217	TDD MN 34A	JXXDSN	00 =		JXXC4P 72 34A	
XA217	TDD MP 35A	JXXDSP	00 =		JXRS0B 73 36B	
XA217	TDD MQ 35B	JXXDSQ	00 =			IOU INPUT ITR DATAE/F
XA217	TDD MQ 34B	(74)	01	JXTRIA	65 34B	
XA224	TT3 D3 27B	JXXRCA	00 =			SET IOU REQUEST CONTROL F/F
XA224	TT3 D3 24B	(51)	01	JXXRCO	JXXREP JDEVIR 45 24B 47 25B 49 26B	
XA217	TDD JI 32A	JXXRCI	00 =		JXGN3A 68 32A	
XA217	TDD JN 33A	JXXRCN	00 =		JXXR2P 70 33A	
XA217	TDD JP 31B	JXXRCP	00 =		JXRS0B 66 31A	
XA217	TDD JQ 32B	JXXRCQ	00 =			IOU REQUEST CONTROL F/F
XA217	TDD JQ 33B	(61)	01	JXXRCA	63 33B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA215	TD4	F1	37A	JXXRC0	00	=		IQU REQUEST OR_GATE
XA215	TD4	F1	37B	( 76 )	01		JAEN1A JLPT1A JIEN1A JINT2A 75 37B 77 38B 78 38A 79 39B	
XA218	TDD	JI	32A	JXXREI	00	=		
				( )	01		JXGN3A 68 32A	
XA218	TDD	JN	33A	JXXREN	00	=		
				( )	01		JXEA0A 70 33A	
XA218	TDD	JP	31B	JXXREP	00	=		
XA218	TDD	JP	31A	( 59 )	01		JXRS0B 66 31A	
XA218	TDD	JQ	32B	JXXREQ	00	=		IQU REQUEST ENABLE/E
XA218	TDD	JQ	33B	( 61 )	01		JXXROP 63 33B	
XA230	TDD	JI	32A	JXXROI	00	=		
				( )	01		JXXR2P 68 32A	
XA230	TDD	JN	33A	JXXRON	00	=		
				( )	01		J16MH0 70 33A	
XA230	TDD	JP	31B	JXXROP	00	=		
XA230	TDD	JP	31A	( 59 )	01		JXXRCQ 66 31A	
XA230	TDD	JQ	32B	JXXROQ	00	=		IQU REQUEST COUNTER_BIT 0
XA230	TDD	JQ	33B	( 61 )	01		SPI0162 63 33B	
XA229	TDD	JI	32A	JXXRII	00	=		
				( )	01		JXXROQ 68 32A	
XA229	TDD	JN	33A	JXXR1N	00	=		
				( )	01		J16MH0 70 33A	
XA229	TDD	JP	31B	JXXR1P	00	=		
XA229	TDD	JP	31A	( 59 )	01		JXXRCQ 66 31A	

CONNECTOR	UNIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERMINATION	DESIGNATION	FACTOR	COMMENT
XA229	TDD	JQ	32B	JXXR1Q	00 =			
XA229	TDD	JQ	33B	(61)	01		SPI0162 63 33B	
XA228	TDD	JI	32A	JXXR2I	00 =		JXXR1Q 68 32A	
XA228	TDD	JN	33A	JXXR2N	00 =		J16MH0 70 33A	
XA228	TDD	JP	31B	JXXR2P	00 =			
XA228	TDD	JP	31A	(59)	01		JXXRC0 66 31A	
XA228	TDD	JQ	32B	JXXR2Q	00 =			I/O REQUEST COUNTER BIT 2
XA228	TDD	JQ	33B	(61)	01		SPI0162 63 33B	
XA235	TQ2	B2	09A	JXX04A	00 =			
XA235	TQ2	B2	10A	(14)	01		JXXB00 JXXA40 18 10A 20 11A	
XA237	TD4	F2	35A	JXX05A	00 =			I/O STATE IS 511/OBYTE IS 0
XA237	TD4	F2	36A	(69)	01		JXXB0P JXXB2P JXXA0P JXXA1Q 71 36A 72 34A 73 36B 74 35B	
XA238	TQ2	F2	34A	JXX050	00 =			
XA238	TQ2	F2	36A	(72)	01		JXX05A SPI0182 71 36A 73 36B	
XA232	TT3	C3	19B	JX0DEA	00 =			OFR DATA PARITY ERROR
XA232	TT3	C3	16B	(39)	01		JX0FRS JXROPFR JXXA50 33 16B 35 17B 37 18B	
XA223	TQ2	C4	19B	JX0DRA	00 =			OFR RESET
XA223	TQ2	C4	17B	(39)	01		JX0FRS JXXB10 35 17B 37 18B	
XA232	TT3	D3	27B	JX0D0A	00 =			OFR DATA STROBE
XA232	TT3	D3	24B	(51)	01		JX0FRS JXXB20 JXXA50 45 24B 47 25B 49 26B	
XA237	TD4	D2	24B	JX0FRR	00 =			
XA237	TD4	D2	23B	(45)	01		JX0FRS JXXB0A JXXB3A JXRS0B 43 23B 46 21A 48 22A 50 23A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA235	TQ2	C3	16B	JXOFRS	00 =			
XA235	TQ2	C3	14B	(33)	01	JXOFRR JXOROA 29 14B 31 15B		DER COMMAND F/F
XA227	TQ2	C3	16B	JXOFRO	00 =			
XA227	TQ2	C3	14B	(33)	01	JXR098T SPIO162 29 14B 31 15B		
XA227	TQ2	D4	27B	JXONLO	00 =			
XA227	TQ2	D4	25B	(51)	01	JXASLA JXBLSA 47 25B 49 26B		ON LINE CONTROL
XA225	TD4	C2	16B	JXOROA	00 =			
XA225	TD4	C2	15A	(33)	01	JXCA10 JXDEVA JXOFRO JBUSYA 30 15A 31 15B 34 16A 36 17A		COMMAND IS DER
XA230	TDD	FI	16A	JX1MAI	00 =			
XA230	TDD	FI	16A	( )	01	JX1MBP 34 16A		
XA230	TDD	FN	15A	JX1MAN	00 =			
XA230	TDD	FN	15A	( )	01	J04M20 30 15A		
XA230	TDD	FP	16B	JX1MAP	00 =			
XA230	TDD	FP	17A	(33)	01	SPIO142 36 17A		
XA230	TDD	FQ	15B	JX1MAQ	00 =			
XA230	TDD	FQ	14B	(31)	01	SPIO032 29 14B		2 PHASE CLOCK BIT 0
XA229	TDD	FI	16A	JX1MBI	00 =			
XA229	TDD	FI	16A	( )	01	JX1MAQ 34 16A		
XA229	TDD	FN	15A	JX1MBN	00 =			
XA229	TDD	FN	15A	( )	01	J04M20 30 15A		
XA229	TDD	FP	16B	JX1MBP	00 =			
XA229	TDD	FP	17A	(33)	01	SPIO032 36 17A		
XA229	TDD	FQ	15B	JX1MBQ	00 =			
XA229	TDD	FQ	14B	(31)	01	SPIO042 29 14B		2 PHASE CLOCK BIT 1



CONNECTOR	CRUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIG- NATOR	FACTOR	COMMENT
XA226	TQ2	E4	33B	JOUT0A	00	=		SEND KEYBOARD DATA TO PRINTER
XA226	TQ2	E4	31B	(63)	01		J1ENRA J1OUTR 59 31B 61 32B	
XA239	TQ2	B4	13B	J04MZ0	00	=		4 MHZ RECEIVER
XA239	TQ2	B4	11B	(27)	01		T04MHK SPI0202 23 11B 25 12B	
XA226	TQ2	E1	31A	J16MHA	00	=		16 MHZ RECEIVER
XA226	TQ2	E1	32A	(66)	01		T16MHA SPI0152 68 32A 70 33A	
XA227	TQ2	E3	30B	J16MHO	00	=		
XA227	TQ2	E3	28B	(57)	01		J16MHA SPI0162 53 28B 55 29B	
XA227	TQ2	E4	33B	J16MIO	00	=		
XA227	TQ2	E4	31B	(63)	01		J16MHA SPI0162 59 31B 61 32B	
XA113	TS8	A1	05B	KAEBRA	00	=		RESET OUTPUT COMMA
XA113	TS8	A1	02B	(11)	01		KK1290 KK08C0 KK04F0 KK03F0 KXCP3B SPI0011 SPI0031 SPI0021 01 02B 04 04A 05 03B 06 05A 07 03A 08 06A 10 07A 13 06B	
XA120	TT3	A1	04A	KAEB0A	00	=		SET OUTPUT COMMAND
XA120	TT3	A1	05A	(04)	01		KDOUTQ KXEB1Q KXCP3B 06 05A 08 06A 10 07A	
XA115	TD4	B1	11B	KAEN0A	00	=		START OUTPUT COMMA
XA115	TD4	B1	12A	(23)	01		KDOUTQ KSNC2S KBUSYS KXCP3B 22 12A 24 13A 25 12B 26 14A	
XA125	TD4	D2	24B	KAENOR	00	=		
XA125	TD4	D2	23B	(45)	01		KAENOS KAOENS KAENIA KXRSOB 43 23B 46 21A 48 22A 50 23A	
XA124	TT3	D2	23B	KAENOS	00	=		OUTPUT COUNTER BIT
XA124	TT3	D2	22B	(43)	01		KAENOR KAEN0A KAEN2A 41 22B 46 21A 48 22A	
XA123	TQ2	D2	21A	KAEN1A	00	=		
XA123	TQ2	D2	22A	(46)	01		KAEN1S KXCP3B 48 22A 50 23A	
XA120	TT3	C3	19B	KAEN1R	00	=		
XA120	TT3	C3	16B	(39)	01		KAEN1S KAEN3A KXRSOB 33 16B 35 17B 37 18B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA121	TQ2	C3	16B	KAEN1S	00	=		OUTPUT COUNTER BIT
XA121	TQ2	C3	14B	(33)	01		KAEN1R KAEN4A 29 14B 31 15B	
XA113	TS8	C1	17B	KAEN2A	00	=		
XA113	TS8	C1	15A	(35)	01		KDOUTQ KAOENS KK1290 KKPE00 KXCP3B SPI0021 SPI0011 SPI0031 30 15A 31 15B 34 16A 36 17A 37 18B 38 18A 40 19A 42 20A	
XA120	TT3	D1	23A	KAEN2R	00	=		
XA120	TT3	D1	24A	(50)	01		KAEN2S KAEN5A KXRSOB 52 24A 54 25A 56 26A	
XA121	TQ2	D1	24A	KAEN2S	00	=		OUTPUT COUNTER BIT
XA121	TQ2	D1	25A	(52)	01		KAEN2R KXED0A 54 25A 56 26A	
XA122	TQ2	C1	18A	KAEN3A	00	=		
XA122	TQ2	C1	19A	(38)	01		KAENOR KXCP1B 40 19A 42 20A	
XA120	TT3	D2	23B	KAEN3R	00	=		
XA120	TT3	D2	22B	(43)	01		KAEN3S KAEN7A KXRSOB 41 22B 46 21A 48 22A	
XA121	TQ2	D2	21A	KAEN3S	00	=		OUTPUT COUNTER BIT
XA121	TQ2	D2	22A	(46)	01		KAEN3R KAEN6A 48 22A 50 23A	
XA122	TQ2	C2	15A	KAEN4A	00	=		
XA122	TQ2	C2	16A	(30)	01		KAENOS KXCP1B 34 16A 36 17A	
XA122	TQ2	E2	28A	KAEN5A	00	=		
XA122	TQ2	E2	29A	(60)	01		KAEN3S KXCP3B 62 29A 64 30A	
XA122	TQ2	C3	16B	KAEN6A	00	=		
XA122	TQ2	C3	14B	(33)	01		KAEN2S KXCP1B 29 14B 31 15B	
XA122	TQ2	C4	19B	KAEN7A	00	=		
XA122	TQ2	C4	17B	(39)	01		KAEN2R KXCP1B 35 17B 37 18B	
XA120	TT3	C1	17A	KAEOBR	00	=		
XA120	TT3	C1	18A	(36)	01		KAEOBS KAEBRA KXRSOB 38 18A 40 19A 42 20A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA121	TQ2	C1	18A	KAE0BS	00	=		EOB RECEIVED ON OU
XA121	TQ2	C1	19A	(38)	01		KAE0BR KAE0A 40 19A 42 20A	
XA125	TD4	E2	30B	KAIENR	00	=		
XA125	TD4	E2	29B	(57)	01		KAIENS KAIERA KETX1A KXRSOB 55 29B 60 28A 62 29A 64 30A	
XA123	TQ2	E3	30B	KAIENS	00	=		AUTO INPUT MODE EN
XA123	TQ2	E3	28B	(57)	01		KAIENR KAINOA 53 28B 55 29B	
XA124	TT3	F1	36B	KAIERA	00	=		RESET AUTO INPUT
XA124	TT3	F1	37B	(73)	01		KDAINQ KXEB1Q KXCP3B 75 37B 77 38B 79 39B	
XA125	TD4	F1	37A	KAINOA	00	=		START INPUT COMMAN
XA125	TD4	F1	37B	(76)	01		KDAINQ KSNC2S KBUSYS KXCP3B 75 37B 77 38B 78 38A 79 39B	
XA120	TT3	C2	15B	KAOENR	00	=		
XA120	TT3	C2	14B	(31)	01		KAOENS KAOERA KXRSOB 29 14B 30 15A 34 16A	
XA121	TQ2	C2	15A	KAOENS	00	=		AUTO OUTPUT MODE
XA121	TQ2	C2	16A	(30)	01		KAOENR KAENOA 34 16A 36 17A	
XA113	TS8	B1	11B	KACERA	00	=		RESET AUTO OUTPUT
XA113	TS8	B1	09A	(23)	01		KAE0BS KK1290 KK08C0 KK04F0 KK03E0 KXCP3B SPI0021 SPI0011 14 09A 18 10A 19 09B 20 11A 22 12A 24 13A 25 12B 26 14A	
XA115	TD4	B2	10B	KBSYOA	00	=		SET HARDWARE BUSY
XA115	TD4	B2	09A	(21)	01		KSNC1S SPI0021 KXCP1B SPI0011 14 09A 18 10A 19 09B 20 11A	
XA123	TQ2	D4	27B	KBUSYA	00	=		HARDWARE BUSY WHEN
XA123	TQ2	D4	25B	(51)	01		KPBZY0 SPI0151 47 25B 49 26B	
XA124	TT3	B3	13B	KBUSYR	00	=		
XA124	TT3	B3	10B	(27)	01		KBUSYS KINT1A KXRSOB 21 10B 23 11B 25 12B	
XA123	TQ2	B3	10B	KBUSYS	00	=		HARDWARE BUSY F/E
XA123	TQ2	B3	08B	(21)	01		KBUSYR KBSYOA 17 08B 19 09B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGN RANGE	FACTOR	COMMENT
				KCDERI	00 =			
XA117	TDD	EI	19A	( )	01		SPI0011 40 19A	
				KCDERN	00 =			
XA117	TDD	EN	20A	( )	01		SPI0021 42 20A	
XA117	TDD	EP	17B	KCDERP	00 =			
XA117	TDD	EP	18A	(35 )	01		KRS0A 38 18A	
XA117	TDD	EQ	18B	KCDERQ	00 =			COMPUTER DATA PART
XA117	TDD	EQ	19B	(37 )	01		KCDESA 39 19B	
XA121	TQ2	F1	37B	KCDESA	00 =			
XA121	TQ2	F1	38B	(75 )	01		KCDESO SPI0131 77 38B 79 39B	
XA122	TQ2	F2	34A	KCDESO	00 =			SET COMPUTER DATA
XA122	TQ2	F2	36A	(72 )	01		KXDPEA KX0DEA 71 36A 73 36B	
				KDAINI	00 =			
XA130	TDD	EI	19A	( )	01		KDAINO 40 19A	
				KDAINN	00 =			
XA130	TDD	EN	20A	( )	01		KXDV10 42 20A	
XA130	TDD	EP	17B	KDAINP	00 =			
XA130	TDD	EP	18A	(35 )	01		KXRS0B 38 18A	
XA130	TDD	EQ	18B	KDAINQ	00 =			INPUT COMMAND
XA130	TDD	EQ	19B	(37 )	01		SPI0161 39 19B	
XA126	TQ2	B3	10B	KDAINO	00 =			
XA126	TQ2	B3	08B	(21 )	01		KXRAF5T SPI0151 17 08B 19 09B	
XA126	TQ2	E2	28A	KDCP00	00 =			DATA REGISTER CLOC
XA126	TQ2	E2	29A	(60 )	01		KXED0A KX0D0A 62 29A 64 30A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA126	TQ2	E3	30B	KDCP10	00	=		DATA REGISTER CLOC
XA126	TQ2	E3	28B	(57)	01		KXED0A KX0D0A 53 28B 55 29B	
XA122	TQ2	D3	24B	KDEVIR	00	=		
XA122	TQ2	D3	22B	(45)	01		KDEVIS KXR50B 41 22B 43 23B	
XA121	TQ2	D3	24B	KDEVIS	00	=		END INPUT COMND RE
XA121	TQ2	D3	22B	(45)	01		KDEVIR KDEV0A 41 22B 43 23B	
XA119	TQ2	D4	27B	KDEV0A	00	=		
XA119	TQ2	D4	25B	(51)	01		KDEV1Q KXCP3B 47 25B 49 26B	
				KDEV0I	00	=		
XA117	TDD	D1	10A	( )	01		KXGN2A 18 10A	
				KDEVON	00	=		
XA117	TDD	DN	09A	( )	01		KDEV0A 14 09A	
XA117	TDD	DP	10B	KDEVOP	00	=		
XA117	TDD	DP	11A	(21)	01		KXR50B 20 11A	
XA117	TDD	DQ	09B	KDEV0Q	00	=		END INPUT COUNTER
XA117	TDD	DQ	08B	(19)	01		KXDV3A 17 08B	
XA121	TQ2	D4	27B	KDEV1A	00	=		
XA121	TQ2	D4	25B	(51)	01		KDEV3S KXCP3B 47 25B 49 26B	
				KDEV1I	00	=		
XA118	TDD	D1	10A	( )	01		KDEV0Q 18 10A	
				KDEV1N	00	=		
XA118	TDD	DN	09A	( )	01		KXCP1B 14 09A	
XA118	TDD	DP	10B	KDEV1P	00	=		
XA118	TDD	DP	11A	(21)	01		KXR50B 20 11A	

CONNECTOR	CONNECTOR GROUP	TEST POINTS AND/OR	EQUATION	TERM. DESIGNATOR	FACTOR	COMMENT
XA118	TDD	DQ 09B	KDEV1Q	00 =		END INPUT COUNTER
XA118	TDD	DQ 08B	(19 )	01	SPI0021 17 08B	
XA124	TT3	C3 19B	KDEV2A	00 =		
XA124	TT3	C3 16B	(39 )	01	KDEV2S KXXREP KXCP1B 33 16B 35 17B 37 18B	
XA120	TT3	D3 27B	KDEV2R	00 =		
XA120	TT3	D3 24B	(51 )	01	KDEV2S KDEV1A KXRSOB 45 24B 47 25B 49 26B	
XA122	TQ2	D1 24A	KDEV2S	00 =		END INPUT COUNTER
XA122	TQ2	D1 25A	(52 )	01	KDEV2R KDEVOA 54 25A 56 26A	
XA121	TQ2	C4 19B	KDEV3A	00 =		
XA121	TQ2	C4 17B	(39 )	01	KDEV2R KXCP1B 35 17B 37 18B	
XA120	TT3	E1 30A	KDEV3R	00 =		
XA120	TT3	E1 31A	(64 )	01	KDEV3S KDEV3A KXRSOB 66 31A 68 32A 70 33A	
XA122	TQ2	D2 21A	KDEV3S	00 =		END INPUT COUNTER
XA122	TQ2	D2 22A	(46 )	01	KDEV3R KDEV2A 48 22A 50 23A	
XA114	TS8	B1 11B	KDP7RA	00 =		RESET DATA REG. ON
XA114	TS8	B1 09A	(23 )	01	KDAINQ KAIENS KKTCCP KK0870 KK0485U KK1200 KXCP3B SPI0011 14 09A 18 10A 19 09B 20 11A 22 12A 24 13A 25 12B 26 14A	
XA114	TS8	C1 17B	KDP7SA	00 =		LOAD DATA REGISTER
XA114	TS8	C1 15A	(35 )	01	KDAINQ KAIENS KEYINO KKTCCP KK0870 KK0485U KXCP3B SPI0011 30 15A 31 15B 34 16A 36 17A 37 18B 38 18A 40 19A 42 20A	
XA122	TQ2	B4 13B	KDP7S0	00 =		
XA122	TQ2	B4 11B	(27 )	01	KDP7SA SPI0131 23 11B 25 12B	
XA127	TQ2	F1 37B	KDRS0A	00 =		
XA127	TQ2	F1 38B	(75 )	01	KDRS00 SPI0161 77 38B 79 39B	
XA124	TT3	E3 33B	KDRS00	00 =		RESET DATA REGISTE
XA124	TT3	E3 30B	(63 )	01	KDP7RA KXODRA KXRSOB 57 30B 59 31B 61 32B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA127	TQ2	F2	34A	KDRS1A	00	=		
XA127	TQ2	F2	36A	(72)	01		KDRS00 SPI0161 71 36A 73 36B	
				KDOPBI	00	=		
XA130	TDD	KI	29A	( )	01		KXRPCS 62 29A	
				KDOPBN	00	=		
XA130	TDD	KN	28A	( )	01		KDCP00 60 28A	
XA130	TDD	KP	30B	KDOPBP	00	=		
XA130	TDD	KP	30A	(57)	01		KDRS0A 64 30A	
XA130	TDD	KQ	29B	KDOPBQ	00	=		DATA REGISTER BIT F
XA130	TDD	KQ	28B	(55)	01		KDOPSA 53 28B	
XA121	TQ2	B4	13B	KDOPSA	00	=		DATA REGISTER SET
XA121	TQ2	B4	11B	(27)	01		KDP750 KK1280 23 11B 25 12B	
				KDOUTI	00	=		
XA128	TDD	FI	16A	( )	01		KDOUT0 34 16A	
				KDOUTN	00	=		
XA128	TDD	FN	15A	( )	01		KXDV10 30 15A	
XA128	TDD	FP	16B	KDOUTP	00	=		
XA128	TDD	FP	17A	(33)	01		KXRS0B 36 17A	
XA128	TDD	FQ	15B	KDOUTQ	00	=		OUTPUT COMMAND F/E
XA128	TDD	FQ	14B	(31)	01		SPI0141 29 14B	
XA126	TQ2	B4	13B	KDOUT0	00	=		
XA126	TQ2	B4	11B	(27)	01		KXRAF7T SPI0151 23 11B 25 12B	
				KD00BI	00	=		
XA130	TDD	LI	38B	( )	01		KXROCS 77 38B	

CONNECTOR	GROUP TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERMINAL	DESIGNATION	FACTOR	COMMENT
				KD00BN	00 =			
XA130	TDD	LN	39B	( )	01		KDCP00 79 39B	
XA130	TDD	LP	37A	KD00BP	00 =			
XA130	TDD	LP	37B	(76 )	01		KDRSOA 75 37B	
XA130	TDD	LQ	38A	KD00BQ	00 =			
XA130	TDD	LQ	39A	(78 )	01		SPI0161 80 39A	DATA REGISTER BIT 0
				KD01BI	00 =			
XA130	TDD	MY	36A	( )	01		KXRICS 71 36A	
				KD01BN	00 =			
XA130	TDD	MN	34A	( )	01		KDCP00 72 34A	
XA130	TDD	MP	35A	KD01BP	00 =			
XA130	TDD	MP	36B	(69 )	01		KDRSOA 73 36B	
XA130	TDD	MQ	35B	KD01BQ	00 =			
XA130	TDD	MQ	34B	(74 )	01		KD01SA 65 34B	
XA121	TQ2	A1	05A	KD01SA	00 =			
XA121	TQ2	A1	06A	(06 )	01		KDP750 KK1270 08 06A 10 07A	
				KD02BI	00 =			
XA129	TDD	KI	29A	( )	01		KXR2CS 62 29A	
				KD02BN	00 =			
XA129	TDD	KN	28A	( )	01		KDCP00 60 28A	
XA129	TDD	KP	30B	KD02BP	00 =			
XA129	TDD	KP	30A	(57 )	01		KDRSOA 64 30A	
XA129	TDD	KQ	29B	KD02BQ	00 =			
XA129	TDD	KQ	28B	(55 )	01		KD02SA 53 28B	



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIG. FACTOR	FACTOR	COMMENT
XA121	TQ2	A2	02B	KD02SA	00	=		
XA121	TQ2	A2	04A	(01)	01		KDP7S0 KK1260 04 04A 05 03B	
				KD03B1	00	=		
XA129	TDD	LI	38B	( )	01		KXR3CS 77 38B	
				KD03RN	00	=		
XA129	TDD	LN	39B	( )	01		KDCP00 79 39B	
XA129	TDD	LP	37A	KD03BP	00	=		
XA129	TDD	LP	37B	(76)	01		KDR50A 75 37B	
XA129	TDD	LQ	38A	KD03BQ	00	=		
XA129	TDD	LQ	39A	(78)	01		KD03SA 80 39A	
XA121	TQ2	A3	04B	KD03SA	00	=		
XA121	TQ2	A3	02A	(09)	01		KDP7S0 KK1250 03 02A 07 03A	
				KD04B1	00	=		
XA129	TDD	MI	36A	( )	01		KXR4CS 71 36A	
				KD04BN	00	=		
XA129	TDD	MN	34A	( )	01		KDCP10 72 34A	
XA129	TDD	MP	35A	KD04BP	00	=		
XA129	TDD	MP	36B	(69)	01		KDR51A 73 36B	
XA129	TDD	MQ	35B	KD04BQ	00	=		
XA129	TDD	MQ	34B	(74)	01		KD04SA 65 34B	
XA121	TQ2	A4	07B	KD04SA	00	=		
XA121	TQ2	A4	05B	(15)	01		KDP7S0 KK1240 11 05B 13 06B	
				KD05B1	00	=		
XA128	TDD	KI	29A	( )	01		KXR5CS 62 29A	

CONNECTOR	CIRCUIT TYPE	REG. NO.	TEST POINTS AND/OR	EQUATION	TERMINAL	DESIGNATOR	FACTOR	COMMENT
XA128	TDD	KN	28A	KD05BN ( )	00 =	01	KDCP10 60 28A	
XA128	TDD	KP	30B	KD05BP ( )	00 =	01	KDRS1A 64 30A	
XA128	TDD	KQ	29B	KD05BQ ( )	00 =	01	KD05SA 53 28B	
XA121	TQ2	B1	12A	KD05SA ( )	00 =	01	KDP7S0 KK1230 24 13A 26 14A	
XA128	TDD	LI	38B	KD06BI ( )	00 =	01	KXR6CS 77 38B	
XA128	TDD	LN	39B	KD06BN ( )	00 =	01	KDCP10 79 39B	
XA128	TDD	LP	37A	KD06BP ( )	00 =	01	KDRS1A 75 37B	
XA128	TDD	LQ	38A	KD06BQ ( )	00 =	01	KD06SA 80 39A	
XA121	TQ2	B2	09A	KD06SA ( )	00 =	01	KDP7S0 KK1220 18 10A 20 11A	
XA128	TDD	MI	36A	KD07BI ( )	00 =	01	KXR7CS 71 36A	
XA128	TDD	MN	34A	KD07BN ( )	00 =	01	KDCP10 72 34A	
XA128	TDD	MP	35A	KD07BP ( )	00 =	01	KDRS1A 73 36B	

CONNECTOR	CONNECTOR TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERMINAL	DESIGNATOR	FACTOR	COMMENT
XA128	TDD	MQ	35B	KD07BQ	00	=		DATA REGISTER BIT 7
XA128	TDD	MQ	34B	(74)	01		KD07SA 65 34B	
XA121	TQ2	B3	10B	KD07SA	00	=		DATA REGISTER SET
XA121	TQ2	B3	08B	(21)	01		KDP750 KK1210 17 08B 19 09B	
XA139	TQ2	B2	09A	KENINO	00	=		
XA139	TQ2	B2	10A	(14)	01		KXRAF6T SPI0201 18 10A 20 11A	
XA114	TS8	D1	25B	KETX0A	00	=		EXT CHARACTER IS
XA114	TS8	D1	23B	(47)	01		KD0PBQ KD01BP KD02BP KD03BP KD04BP KD05BP KD06BQ KD07BQ 43 23B 46 21A 48 22A 49 26B 50 23A 52 24A 54 25A 56 26A	
XA119	TQ2	E1	31A	KETX00	00	=		
XA119	TQ2	E1	32A	(66)	01		KETX0A SPI0131 68 32A 70 33A	
XA120	TT3	E2	29B	KETX1A	00	=		EXT CHARACTER STRO
XA120	TT3	E2	28B	(55)	01		KIENIS KETX00 KXCP3B 53 28B 60 28A 62 29A	
XA143	DCF	D5	38B	KEYINDX	00	=		KEYBOARD INPUT DATA RECEIVER
XA143	DCF	D5	36A	(80)	01		KXGN1A 72 36A	
XA138	TQ2	B3	10B	KEYINO	00	=		
XA138	TQ2	B3	08B	(21)	01		KEYINOX SPI0181 17 08B 19 09B	
XA143	DCF	D6	38A	KEYINOX	00	=		
XA143	DCF	D6	37A	(76)	01		KXGN2A 74 37A	
XA123	TQ2	E4	33B	KIENRA	00	=		RESET INPUT DATA
XA123	TQ2	E4	31B	(63)	01		KIENRO SPI0151 59 31B 61 32B	
XA122	TQ2	E3	30B	KIENRO	00	=		
XA122	TQ2	E3	28B	(57)	01		KAIENS KEYINOX 53 28B 55 29B	
XA124	TT3	E1	30A	KIENSA	00	=		START MAIN TIME CO
XA124	TT3	E1	31A	(64)	01		KAIENS KIEN0Q KIEN1P 66 31A 68 32A 70 33A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DECODE	FACTOR	COMMENT
XA114	TSB	F1	37A	KIENOA	00 =			
XA114	TSB	F1	36A	(76)	01		KDAINQ KAIENS KK1290 KK08B3U KXCP3B SPI0021 SPI0011 SPI0031 71 36A 72 34A 73 36B 74 35B 75 37B 77 38B 78 38A 79 39B	START INPUT COUNT
				KIENOI	00 =			
XA117	TDD	KI	29A	( )	01		SPI0011 62 29A	
				KIENON	00 =			
XA117	TDD	KN	28A	( )	01		K16MIO 60 28A	
XA117	TDD	KP	30B	KIENOP	00 =			
XA117	TDD	KP	30A	(57)	01		KIENRA 64 30A	
XA117	TDD	KQ	29B	KIENQQ	00 =			
XA117	TDD	KQ	28B	(55)	01		SPI0021 53 28B	DATA DETECT COUNT
XA125	TD4	D1	25B	KIENOR	00 =			
XA125	TD4	D1	26B	(47)	01		KIENOS KIENIA KAIENS KXRSOB 49 26B 52 24A 54 25A 56 26A	
XA123	TQ2	D1	24A	KIENOS	00 =			
XA123	TQ2	D1	25A	(52)	01		KIENOR KIENOA 54 25A 56 26A	INPUT COUNTER BIT
XA122	TQ2	D4	27B	KIENIA	00 =			
XA122	TQ2	D4	25B	(51)	01		KIENIS KXCP3B 47 25B 49 26B	
				KIENII	00 =			
XA118	TDD	KI	29A	( )	01		KIENQQ 62 29A	
				KIENIN	00 =			
XA118	TDD	KN	28A	( )	01		K16MIO 60 28A	
XA118	TDD	KP	30B	KIENIP	00 =			
XA118	TDD	KP	30A	(57)	01		KIENRA 64 30A	
XA118	TDD	KQ	29B	KIENIQ	00 =			
XA118	TDD	KQ	28B	(55)	01		SPI0011 53 28B	DATA DETECT COUNT

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	SERIAL FACTOR	FACTOR	COMMENT
			AND	OR					
XA124	TT3	B1	11A		KIEN1R	00	=		
XA124	TT3	B1	12A		(20 )	01		KIEN1S KIEN3A KXRS0B 22 12A 24 13A 26 14A	
XA123	TQ2	B1	12A		KIEN1S	00	=		INPUT COUNTER BIT
XA123	TQ2	B1	13A		(22 )	01		KIEN1R KIEN2A 24 13A 26 14A	
XA122	TQ2	A3	04B		KIEN2A	00	=		
XA122	TQ2	A3	02A		(09 )	01		KIEN0S KXCPIB 03 02A 07 03A	
XA121	TQ2	E4	33B		KIEN3A	00	=		
XA121	TQ2	E4	31B		(63 )	01		KIEN0R KXCPIB 59 31B 61 32B	
XA543	TLD	F4	39A		KIFOND4	00	=		
XA543	TLD	F4	37A		(80 )	01		SPI028 SPI029 76 37A 78 38A	
XA122	TQ2	F1	37B		KINT1A	00	=		
XA122	TQ2	F1	38B		(75 )	01		KINT10 SPI0131 77 38B 79 39B	
XA113	TS8	F1	37A		KINT10	00	=		END OF DEV COMMAND
XA113	TS8	F1	36A		(76 )	01		KSC11A KETX1A KAIERA KPINTA SPI0011 SPI0021 SPI0031 SPI0041 71 36A 72 34A 73 36B 74 35B 75 37B 77 38B 78 38A 79 39B	
XA122	TQ2	B2	09A		KINT2A	00	=		
XA122	TQ2	B2	10A		(14 )	01		KINT10 SPI0131 18 10A 20 11A	
XA131	TS8	F1	37A		KIOTRA	00	=		
XA131	TS8	F1	36A		(76 )	01		KAIENS KK1290 KK08C0 KK04F0 KK03E0 KXCP3B SPI0161 SPI0141 71 36A 72 34A 73 36B 74 35B 75 37B 77 38B 78 38A 79 39B	
XA124	TT3	E2	29B		KIOUTR	00	=		
XA124	TT3	E2	28B		(55 )	01		KIOUTS KXEAOA KXRS0B 53 28B 60 28A 62 29A	
XA123	TQ2	E2	28A		KIOUTS	00	=		PRINT INHIBIT F/E
XA123	TQ2	E2	29A		(60 )	01		KIOUTR KIEN1A 62 29A 64 30A	
XA135	TQ2	F2	34A		KKPEOA	00	=		DIVIDE BY 13 PARRALLEL ENTRY
XA135	TQ2	F2	36A		(72 )	01		KK08C0 KK04B5U 71 36A 73 36B	

CONNECTOR	SUBCPT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	RELATION	FACTOR	COMMENT
XA122	TQ2	E1	31A	KKPE00	00	=		
XA122	TQ2	E1	32A	(66)	01		KKPE0A SPI0131 68 32A 70 33A	
XA121	TQ2	E1	31A	KKPE1A	00	=		
XA121	TQ2	E1	32A	(66)	01		KK1290 KKPE00 68 32A 70 33A	DIVIDE BY 10 PARAL
XA117	TDD	CI	13A	KKTCCI	00	=		
				( )	01		SPI0011 24 13A	
XA117	TDD	CN	14A	KKTCCN	00	=		
				( )	01		KK15QA 26 14A	
XA117	TDD	CP	11B	KKTCCP	00	=		
XA117	TDD	CP	12A	(23)	01		KKTCRA 22 12A	
XA117	TDD	CQ	12B	KKTCCQ	00	=		
XA117	TDD	CQ	13B	(25)	01		KKTC1A 27 13B	MAIN TIMING COUNT
XA127	TQ2	F4	39A	KKTCRA	00	=		
XA127	TQ2	F4	37A	(80)	01		KKTCRO SPI0161 76 37A 78 38A	START MAIN TIMING COUNTER
XA132	TT3	F1	36B	KKTCRO	00	=		
XA132	TT3	F1	37B	(73)	01		KAEN5A KIENSA SPI0172 75 37B 77 38B 79 39B	
XA119	TQ2	A4	07B	KKTC1A	00	=		
XA119	TQ2	A4	05B	(15)	01		KKTC10 SPI0021 11 05B 13 06B	
XA120	TT3	B3	13B	KKTC10	00	=		
XA120	TT3	B3	10B	(27)	01		KIEN0A KXRS0B SPI0021 21 10B 23 11B 25 12B	
XA128	TDD	HI	22A	KK00BI	00	=		
				( )	01		KK0090 48 22A	
XA128	TDD	HN	21A	KK00BN	00	=		
				( )	01		KXCP1B 46 21A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA128	TDD	HP	24B	KK008P	00	=		
XA128	TDD	HP	23A	(45)	01	=	KKTCPP 50 23A	
XA128	TDD	HQ	23B	KK008Q	00	=		
XA128	TDD	HQ	22B	(43)	01	=	SPI005I 41 22B	
XA135	TQ2	F4	39A	KK0090	00	=		
XA135	TQ2	F4	37A	(80)	01	=	KK009A1 KK008Q 76 37A 78 38A	
XA133	DBC	C1	18A	KK03B1U	00	=		
XA133	DBC	C1	18B	(38)	01	=	SPI014I SPI018I SPI005I SPI004I SPI017I 39 18B 41 19B 43 22B 45 23B 50 24A	BITS 0-1-2-3 OF MAIN TIMER
XA133	DBC	C2	19A	KK03B2U	00	=		
XA133	DBC	C2	17B	(40)	01	=	KXCP1B 37 17B	
XA133	DBC	C3	20A	KK03B3U	00	=		
XA133	DBC	C3	23A	(42)	01	=	SPI020I 47 23A	
XA133	DBC	C4	21A	KK03B4U	00	=		
XA133	DBC	C4	22A	(46)	01	=	KK008Q 48 22A	
XA133	DBC	C5	17A	KK03B5U	00	=		
XA133	DBC	C5	16B	(36)	01	=	KKYCCP 35 16B	
XA119	TQ2	A1	05A	KK03D0	00	=		
XA119	TQ2	A1	06A	(06)	01	=	KK0305T SPI0011 08 06A 10 07A	STATE D OF KK03 BI
XA119	TQ2	A2	02B	KK03E0	00	=		
XA119	TQ2	A2	04A	(01)	01	=	KK0306T SPI0011 04 04A 05 03B	STATE E OF KK03 BI
XA119	TQ2	A3	04B	KK03F0	00	=		
XA119	TQ2	A3	02A	(09)	01	=	KK0307T SPI0011 03 02A 07 03A	STATE F OF KK03 BI
XA122	TQ2	E4	33B	KK03QA	00	=		
XA122	TQ2	E4	31B	(63)	01	=	KK03B4U SPI0131 59 31B 61 32B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
			AND	OR					
XA134	DBC	B1	12A		KK0300T	00	=	KK03 BITS STATE DECODER	
XA134	DBC	B1	10A		(24)	01		KK03B1U 20 10A	
XA134	DBC	B2	13A		KK0301T	00	=		
XA134	DBC	B2	11A		(26)	01		KK03B2U 22 11A	
XA134	DBC	B3	14A		KK0302T	00	=		
XA134	DBC	B3	09B		(27)	01		KK03B3U 19 09B	
XA134	DBC	B4	15A		KK0303T	00	=		
XA134	DBC	B4	10B		(30)	01		KK030A 21 10B	
XA134	DBC	B5	16A		KK0304T	00	=		
XA134	DBC	B5			(33)	01		SPA 4T	
XA134	DBC	B6	11B		KK0305T	00	=		
XA134	DBC	B6			(23)	01		SPA 5T	
XA134	DBC	B7	12B		KK0306T	00	=		
XA134	DBC	B7			(25)	01		SPA 6T	
XA134	DBC	B8	13B		KK0307T	00	=		
XA134	DBC	B8			(29)	01		SPA 7T	
XA134	DBC	B9	14B		KK0308T	00	=		
XA134	DBC	B9			(31)	01		SPA 8T	
XA134	DBC	B0	15B		KK0309T	00	=		
XA134	DBC	B0			(34)	01		SPA 9T	
XA134	DBC	C1	18A		KK0481U	00	=		BITS 4-5-6-7 OF MAIN TIMER
XA134	DBC	C1	18B		(38)	01		SPI0181 SPI0051 SPI0041 SPI0141 SPI0171 39 18B 41 19B 43 22B 45 23B 50 24A	
XA134	DBC	C2	19A		KK0482U	00	=		
XA134	DBC	C2	17B		(40)	01		KXCP1B 37 17B	



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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA134	DBC	C3	20A	KK04B3U	00	=		
XA134	DBC	C3	23A	(42)	01	=	SPI0191 47 23A	
XA134	DBC	C4	21A	KK04B4U	00	=		
XA134	DBC	C4	22A	(46)	01	=	KK03B5U 48 22A	
XA134	DBC	C5	17A	KK04B5U	00	=		
XA134	DBC	C5	16B	(36)	01	=	KKTCCP 35 16B	
XA115	TD4	A2	04B	KK04FA	00	=		KK04 STATE F
XA115	TD4	A2	02B	(09)	01	=	KK04B4U KK04B3U KK04B2U KK04B1U 01 02B 04 04A 05 03B 07 03A	
XA123	TQ2	A4	07B	KK04F0	00	=		
XA123	TQ2	A4	05B	(15)	01	=	KK04FA SPI0151 11 05B 13 06B	
XA133	DBC	D1	26A	KK08B1U	00	=		BITS 8-9-10-11 OF MAIN TIMER
XA133	DBC	D1	26B	(54)	01	=	KXGN1A KXGN2A KXGN3A KXGN4A KKPE0A 53 26B 55 27B 57 29B 59 30B 63 31A	
XA133	DBC	D2	28B	KK08B2U	00	=		
XA133	DBC	D2	25B	(56)	01	=	KXCP1B 51 25B	
XA133	DBC	D3	28A	KK08B3U	00	=		
XA133	DBC	D3	31B	(60)	01	=	SPI0181 61 31B	
XA133	DBC	D4	29A	KK08B4U	00	=		
XA133	DBC	D4	30A	(62)	01	=	KK04B5U 64 30A	
XA133	DBC	D5	25A	KK08B5U	00	=		
XA133	DBC	D5	24B	(52)	01	=	KKTCCP 49 24B	
XA115	TD4	A1	05B	KK08CA	00	=		KK08 STATE C
XA115	TD4	A1	05A	(11)	01	=	KK08B4U KK08B3U KK09QA KK08QA 06 05A 08 06A 10 07A 13 06B	
XA122	TQ2	A2	02B	KK08CC	00	=		
XA122	TQ2	A2	04A	(01)	01	=	KK08CA SPI0131 04 04A 05 03B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATION	FACTOR	COMMENT
XA119	TQ2	E2	28A	KK08QA	00	=		
XA119	TQ2	E2	29A	(60)	01		KK08B1U SPI0131 62 29A 64 30A	
XA124	TT3	D1	23A	KK087A	00	=		KK08 BITS STATE 7
XA124	TT3	D1	24A	(50)	01		KK08B3U KK08B2U KK08B1U 52 24A 54 25A 56 26A	
XA123	TQ2	E1	31A	KK0870	00	=		
XA123	TQ2	E1	32A	(66)	01		KK087A SPI0151 68 32A 70 33A	
XA119	TQ2	E3	30B	KK09QA	00	=		
XA119	TQ2	E3	28B	(57)	01		KK08B2U SPI0131 53 28B 55 29B	
XA134	DBC	D1	26A	KK12B1U	00	=		BITS 12-13-14-15 OF MAIN TIME
XA134	DBC	D1	26B	(54)	01		KXGN1A KXGN2A KXGN3A KXGN4A KKPE1A 53 26B 55 27B 57 29B 59 30B 63 31A	
XA134	DBC	D2	28B	KK12B2U	00	=		
XA134	DBC	D2	25B	(56)	01		KXCP1B 51 25B	
XA134	DBC	D3	28A	KK12B3U	00	=		
XA134	DBC	D3	31B	(60)	01		SPI0181 61 31B	
XA134	DBC	D4	29A	KK12B4U	00	=		
XA134	DBC	D4	30A	(62)	01		KKFE00 64 30A	
XA134	DBC	D5	25A	KK12B5U	00	=		
XA134	DBC	D5	24B	(52)	01		KKTCCP 49 24B	
XA119	TQ2	B1	12A	KK1200	00	=		KK12 STATE 0
XA119	TQ2	B1	13A	(22)	01		KK1200T SPI0011 24 13A 26 14A	
XA133	DBC	A1	04A	KK1200T	00	=		KK12 BITS STATE DECODER
XA133	DBC	A1	02A	(08)	01		KK12B1U 04 02A	
XA133	DBC	A2	05A	KK1201T	00	=		
XA133	DBC	A2	03A	(10)	01		KK12B2U 06 03A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESIG. FACTOR	FACTOR	COMMENT
			AND	OR					
XA133	DBC	A3	06A		KK1202T	00	=		
XA133	DBC	A3	02B		(14)	01		KK12B3U 03 02B	
XA133	DBC	A4	07A		KK1203T	00	=		
XA133	DBC	A4	03B		(13)	01		KK12B4U 05 03B	
XA133	DBC	A5	08B		KK1204T	00	=		
XA133	DBC	A5			(17)	01		SPA 4T	
XA133	DBC	A6	04B		KK1205T	00	=		
XA133	DBC	A6			(07)	01		SPA 5T	
XA133	DBC	A7	05B		KK1206T	00	=		
XA133	DBC	A7			(09)	01		SPA 6T	
XA133	DBC	A8	06B		KK1207T	00	=		
XA133	DBC	A8			(11)	01		SPA 7T	
XA133	DBC	A9	07B		KK1208T	00	=		
XA133	DBC	A9			(15)	01		SPA 8T	
XA133	DBC	A0	09A		KK1209T	00	=		
XA133	DBC	A0			(18)	01		SPA 9T	
XA119	TQ2	B2	09A		KK1210	00	=		KK12 STATE 1
XA119	TQ2	B2	10A		(14)	01		KK1201T SPI0131 18 10A 20 11A	
XA119	TQ2	B3	10B		KK1220	00	=		KK12 STATE 2
XA119	TQ2	B3	08B		(21)	01		KK1202T SPI0131 17 08B 19 09B	
XA119	TQ2	B4	13B		KK1230	00	=		KK12 STATE 3
XA119	TQ2	B4	11B		(27)	01		KK1203T SPI0131 23 11B 25 12B	
XA119	TQ2	C1	18A		KK1240	00	=		KK12 STATE 4
XA119	TQ2	C1	19A		(38)	01		KK1204T SPI0131 40 19A 42 20A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM DESIGNATION	FACTOR	COMMENT
XA119	TQ2	C2	15A	KK1250	00 =		KK12 STATE 5
XA119	TQ2	C2	16A	(30)	01	KK1205T SPI0131 34 16A 36 17A	
XA119	TQ2	C3	16B	KK1260	00 =		KK12 STATE 6
XA119	TQ2	C3	14B	(33)	01	KK1206T SPI0131 29 14B 31 15B	
XA119	TQ2	C4	19B	KK1270	00 =		KK12 STATE 7
XA119	TQ2	C4	17B	(39)	01	KK1207T SPI0131 35 17B 37 18B	
XA119	TQ2	D1	24A	KK1280	00 =		KK12 STATE 8
XA119	TQ2	D1	25A	(52)	01	KK1208T SPI0131 54 25A 56 26A	
XA119	TQ2	D2	21A	KK1290	00 =		KK12 STATE 9
XA119	TQ2	D2	22A	(46)	01	KK1209T SPI0131 48 22A 50 23A	
XA119	TQ2	D3	24B	KK15QA	00 =		
XA119	TQ2	D3	22B	(45)	01	KK1284U SPI0131 41 22B 43 23B	
XA124	TT3	A2	03A	KLPTBR	00 =		
XA124	TT3	A2	02B	(07)	01	KLPTBS KXXDDP KXRS0B 01 02B 03 02A 05 03B	
XA123	TQ2	A2	02B	KLPTBS	00 =		LOOP TEST BUSY E/E
XA123	TQ2	A2	04A	(01)	01	KLPTBR KX000A 04 04A 05 03B	
				KLPTOI	00 =		
XA117	TDD	BI	03B	( )	01	KXGN2A 05 03B	
				KLPTON	00 =		
XA117	TDD	BN	02B	( )	01	KLPT1A 01 02B	
XA117	TDD	BP	04B	KLPTOP	00 =		
XA117	TDD	BP	04A	(09)	01	KXRS0B 04 04A	
				KLPTOQ	00 =		LOOP TEST COUNTER
XA117	TDD	BQ	03A	(07)	01	KX000A 03 02A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESIGNATION	FACTOR	COMMENT
			AND	OR					
XA122	TQ2	A1	05A		KLPT1A	00	=		
XA122	TQ2	A1	06A		(06)	01		KLPTIQ KXCP3B 08 06A 10 07A	
					KLPT1I	00	=		
XA118	TDD	BI	03B		( )	01		KLPT00 05 03B	
					KLPT1N	00	=		
XA118	TDD	BN	02B		( )	01		KXCP1B 01 02B	
XA118	TDD	BP	04B		KLPT1P	00	=		
XA118	TDD	BP	04A		(09)	01		KXRS0B 04 04A	
XA118	TDD	BQ	03A		KLPTIQ	00	=		LOOP TEST COUNTER
XA118	TDD	BQ	02A		(07)	01		SPI0051 03 02A	
XA124	TT3	A1	04A		KLPT2R	00	=		
XA124	TT3	A1	05A		(04)	01		KLPT2S KXXDDP KXRS0B 06 05A 08 06A 10 07A	I/O INPUT REG ENABLE BIT
XA123	TQ2	A1	05A		KLPT2S	00	=		LOOP TEST DATA WAIT
XA123	TQ2	A1	06A		(06)	01		KLPT2R KLPT1A 08 06A 10 07A	
XA127	TQ2	F3	35A		KLTE0A	00	=		LOOP TEST START INPUT DATA
XA127	TQ2	F3	34B		(69)	01		KLPT2S KXEA00 65 34B 74 35B	
XA113	TS8	E1	31B		KNULLA	00	=		
XA113	TS8	E1	29B		(59)	01		KD00BP KD01BP KD02BP KD03BP KD04BP KD05BP KD06BP KD07BP 55 29B 60 28A 61 32B 62 29A 64 30A 66 31A 68 32A 70 33A	
XA125	TD4	B2	10B		KPBZY0	00	=		TTY HARDWARE BUSY
XA125	TD4	B2	09A		(21)	01		KSNCOR KINT2A KXXDIR KLPT8R 14 09A 18 10A 19 09B 20 11A	
XA116	MUX	C1	17A		KPDTX1X	00	=		PARALLEL TO SERIAL
XA116	MUX	C1	16B		(36)	01		SPI0011 KD07BP KD06BP KD05BP KD04BP KD03BP KD02BP KD01BP 35 16B 37 17B 39 18B 41 19B 43 22B 45 23B 47 23A 50 24A	
XA116	MUX	C2	18A		KPDTX2X	00	=		
XA116	MUX	C2	21A		(38)	01		KK12B1U KK12B2U KK12B3U KK12B4U 46 21A 42 20A 40 19A 48 22A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATION	FACTOR	COMMENT
XA124	TT3	B2	09B	KPDT8A	00	=		
XA124	TT3	B2	09A	(19)	01		KPOUTO K00PBP KK1280 14 09A 17 08B 18 10A	
XA122	TQ2	B3	10B	KPDT9A	00	=		
XA122	TQ2	B3	08B	(21)	01		KPOUTO KPDTX1X 17 08B 19 09B	
XA120	TT3	B2	09B	KPDT90	00	=		
XA120	TT3	B2	09A	(19)	01		KPDT9A KPDT8A KOUTOA 14 09A 17 08B 18 10A	SERIAL OUTPUT DATA
				KPETXI	00	=		
XA118	TDD	FI	16A	( )	01		SPI0031 34 16A	
				KPETXN	00	=		
XA118	TDD	FN	15A	( )	01		SPI0041 30 15A	
XA118	TDD	FP	16B	KPETXP	00	=		
XA118	TDD	FP	17A	(33)	01		KSR50A 36 17A	
XA118	TDD	FQ	15B	KPETXQ	00	=		
XA118	TDD	FQ	14B	(31)	01		KETX1A 29 14B	EXT ON INPUT COMMA
				KPEOBI	00	=		
XA118	TDD	EI	19A	( )	01		SPI0011 40 19A	
				KPEOBN	00	=		
XA118	TDD	EN	20A	( )	01		SPI0021 42 20A	
XA118	TDD	EP	17B	KPEOBP	00	=		
XA118	TDD	EP	18A	(35)	01		KSR50A 38 18A	
XA118	TDD	EQ	18B	KPEOBQ	00	=		
XA118	TDD	EQ	19B	(37)	01		KAIERA 39 19B	EOB ON INPUT COMMA
XA131	TS8	E1	31B	KPINTA	00	=		
XA131	TS8	E1	29B	(59)	01		KAE0BS KK1290 KK08C0 KK04F0 KK03D0 KXCP3B SPI0161 SPI0141 55 29B 60 28A 61 32B 62 29A 64 30A 66 31A 68 32A 70 33A	EOB INTERRUPT ON OUTPUT COMND

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA121	TQ2	F3	35A	KPRQPA	00	=		
XA121	TQ2	F3	34B	(69)	01		KDAINQ KXXREQ 65 34B 74 35B	
XA122	TQ2	F4	39A	KPRQPO	00	=		REQUEST PENDING ON
XA122	TQ2	F4	37A	(80)	01		KPRQPA SPI0131 76 37A 78 38A	
XA115	TD4	C1	17B	KPOUTA	00	=		
XA115	TD4	C1	18B	(35)	01		KDOUTQ KAOENS KKTCCP KNULLA 37 18B 38 18A 40 19A 42 20A	
XA143	DCF	D7	33A	KPOUTDX4	00	=		OUTPUT DATA TO TTYDRIVER
XA143	DCF	D7	36A	(61)	01		KXGNIA 72 36A	
XA118	TDD	C1	13A	KPOUTI	00	=		
XA118	TDD	C1	13A	( )	01		KPDT90 24 13A	
XA118	TDD	CN	14A	KPOUTN	00	=		
XA118	TDD	CN	14A	( )	01		KXCP3B 26 14A	
XA118	TDD	CP	11B	KPOUTP	00	=		
XA118	TDD	CP	12A	(23)	01		KXRS0B 22 12A	
XA118	TDD	CQ	12B	KPOUTQ	00	=		
XA118	TDD	CQ	13B	(25)	01		SPI0011 27 13B	
XA123	TQ2	C3	16B	KPOUTO	00	=		OUTPUT TO TTY ENAB
XA123	TQ2	C3	14B	(33)	01		KPOUTA SPI0131 29 14B 31 15B	
XA143	DCF	D8	34A	KPOUTOX	00	=		
XA143	DCF	D8	35A	(68)	01		KPOUTQ 70 35A	
XA120	TT3	F3	39A	KRTDEA	00	=		INPUT COMMAND STAR
XA120	TT3	F3	35A	(80)	01		KDAINQ KAIENS KXEA00 69 35A 76 37A 78 38A	
XA132	TT3	F2	35B	KSC11A	00	=		NOT BUSY COMMAND REJECT INTRP
XA132	TT3	F2	34B	(74)	01		KSNC2S KBUSYR KXCP3B 65 34B 71 36A 72 34A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA114	TS8	E1	31B	KSNCOA	00	=		
XA114	TS8	E1	29B	(59)	01		KSNCO5 KSNC2R KBUSYR KINT1A KXCP3B SPI0011 SPI0021 SPI0031 55 29B 60 28A 61 32B 62 29A 64 30A 66 31A 68 32A 70 33A	
XA124	TT3	A3	07B	KSNCOR	00	=		
XA124	TT3	A3	04B	(15)	01		KSNCO5 KINT1A KXRS0B 09 04B 11 05B 13 06B	
XA123	TQ2	A3	04B	KSNCO5	00	=		DEVICE COMMAND SYN
XA123	TQ2	A3	02A	(09)	01		KSNCOR KSYN1A 03 02A 07 03A	
XA122	TQ2	B1	12A	KSNCL1A	00	=		
XA122	TQ2	B1	13A	(22)	01		KSNCO2S KXCP3B 24 13A 26 14A	
XA124	TT3	C1	17A	KSNCL1R	00	=		
XA124	TT3	C1	18A	(36)	01		KSNCO1S KSNCL1A KXRS0B 38 18A 40 19A 42 20A	
XA123	TQ2	C1	18A	KSNCO1S	00	=		DEVICE COMMAND SYN
XA123	TQ2	C1	19A	(38)	01		KSNCL1R KSNCOA 40 19A 42 20A	
XA126	TQ2	D2	21A	KSNCO2A	00	=		
XA126	TQ2	D2	22A	(46)	01		KSNCO1S KXCP1B 48 22A 50 23A	
XA124	TT3	C2	15B	KSNCO2R	00	=		
XA124	TT3	C2	14B	(31)	01		KSNCO2S KSNCO3A KXRS0B 29 14B 30 15A 34 16A	
XA123	TQ2	C2	15A	KSNCO2S	00	=		DEVICE COMMAND SYN
XA123	TQ2	C2	16A	(30)	01		KSNCO2R KSNCO2A 34 16A 36 17A	
XA123	TQ2	B4	13B	KSNCO3A	00	=		
XA123	TQ2	B4	11B	(27)	01		KSNCL1R KXCP1B 23 11B 25 12B	
XA119	TQ2	F2	34A	KSRSOA	00	=		
XA119	TQ2	F2	36A	(72)	01		KSRSO0 SPI0131 71 36A 73 36B	
XA120	TT3	F2	35B	KSRSO0	00	=		RESET ERROR/STATUS
XA120	TT3	F2	34B	(74)	01		KSNCO2A KXODRA KXRS0B 65 34B 71 36A 72 34A	



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM DESIGNATOR	FACTOR	COMMENT
XA127	TQ2	B3	10B	KSYN1A	00 =		START NEW INPUT OR OUTPUT COM
XA127	TQ2	B3	08B	(21)	01	KSYN10 KXDV10 17 08B 19 09B	
XA126	TQ2	B2	09A	KSYN10	00 =		
XA126	TQ2	B2	10A	(14)	01	KXRAF5T KXRAF7T 18 10A 20 11A	
				KXACMB4	00 =		
XA241	TLD	C2	15A	( )	01	KXACMD 30 15A	PORT A/B COMMON LINE BUS
XA240	TLD	C2	15A	( )	02 +	KXBCMD 30 15A	PORT A/B COMMAND LINE BUS
XA241	TLD	C2	15A	KXACMD	00 =		KXACMB4 BUSS
XA241	TLD	C2	16A	(30)	01	KXAIFO KXACMOX 34 16A 36 17A	
XA143	DCF	C1	25B	KXACMDX	00 =		TACMAB BUSS
XA143	DCF	C1	29A	(46)	01	KXA0EA 52 29A	
XA143	DCF	C2	26B	KXACMOX	00 =		
XA143	DCF	C2	27B	(47)	01	KXGNIA 49 27B	
				KXAENB4	00 =		
XA241	TLD	C3	16B	( )	01	KXAEND 33 16B	PORT A/B ENABLE LINE BUS
XA240	TLD	C3	16B	( )	02 +	KXBEND 33 16B	PORT A/B ENABLE LINE BUS
XA241	TLD	C3	16B	KXAEND	00 =		KXAENB4 BUSS
XA241	TLD	C3	14B	(33)	01	KXAIFO KXAENOX 29 14B 31 15B	
XA143	DCF	C3	30B	KXAENDX	00 =		TAENAB BUSS
XA143	DCF	C3	29A	(55)	01	KXA0EA 52 29A	
XA143	DCF	C4	29B	KXAENOX	00 =		
XA143	DCF	C4	28B	(56)	01	KXGNIA 51 28B	
XA138	TQ2	A1	05A	KXA1EA	00 =		
XA138	TQ2	A1	06A	(06)	01	KXASLOX KXXDRA 08 06A 10 07A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA139	TQ2	A2	02B	KXAIEO	00	=		PORT A DATA RECEIVE ENABLE
XA139	TQ2	A2	04A	(01)	01		KXAIEA SPI0181 04 04A 05 03B	
XA139	TQ2	A3	04B	KXAIFO	00	=		
XA139	TQ2	A3	02A	(09)	01		KXAIEA SPI0181 03 02A 07 03A	
XA143	DCF	C5	31B	KXAINDX	00	=		TAINAR BUSS
XA143	DCF	C5	29A	(60)	01		KXAOEA 52 29A	
XA143	DCF	C6	31A	KXAINOX	00	=		
XA143	DCF	C6	30A	(57)	01		KXDBIO 54 30A	
				KXAPCB4	00	=		
XA241	TLD	C1	18A	( )	01		KXAPCD 38 18A	PORT A/B PARITY LINE BU S
XA240	TLD	C1	18A	( )	02	+	KXBPCD 38 18A	PORT A/B PARITY LINE BU S
XA241	TLD	C1	18A	KXAPCD	00	=		KXAPCB4 BUSS
XA241	TLD	C1	19A	(38)	01		KXAIFO KXAPCOX 40 19A 42 20A	
XA143	DCF	C7	25A	KXAPCDX	00	=		TAOPAB BUSS
XA143	DCF	C7	29A	(43)	01		KXAOEA 52 29A	
XA143	DCF	C8	26A	KXAPCOX	00	=		
XA143	DCF	C8	28A	(48)	01		KXDSBPR 50 28A	
XA137	TD4	E1	31B	KXARQA	00	=		
XA137	TD4	E1	32B	(59)	01		KXASLOX KXINHR DEVINH KXXROQ 61 32B 66 31A 68 32A 70 33A	
XA127	TQ2	E1	31A	KXARQO	00	=		PORT A REQUEST
XA127	TQ2	E1	32A	(66)	01		KXARQA SPI0161 68 32A 70 33A	
XA132	TT3	B1	11A	KXARSA	00	=		PORT A IOU RESET
XA132	TT3	B1	12A	(20)	01		KXACMOX KXAENOX KXASLOX 22 12A 24 13A 26 14A	
XA241	TLD	C4	19B	KXAR4D1	00	=		PORT A REQUEST
XA241	TLD	C4	17B	(39)	01		KXARQO SPI0201 35 17B 37 18B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM DESIGNATOR	FACTOR	COMMENT
XA138	TQ2	B1	12A	KXASLA	00 =		PORT A SELECT
XA138	TQ2	B1	13A	(22 )	01	KXASLOX SPI0181 24 13A 26 14A	
XA142	DCF	D1	32B	KXASLDX	00 =		
XA142	DCF	D1	36A	(65 )	01	SPI0211 72 36A	
XA142	DCF	D2	33B	KXASLOX	00 =		
XA142	DCF	D2	34B	(69 )	01	SPI0191 71 34B	
XA143	DCF	D1	32B	KXASTDX4	00 =		
XA143	DCF	D1	36A	(65 )	01	KXGN1A 72 36A	
XA143	DCF	D2	33B	KXASTOX	00 =		
XA143	DCF	D2	34B	(69 )	01	KXASLOX 71 34B	
				KXA0CB4	00 =		
XA241	TLD	A1	05A	( )	01	KXA0CD 06 05A	PORT A/B DATA LINE0 BUS
XA240	TLD	A1	05A	( )	02 +	KXB0CD 06 05A	PORT A/B DATA LINE0 BUS
XA241	TLD	A1	05A	KXA0CD	00 =		KXA0CB4 BUSS
XA241	TLD	A1	06A	(06 )	01	KXA1E0 KXA0COX 08 06A 10 07A	
XA143	DCF	A1	02B	KXA0COX	00 =		TA00AB BUSS
XA143	DCF	A1	05A	(07 )	01	KXA0EA 06 05A	
XA143	DCF	A2	03B	KXA0COX	00 =		
XA143	DCF	A2	04B	(09 )	01	KXS031U 11 04B	
XA137	TD4	A1	05B	KXA0EA	00 =		PORT A DATA SEND ENABLE
XA137	TD4	A1	05A	(11 )	01	KXASLOX KXINHR DEVINH KXXC50 06 05A 08 06A 10 07A 13 06B	
				KXA1CB4	00 =		
XA241	TLD	A2	02B	( )	01	KXA1CD 01 02B	
XA240	TLD	A2	02B	( )	02 +	KXB1CD 01 02B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	RELATIONSHIP	FACTOR	COMMENT
XA241	TLD	A2	02B	KXA1CD	00	=		
XA241	TLD	A2	04A	(01)	01		KXA1E0 KXA1COX 04 04A 05 03B	KXA1CB4 BUSS
XA143	DCF	A3	07B	KXA1CDX	00	=		
XA143	DCF	A3	05A	(17)	01		KXA0EA 06 05A	TA01AB BUSS
XA143	DCF	A4	06B	KXA1COX	00	=		
XA143	DCF	A4	05B	(15)	01		KXS032U 13 05B	
				KXA2CB4	00	=		
XA241	TLD	A3	04B	( )	01		KXA2CD 09 04B	
XA240	TLD	A3	04B	( )	02	+	KXB2CD 09 04B	
XA241	TLD	A3	04B	KXA2CD	00	=		
XA241	TLD	A3	02A	(09)	01		KXA1E0 KXA2COX 03 02A 07 03A	KXA2CB4 BUSS
XA143	DCF	A5	08B	KXA2CDX	00	=		
XA143	DCF	A5	05A	(14)	01		KXA0EA 06 05A	TA02AB BUSS
XA143	DCF	A6	07A	KXA2COX	00	=		
XA143	DCF	A6	06A	(10)	01		KXS033U 08 06A	
				KXA3CB4	00	=		
XA241	TLD	A4	07B	( )	01		KXA3CD 15 07B	
XA240	TLD	A4	07B	( )	02	+	KXB3CD 15 07B	
XA241	TLD	A4	07B	KXA3CD	00	=		
XA241	TLD	A4	05B	(15)	01		KXA1E0 KXA3COX 11 05B 13 06B	KXA3CB4 BUSS
XA143	DCF	A7	02A	KXA3CDX	00	=		
XA143	DCF	A7	05A	(01)	01		KXA0EA 06 05A	TA03AB BUSS
XA143	DCF	A8	03A	KXA3COX	00	=		
XA143	DCF	A8	04A	(03)	01		KXS034U 04 04A	

CONNECTOR	CIRCUIT GROUP	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIG- NATOR	FACTOR	COMMENT
				KXA4CB4	00	=		
XA241	TLD	B1	12A	( )	01		KXA4CD 22 12A	
XA240	TLD	B1	12A	( )	02	+	KXB4CD 22 12A	
XA241	TLD	B1	12A	KXA4CD	00	=		KXA4CB4 BUSS
XA241	TLD	B1	13A	(22 )	01		KXA1E0 KXA4COX 24 13A 26 14A	
XA143	DCF	B1	10B	KXA4CDX	00	=		TA04AB BUSS
XA143	DCF	B1	13A	(27 )	01		KXA0EA 36 13A	
XA143	DCF	B2	11B	KXA4COX	00	=		
XA143	DCF	B2	12B	(29 )	01		KXS471U 31 12B	
				KXA5CB4	00	=		
XA241	TLD	B2	09A	( )	01		KXA5CD 14 09A	
XA240	TLD	B2	09A	( )	02	+	KXB5CD 14 09A	
XA241	TLD	B2	09A	KXA5CD	00	=		KXA5CB4 BUSS
XA241	TLD	B2	10A	(14 )	01		KXA1E0 KXA5COX 18 10A 20 11A	
XA143	DCF	B3	15B	KXA5CDX	00	=		TA05AB BUSS
XA143	DCF	B3	13A	(37 )	01		KXA0EA 36 13A	
XA143	DCF	B4	14B	KXA5COX	00	=		
XA143	DCF	B4	13B	(35 )	01		KXS472U 33 13B	
				KXA6CB4	00	=		
XA241	TLD	B3	10B	( )	01		KXA6CD 21 10B	
XA240	TLD	B3	10B	( )	02	+	KXB6CD 21 10B	
XA241	TLD	B3	10B	KXA6CD	00	=		KXA6CB4 BUSS
XA241	TLD	B3	08B	(21 )	01		KXA1E0 KXA6COX 17 08B 19 09B	
XA143	DCF	B5	16A	KXA6CDX	00	=		TA06AB BUSS
XA143	DCF	B5	13A	(41 )	01		KXA0EA 36 13A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DEFINITION	FACTOR	COMMENT
XA143	DCF	B6	15A	KXA6COX	00	=		
XA143	DCF	B6	14A	(40)	01		KXS473U 38 14A	
				KXA7CB4	00	=		
XA241	TLD	B4	13B	( )	01		KXA7CD 27 13B	PORT A/B DATA LINE7 BUS
XA240	TLD	B4	13B	( )	02	+	KXB7CD 27 13B	PORT A/B DATA LINE7 BUS
XA241	TLD	B4	13B	KXA7CD	00	=		
XA241	TLD	B4	11B	(27)	01		KXAIEO KXA7COX 23 11B 25 12B	KYA7CB4 BUSS
XA143	DCF	B7	10A	KXA7CDX	00	=		
XA143	DCF	B7	13A	(23)	01		KXA0EA 36 13A	TA07AB BUSS
XA143	DCF	B8	11A	KXA7COX	00	=		
XA143	DCF	B8	12A	(30)	01		KXS474U 34 12A	
XA240	TLD	C2	15A	KXBCMD	00	=		
XA240	TLD	C2	16A	(30)	01		KXBIFO KXBCMOX 34 16A 36 17A	KYACMB4 BUSS
XA142	DCF	C1	25B	KXBCMDX	00	=		
XA142	DCF	C1	29A	(46)	01		KXB0EA 52 29A	TACMBB BUSS
XA142	DCF	C2	26B	KXBCMOX	00	=		
XA142	DCF	C2	27B	(47)	01		KXGN1A 49 27B	
XA240	TLD	C3	16B	KXBEND	00	=		
XA240	TLD	C3	14B	(33)	01		KXBIFO KXBENOX 29 14B 31 15B	KYAENB4 BUSS
XA142	DCF	C3	30B	KXBENDX	00	=		
XA142	DCF	C3	29A	(55)	01		KXB0EA 52 29A	TAENBB BUSS
XA142	DCF	C4	29B	KXBENOX	00	=		
XA142	DCF	C4	28B	(56)	01		KXGN1A 51 28B	
XA138	TQ2	A2	02B	KXBIEA	00	=		
XA138	TQ2	A2	04A	(01)	01		KXBSLOX KXXDRA 04 04A 05 03B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM.	DESIGNATOR	FACTOR	COMMENT
XA139	TQ2	A4	07B	KXBIEO	00	=		PORT B DATA RECEIVE ENABLE
XA139	TQ2	A4	05B	(15)	01		KXBIEA SPI0201 11 05B 13 06B	
XA139	TQ2	B1	12A	KXBIFC	00	=		
XA139	TQ2	B1	13A	(22)	01		KXBIEA SPI0201 24 13A 26 14A	
XA142	DCF	C5	31B	KXBINDX	00	=		TAINBB BUSS
XA142	DCF	C5	29A	(60)	01		KXBOEA 52 29A	
XA142	DCF	C6	31A	KXBINOX	00	=		
XA142	DCF	C6	30A	(57)	01		KXDBIO 54 30A	
XA240	TLD	C1	18A	KXBPCD	00	=		KXAPCB4 BUSS
XA240	TLD	C1	19A	(38)	01		KXBIFC KXBPCOX 40 19A 42 20A	
XA142	DCF	C7	25A	KXBPCDX	00	=		TAQPB BUSS
XA142	DCF	C7	29A	(43)	01		KXBOEA 52 29A	
XA142	DCF	C8	26A	KXBPCOX	00	=		
XA142	DCF	C8	28A	(48)	01		KXDSBPR 50 28A	
XA137	TD4	E2	30B	KXBRQA	00	=		
XA137	TD4	E2	29B	(57)	01		KXBSLOX KXINHR DEVINH KXXRQO 55 29B 60 28A 62 29A 64 30A	
XA127	TQ2	E2	28A	KXBRQO	00	=		PORT B REQUEST
XA127	TQ2	E2	29A	(60)	01		KXBRQA SPI0161 62 29A 64 30A	
XA132	TT3	B2	09B	KXBRSA	00	=		PORT B IOU RESET
XA132	TT3	B2	09A	(19)	01		KXBCMOX KXBENOX KXBSLOX 14 09A 17 08B 18 10A	
XA240	TLD	C4	19B	KXBR4D1	00	=		PORT B REQUEST
XA240	TLD	C4	17B	(39)	01		KXBRQO SPI0201 35 17B 37 18B	
XA138	TQ2	B2	09A	KXBSLA	00	=		PORT B SELECT
XA138	TQ2	B2	10A	(14)	01		KXBSLOX SPI0181 18 10A 20 11A	

CONNECTOR	CUR. JNT. TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIG. PREFIX	FACTOR	COMMENT
XA142	DCF	D3	37B	KXBSLDX	00	=		
XA142	DCF	D3	36A	(78 )	01		SPI0211 72 36A	
XA142	DCF	D4	36B	KXBSLOX	00	=		
XA142	DCF	D4	35B	(75 )	01		SPI0041 73 35B	
XA143	DCF	D3	37B	KXBSTD4	00	=		
XA143	DCF	D3	36A	(78 )	01		KXGN1A 72 36A	
XA143	DCF	D4	36B	KXBSTOX	00	=		
XA143	DCF	D4	35B	(75 )	01		KXBSLOX 73 35B	
XA240	TLD	A1	05A	KXBOCD	00	=		
XA240	TLD	A1	06A	(06 )	01		KXBIE0 KXBOCOX 08 06A 10 07A	KXA0CB4 BUSS
XA142	DCF	A1	02B	KXBOCDX	00	=		
XA142	DCF	A1	05A	(07 )	01		KXB0EA 06 05A	TA00BB BUSS
XA142	DCF	A2	03B	KXBOCOX	00	=		
XA142	DCF	A2	04B	(09 )	01		KXS031U 11 04B	
XA137	TD4	A2	04B	KXB0EA	00	=		
XA137	TD4	A2	02B	(09 )	01		KXBSLOX KXINHR DEVINH KXXCS0 01 02B 04 04A 05 03B 07 03A	PORT B DATA SEND ENABLE
XA240	TLD	A2	02B	KXB1CD	00	=		
XA240	TLD	A2	04A	(01 )	01		KXBIE0 KXB1COX 04 04A 05 03B	KXA1CB4 BUSS
XA142	DCF	A3	07B	KXB1CDX	00	=		
XA142	DCF	A3	05A	(17 )	01		KXB0EA 06 05A	TA01BB BUSS
XA142	DCF	A4	06B	KXB1COX	00	=		
XA142	DCF	A4	05B	(15 )	01		KXS032U 13 05B	
XA240	TLD	A3	04B	KXB2CD	00	=		
XA240	TLD	A3	02A	(09 )	01		KXBIE0 KXB2COX 03 02A 07 03A	KXA2CB4 BUSS



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA142	DCF	A5	08B	KXB2CDX	00	=		TA02BB BUSS
XA142	DCF	A5	05A	(14)	01		KXB0EA 06 05A	
XA142	DCF	A6	07A	KXB2COX	00	=		
XA142	DCF	A6	06A	(10)	01		KXS033U 08 06A	
XA240	TLD	A4	07B	KXB3CD	00	=		KXA3CB4 BUSS
XA240	TLD	A4	05B	(15)	01		KXB1E0 KXB3COX 11 05B 13 06B	
XA142	DCF	A7	02A	KXB3CDX	00	=		TA03BB BUSS
XA142	DCF	A7	05A	(01)	01		KXB0EA 06 05A	
XA142	DCF	A8	03A	KXB3COX	00	=		
XA142	DCF	A8	04A	(03)	01		KXS034U 04 04A	
XA240	TLD	B1	12A	KXB4CD	00	=		KXA4CB4 BUSS
XA240	TLD	B1	13A	(22)	01		KXB1E0 KXB4COX 24 13A 26 14A	
XA142	DCF	B1	10B	KXB4CDX	00	=		TA04BB BUSS
XA142	DCF	B1	13A	(27)	01		KXB0EA 36 13A	
XA142	DCF	B2	11B	KXB4COX	00	=		
XA142	DCF	B2	12B	(29)	01		KXS471U 31 12B	
XA240	TLD	B2	09A	KXB5CD	00	=		KXA5CB4 BUSS
XA240	TLD	B2	10A	(14)	01		KXB1E0 KXB5COX 18 10A 20 11A	
XA142	DCF	B3	15B	KXB5CDX	00	=		TA05BB BUSS
XA142	DCF	B3	13A	(37)	01		KXB0EA 36 13A	
XA142	DCF	B4	14B	KXB5COX	00	=		
XA142	DCF	B4	13B	(35)	01		KXS472U 33 13B	
XA240	TLD	B3	10B	KXB6CD	00	=		KXA6CB4 BUSS
XA240	TLD	B3	08B	(21)	01		KXB1E0 KXB6COX 17 08B 19 09B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA142	DCF	B5	16A	KXB6CDX	00	=		
XA142	DCF	B5	13A	(41)	01		KXB0EA 36 13A	TA06BB BUSS
XA142	DCF	B6	15A	KXB6COX	00	=		
XA142	DCF	B6	14A	(40)	01		KXS473U 38 14A	
XA240	TLD	B4	13B	KXB7CD	00	=		
XA240	TLD	B4	11B	(27)	01		KXB1E0 KXB7COX 23 11B 25 12B	KXA7CB4 BUSS
XA142	DCF	B7	10A	KXB7CDX	00	=		
XA142	DCF	B7	13A	(23)	01		KXB0EA 36 13A	TA07BB BUSS
XA142	DCF	B8	11A	KXB7COX	00	=		
XA142	DCF	B8	12A	(30)	01		KXS474U 34 12A	
XA137	TD4	F1	37A	KXCA0A	00	=		
XA137	TD4	F1	37B	(76)	01		KXR4CS KXRCMS KXX050 KXROPA 75 37B 77 38B 78 38A 79 39B	SET COMMAND ADDRESS E/F
XA131	TS8	A1	05B	KXCA1A	00	=		
XA131	TS8	A1	02B	(11)	01		KXCMA5 KXXB10 KXXA0P KXXA1Q KXRPCS KXR0CS KXR1CR KXR2CR 01 02B 04 04A 05 03B 06 05A 07 03A 08 06A 10 07A 13 06B	DEV COMND BYTE 1 COMMON TERMS
XA135	TQ2	B4	13B	KXCA10	00	=		
XA135	TQ2	B4	11B	(27)	01		KXCA1A SPI0181 23 11B 25 12B	
XA137	TD4	B1	11B	KXCMA5	00	=		
XA137	TD4	B1	12A	(23)	01		KXCMA5 KXX04A KXXB2A KXRS0B 22 12A 24 13A 25 12B 26 14A	
XA135	TQ2	B1	12A	KXCMA5	00	=		
XA135	TQ2	B1	13A	(22)	01		KXCMA5 KXCA0A 24 13A 26 14A	COMMAND ADDRESS FF
XA127	TQ2	A2	02B	KXCP00	00	=		
XA127	TQ2	A2	04A	(01)	01		KXCP1A SPI0151 04 04A 05 03B	KXCP1B BUSS
XA135	TQ2	E4	33B	KXCP1A	00	=		
XA135	TQ2	E4	31B	(63)	01		KX1MAP KX1MBQ 59 31B 61 32B	PHASE 1 OF 2 PHASECLOCK

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
				KXCP1B	00	=		
XA127	TQ2	A3	04B	( )	01		KXCP10 KXCP20 KXCP00 09 04B 15 07B 01 02B	CLOCK PHASE 1 BUS
XA127	TQ2	A3	04B	KXCP10	00	=		KXCP1B BUSS
XA127	TQ2	A3	02A	(09 )	01		KXCP1A SPI0151 03 02A 07 03A	
XA127	TQ2	A4	07B	KXCP20	00	=		KXCP1B BUSS
XA127	TQ2	A4	05B	(15 )	01		KXCP1A SPI0151 11 05B 13 06B	
XA127	TQ2	B2	09A	KXCP3A	00	=		PHASE 3 OF 2 PHASE
XA127	TQ2	B2	10A	(14 )	01		KXIMAQ KXIMBP 18 10A 20 11A	
				KXCP3B	00	=		
XA126	TQ2	A2	02B	( )	01		KXCP30 KXCP40 KXCP50 01 02B 09 04B 15 07B	CLOCK PHASE 3 BUS
XA126	TQ2	A2	02B	KXCP30	00	=		KXCP3B BUSS
XA126	TQ2	A2	04A	(01 )	01		KXCP3A SPI0151 04 04A 05 03B	
XA126	TQ2	A3	04B	KXCP40	00	=		KXCP3B BUSS
XA126	TQ2	A3	02A	(09 )	01		KXCP3A SPI0151 03 02A 07 03A	
XA126	TQ2	A4	07B	KXCP50	00	=		KXCP3B BUSS
XA126	TQ2	A4	05B	(15 )	01		KXCP3A SPI0151 11 05B 13 06B	
XA132	TT3	E3	33B	KXDB10	00	=		INPUT INDICATOR CONTROL
XA132	TT3	E3	30B	(63 )	01		KXXCIP KXXDIP KXXDSP 57 30B 59 31B 61 32B	
XA119	TQ2	F1	37B	KXDBS0	00	=		INPUT MUX STATUS
XA119	TQ2	F1	38B	(75 )	01		KXXDIP KXXDSP 77 38B 79 39B	
XA116	MUX	D1	27B	KXDB0TA	00	=		INPUT DATA MUX BIT
XA116	MUX	D1	25B	(55 )	01		KD00BQ KA0ENS KXXC1Q KXDBS0 51 25B 53 26B 52 25A 49 24B	
XA116	MUX	D2	31B	KXDB0TB	00	=		
XA116	MUX	D2	29B	(61 )	01		KD01BQ KAIENS 57 29B 59 30B	

CONNECTOR	CONNECTOR TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	TEST POINTS	FACTOR	COMMENT
XA116	MUX	D3	28A	KXDB0TC	00	=		
XA116	MUX	D3	26A	(60)	01		KD02BQ KPE0BQ 54 26A 56 28B	
XA116	MUX	D4	31A	KXDB0TD	00	=		
XA116	MUX	D4	29A	(63)	01		KD03BQ KPETXQ 62 29A 64 30A	
XA116	MUX	E1	34B	KXDB4TA	00	=		
XA116	MUX	E1	32A	(73)	01		KD04BQ KPRQPO KXXCIO KXDBSO 69 32A 71 33B 68 33A 66 32B	INPUT DATA MUX BIT
XA116	MUX	E2	37B	KXDB4TB	00	=		
XA116	MUX	E2	35B	(79)	01		KD05BQ KPBZY0 75 35B 77 36B	
XA116	MUX	E3	36A	KXDB4TC	00	=		
XA116	MUX	E3	34A	(74)	01		KD06BQ KKO09A1 70 34A 72 35A	
XA116	MUX	E4	38B	KXDB4TD	00	=		
XA116	MUX	E4	37A	(80)	01		KD07BQ KCDERQ 76 37A 78 38A	
XA127	TQ2	C1	18A	KXDEVA	00	=		
XA127	TQ2	C1	19A	(38)	01		KXR3CS SPI0161 40 19A 42 20A	
XA137	TD4	C2	16B	KXDEVR	00	=		
XA137	TD4	C2	15A	(33)	01		KXDEVS KXXB0A KXXB3A KXR50B 30 15A 31 15B 34 16A 36 17A	
XA135	TQ2	C2	15A	KXDEVS	00	=		
XA135	TQ2	C2	16A	(30)	01		KXDEVR KXDVOA 34 16A 36 17A	DEVICE COMMAND F/EMAYBE BSY
XA126	TQ2	B1	12A	KXDEVO	00	=		
XA126	TQ2	B1	13A	(22)	01		KXDEVA SPI0151 24 13A 26 14A	
XA125	TD4	A2	04B	KXDPEA	00	=		
XA125	TD4	A2	02B	(09)	01		KD0UTQ KXENAS KXR0PPR KXXA50 01 02B 04 04A 05 03B 07 03A	AUTO OUTPUT COMND
XA236	PAR	C1	20A	KXDSBPR	00	=		
XA236	PAR	C1	15B	(42)	01		KXS031U KXS032U KXS033U KXS034U KXS471U KXS472U KXS473U KXS474U 31 15B 33 16B 35 17B 37 18B 38 18A 36 17A 34 16A 30 15A	
XA236	PAR	C1	19B	( )	02	+	KXXCIO 39 19B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERMINAL	DESIGNATOR	FACTOR	COMMENT
XA127	TQ2	C2	15A	KXDVCO	00	=		
XA127	TQ2	C2	15A	(30)	01		KXR090T SPI0161 34 16A 36 17A	
XA137	TD4	C1	17B	KXDVSR	00	=		
XA137	TD4	C1	18B	(35)	01		KXDVSS KXXB0A KXXB3A KXR50B 37 18B 38 18A 40 19A 42 20A	
XA135	TQ2	C1	18A	KXDVSS	00	=		DEVICE COMMAND F/END BUSY
XA135	TQ2	C1	19A	(38)	01		KXDVSR KXDV5A 40 19A 42 20A	
XA125	TD4	C1	17B	KXDVOA	00	=		SET DEVICE COMMANDE/F
XA125	TD4	C1	18B	(35)	01		KXCA10 KXDEVO KXDVCO KBUSYA 37 18B 38 18A 40 19A 42 20A	
XA131	TS8	C1	17B	KXDV1A	00	=		
XA131	TS8	C1	15A	(35)	01		KXDEVS KXXB20 KXXA0P KXXA1Q KXROPA SPI0141 SPI0161 SPI0031 30 15A 31 15B 34 16A 36 17A 37 18B 38 18A 40 19A 42 20A	
XA123	TQ2	F2	34A	KXDV10	00	=		DEV COMMAND DATA
XA123	TQ2	F2	36A	(72)	01		KXDV1A SPI0151 71 36A 73 36B	
XA119	TQ2	E4	33B	KXDV2A	00	=		
XA119	TQ2	E4	31B	(63)	01		KDOUTQ KXDV20 59 31B 61 32B	
XA121	TQ2	E3	30B	KXDV20	00	=		
XA121	TQ2	E3	28B	(57)	01		KA0ENR KXXDIR 53 28B 55 29B	
XA113	TS8	D1	25B	KXDV3A	00	=		START END INPUT CO
XA113	TS8	D1	23B	(47)	01		KXDVSS KXXB20 KXXA0P KXXA1Q KENTNO KXROPA KXDV2A SPI0011 43 23B 46 21A 48 22A 49 26B 50 23A 52 24A 54 25A 56 26A	
XA132	TT3	A3	07B	KXDV5A	00	=		SET DEV COMND F/F NO BUSY
XA132	TT3	A3	04B	(15)	01		KXCA10 KXDEVO KXDVCO 09 04B 11 05B 13 06B	
XA125	TD4	A1	05B	KXEA0A	00	=		SET ENABLE ADDRESS
XA125	TD4	A1	05A	(11)	01		KXR4CS KXRENS KXX050 KXROPA 06 05A 08 06A 10 07A 13 06B	
XA123	TQ2	F4	39A	KXEA00	00	=		
XA123	TQ2	F4	37A	(80)	01		KXEA0A SPI0151 76 37A 78 38A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TEST	DESIGNATOR	FACTOR	COMMENT
XA132	TT3	C1	17A	KXEBOA	00	=		COMMAND IS EOB
XA132	TT3	C1	18A	(36)	01		KXCA10 KXE0B0 KXDEVA 38 18A 40 19A 42 20A	
				KXEBOI	00	=		
XA128	TDD	DI	10A	( )	01		KXGN2A 18 10A	
				KXEBOB	00	=		
XA128	TDD	DN	09A	( )	01		KXEB1A 14 09A	
XA128	TDD	DP	10B	KXEBOP	00	=		
XA128	TDD	DP	11A	(21)	01		KXRSOB 20 11A	
XA128	TDD	DQ	09B	KXEBOC	00	=		EOB SYNC COUNTER BIT 0
XA128	TDD	DQ	08B	(19)	01		KXER0A 17 08B	
XA127	TQ2	D3	24B	KXEB1A	00	=		
XA127	TQ2	D3	22B	(45)	01		KXEB1Q KXCP3B 41 22B 43 23B	
				KXEB1I	00	=		
XA130	TDD	DI	10A	( )	01		KXEBOQ 18 10A	
				KXEB1N	00	=		
XA130	TDD	DN	09A	( )	01		KXCP1B 14 09A	
XA130	TDD	DP	10B	KXEB1P	00	=		
XA130	TDD	DP	11A	(21)	01		KXRSOB 20 11A	
XA130	TDD	DQ	09B	KXEB1Q	00	=		EOB SYNC COUNTER BIT 1
XA130	TDD	DQ	08B	(19)	01		SPI0141 17 08B	
XA125	TD4	B1	11B	KXED0A	00	=		AUTO OUTPUT DATA
XA125	TD4	B1	12A	(23)	01		KDOUTQ KXENAS KXXB10 KXXA50 22 12A 24 13A 25 12B 26 14A	
XA137	TD4	B2	10B	KXENAR	00	=		
XA137	TD4	B2	09A	(21)	01		KXENAS KXX04A KXXB2A KXRSOB 14 09A 18 10A 19 09B 20 11A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA135	TQ2	B3	10B	KXENAS	00	=		
XA135	TQ2	B3	08B	(21)	01		KXENAR KXEAOA 17 08B 19 09B	ENABLE ADDRESS F/F
XA127	TQ2	D1	24A	KXE0B0	00	=		
XA127	TQ2	D1	25A	(52)	01		KXR092T SPI0161 54 25A 56 26A	DATA BYTE DECODER 0 TO 9
XA138	TQ2	A3	04B	KXGN1A	00	=		
XA138	TQ2	A3	02A	(09)	01		SPI0181 SPI0141 03 02A 07 03A	SOFT GROUND
XA135	TQ2	C4	19B	KXGN2A	00	=		
XA135	TQ2	C4	17B	(39)	01		SPI0141 SPI0181 35 17B 37 18B	
XA126	TQ2	D4	27B	KXGN3A	00	=		
XA126	TQ2	D4	25B	(51)	01		SPI0141 SPI0151 47 25B 49 26B	7 K 456
XA139	TQ2	B3	10B	KXGN4A	00	=		
XA139	TQ2	B3	08B	(21)	01		SPI0201 SPI0191 17 08B 19 09B	
XA126	TQ2	D1	24A	KXHST0	00	=		
XA126	TQ2	D1	25A	(52)	01		KXR091T SPI0151 54 25A 56 26A	
XA132	TT3	C2	15B	KXH50A	00	=		
XA132	TT3	C2	14B	(31)	01		KXCA10 KXHST0 KXDEVA 29 14B 30 15A 34 16A	
XA135	TQ2	A4	07B	KXINHR	00	=		
XA135	TQ2	A4	05B	(15)	01		KXINHS KXIN0A 11 05B 13 06B	
XA132	TT3	A2	03A	KXINHS	00	=		
XA132	TT3	A2	02B	(07)	01		KXINHR KXRS0B KXONLO 01 02B 03 02A 05 03B	DATA SEND INHIBIT F/F
XA132	TT3	D2	23B	KXIN0A	00	=		
XA132	TT3	D2	22B	(43)	01		KXRS0B KXRCMS KXONLO 41 22B 46 21A 48 22A	
XA126	TQ2	C4	19B	KXIRCO	00	=		
XA126	TQ2	C4	17B	(39)	01		KXR094T SPI0151 35 17B 37 18B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA132	TT3	B3	13B	KXIROA	00	=		
XA132	TT3	B3	10B	(27)	01	.	KXCA10 KXIRCO KXDEVA 21 10B 23 11B 25 12B	COMMAND IS ITR
				KXIROI	00	=		
XA128	TDD	CI	13A	( )	01		KXGN2A 24 13A	
				KXIRON	00	=		
XA128	TDD	CN	14A	( )	01		KXIR1A 26 14A	
XA128	TDD	CP	11B	KXIROP	00	=		
XA128	TDD	CP	12A	(23)	01		KXRSOB 22 12A	
XA128	TDD	CQ	12B	KXIROQ	00	=		
XA128	TDD	CQ	13B	(25)	01		KXIROA 27 13B	ITR SYNC COUNTER BIT 0
XA127	TQ2	B4	13B	KXIR1A	00	=		
XA127	TQ2	B4	11B	(27)	01		KXIR1Q KXCP3B 23 11B 25 12B	
				KXIR1I	00	=		
XA129	TDD	DI	10A	( )	01		KXIROQ 18 10A	
				KXIR1N	00	=		
XA129	TDD	DN	09A	( )	01		KXCP1B 14 09A	
XA129	TDD	DP	10B	KXIR1P	00	=		
XA129	TDD	DP	11A	(21)	01		KXRSOB 20 11A	
XA129	TDD	DQ	09B	KXIR1Q	00	=		
XA129	TDD	DQ	08B	(19)	01		SPI0141 17 08B	ITR SYNC COUNTER BIT 1
XA138	TQ2	A4	07B	KXPRSA	00	=		
XA138	TQ2	A4	05B	(15)	01		KXPRSOX SPI0181 11 05B 13 06B	CAP PANEL RESET
XA142	DCF	D5	38B	KXPRSDX	00	=		
XA142	DCF	D5	36A	(80)	01		SPI0211 72 36A	



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA142	DCF	D6	38A	KXPRSOX	00	=		
XA142	DCF	D6	37A	(76)	01		SPI0061 74 37A	
XA134	DBC	A1	04A	KXRAF0T	00	=		DATA BYTE DECODER A TO F
XA134	DBC	A1	02A	(58)	01		KXR7CS 04 02A	
XA134	DBC	A2	05A	KXRAF1T	00	=		
XA134	DBC	A2	03A	(10)	01		KXR6CS 06 03A	
XA134	DBC	A3	06A	KXRAF2T	00	=		
XA134	DBC	A3	02B	(14)	01		KXR5CS 03 02B	
XA134	DBC	A4	07A	KXRAF3T	00	=		
XA134	DBC	A4	03B	(13)	01		KXR4CR 05 03B	
XA134	DBC	A5	08B	KXRAF4T	00	=		
XA134	DBC	A5		(17)	01		SPA 4T	
XA134	DBC	A6	04B	KXRAF5T	00	=		
XA134	DBC	A6		(07)	01		SPA 5T	
XA134	DBC	A7	05B	KXRAF6T	00	=		
XA134	DBC	A7		(09)	01		SPA 6T	
XA134	DBC	A8	06B	KXRAF7T	00	=		
XA134	DBC	A8		(11)	01		SPA 7T	
XA134	DBC	A9	07B	KXRAF8T	00	=		
XA134	DBC	A9		(15)	01		SPA 8T	
XA134	DBC	A0	09A	KXRAF9T	00	=		
XA134	DBC	A0		(18)	01		SPA 9T	
XA138	TQ2	C1	18A	KXRCLR	00	=		
XA138	TQ2	C1	19A	(38)	01		KXRCLS KXRRSA 40 19A 42 20A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM DESIGNATOR	FACTOR	COMMENT
XA139	TQ2	C1	18A	KXRCMS	00 =		I/O INPUT REG COMMAND BIT
XA139	TQ2	C1	19A	(38)	01	KXRCLR KXACMB4 40 19A 42 20A	
XA138	TQ2	C2	15A	KXRENR	00 =		
XA138	TQ2	C2	16A	(30)	01	KXRENS KXRRSA 34 16A 36 17A	
XA139	TQ2	C2	15A	KXRENS	00 =		
XA139	TQ2	C2	16A	(30)	01	KXRENR KXAENB4 34 16A 36 17A	
XA138	TQ2	C3	16B	KXRPCR	00 =		
XA138	TQ2	C3	14B	(33)	01	KXRPCS KXRRSA 29 14B 31 15B	
XA139	TQ2	C3	16B	KXRPCS	00 =		
XA139	TQ2	C3	14B	(33)	01	KXRPCR KXAPC84 29 14B 31 15B	
XA126	TQ2	A1	05A	KXRRSA	00 =		RESET I/O INPUT RE
XA126	TQ2	A1	06A	(06)	01	KXRRS0 SPI0151 08 06A 10 07A	
XA127	TQ2	A1	05A	KXRRS0	00 =		
XA127	TQ2	A1	06A	(06)	01	KXXA6A KXRS0B 08 06A 10 07A	
XA135	TQ2	A1	05A	KXRS0A	00 =		KXRS0B BUSS
XA135	TQ2	A1	06A	(06)	01	KXRS00 SPI0181 08 06A 10 07A	
XA135	TQ2	A1	05A	KXRS0B	00 =		MASTER RESET BUS 0
XA135	TQ2	A1	( )	( )	01	KXRS0A KXRS1A KXRS2A 06 05A 01 02B 09 04B	
XA138	TQ2	B4	13B	KXRS00	00 =		
XA138	TQ2	B4	11B	(27)	01	KXST1A SPI0181 23 11B 25 12B	
XA135	TQ2	A2	02B	KXRS1A	00 =		KXRS0B BUSS
XA135	TQ2	A2	04A	(01)	01	KXRS00 SPI0181 04 04A 05 03B	
XA126	TQ2	C1	18A	KXRS1B	00 =		MASTER RESET BUS 1
XA126	TQ2	C1	( )	( )	01	KXRS3A KXRS4A KXRS5A 38 18A 30 15A 33 16B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA135	TQ2	A3	04B	KXRS2A	00	=		
XA135	TQ2	A3	02A	(09)	01	=	KXRS00 SPI0181 03 02A 07 03A	KXRS0B BUSS
XA126	TQ2	C1	18A	KXRS3A	00	=		
XA126	TQ2	C1	19A	(38)	01	=	KXRS00 SPI0151 40 19A 42 20A	KXRS1B BUSS
XA126	TQ2	C2	15A	KXRS4A	00	=		
XA126	TQ2	C2	16A	(30)	01	=	KXRS00 SPI0151 34 16A 36 17A	KXRS1B BUSS
XA126	TQ2	C3	16B	KXRS5A	00	=		
XA126	TQ2	C3	14B	(33)	01	=	KXRS00 SPI0151 29 14B 31 15B	KXRS1B BUSS
XA138	TQ2	D1	24A	KXR0CR	00	=		
XA138	TQ2	D1	25A	(52)	01	=	KXR0CS KXRRSA 54 25A 56 26A	
XA139	TQ2	D1	24A	KXR0CS	00	=		
XA139	TQ2	D1	25A	(52)	01	=	KXR0CR KXA0CB4 54 25A 56 26A	
XA135	TQ2	F1	37B	KXROPA	00	=		I/O INPUT REG PARITY ERROR
XA135	TQ2	F1	38B	(75)	01	=	KXR0PPR SPI0181 77 38B 79 39B	
XA236	PAR	D1	26A	KXR0PPR	00	=		
XA236	PAR	D1	23B	(56)	01	=	KXR0CS KXR1CS KXR2CS KXR3CS KXR4CS KXR5CS KXR6CS KXR7CS 43 23B 45 24B 47 25B 49 26B 52 24A 50 23A 48 22A 46 21A	
XA236	PAR	D1	27B	( )	02	+	KXRPCS 51 27B	
XA133	DBC	B1	12A	KXR090T	00	=		
XA133	DBC	B1	10A	(24)	01	=	KXR7CS 20 10A	
XA133	DBC	B2	13A	KXR091T	00	=		
XA133	DBC	B2	11A	(26)	01	=	KXR6CS 22 11A	
XA133	DBC	B3	14A	KXR092T	00	=		
XA133	DBC	B3	09B	(27)	01	=	KXR5CS 19 09B	
XA133	DBC	B4	15A	KXR093T	00	=		
XA133	DBC	B4	10B	(30)	01	=	KXR4CS 21 10B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERN	DESIGNATOR	FACTOR	COMMENT
XA133	DBC	B5	16A	KXR094T	00	=		
XA133	DBC	B5		(33)	01		SPA 4T	
XA133	DBC	B6	11B	KXR095T	00	=		
XA133	DBC	B6		(23)	01		SPA 5T	
XA133	DBC	B7	12B	KXR096T	00	=		
XA133	DBC	B7		(25)	01		SPA 6T	
XA133	DBC	B8	13B	KXR097T	00	=		
XA133	DBC	B8		(29)	01		SPA 7T	
XA133	DBC	B9	14B	KXR098T	00	=		
XA133	DBC	B9		(31)	01		SPA 8T	
XA133	DBC	B0	15B	KXR099T	00	=		
XA133	DBC	B0		(34)	01		SPA 9T	
XA138	TQ2	D2	21A	KXR1CR	00	=		
XA138	TQ2	D2	22A	(46)	01		KXR1CS KXRRSA 48 22A 50 23A	
XA139	TQ2	D2	21A	KXR1CS	00	=		
XA139	TQ2	D2	22A	(46)	01		KXR1CR KXA1CB4 48 22A 50 23A	
XA138	TQ2	D3	24B	KXR2CR	00	=		
XA138	TQ2	D3	22B	(45)	01		KXR2CS KXRRSA 41 22B 43 23B	
XA139	TQ2	D3	24B	KXR2CS	00	=		
XA139	TQ2	D3	22B	(45)	01		KXR2CR KXA2CB4 41 22B 43 23B	
XA138	TQ2	D4	27B	KXR3CR	00	=		
XA138	TQ2	D4	25B	(51)	01		KXR3CS KXRRSA 47 25B 49 26B	
XA139	TQ2	D4	27B	KXR3CS	00	=		
XA139	TQ2	D4	25B	(51)	01		KXR3CR KXA3CB4 47 25B 49 26B	

CONNECTOR	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA138	TQ2	E1 31A	KXR4CR	00	=		
XA138	TQ2	E1 32A	(66)	01		KXR4CS KXRRSA 68 32A 70 33A	
XA139	TQ2	E1 31A	KXR4CS	00	=		
XA139	TQ2	E1 32A	(66)	01		KXR4CR KXA4CB4 68 32A 70 33A	
XA138	TQ2	E2 28A	KXR5CR	00	=		
XA138	TQ2	E2 29A	(60)	01		KXR5CS KXRRSA 62 29A 64 30A	
XA139	TQ2	E2 28A	KXR5CS	00	=		
XA139	TQ2	E2 29A	(60)	01		KXR5CR KXA5CB4 62 29A 64 30A	
XA138	TQ2	E3 30B	KXR6CR	00	=		
XA138	TQ2	E3 28B	(57)	01		KXR6CS KXRRSA 53 28B 55 29B	
XA139	TQ2	E3 30B	KXR6CS	00	=		
XA139	TQ2	E3 28B	(57)	01		KXR6CR KXA6CB4 53 28B 55 29B	
XA138	TQ2	E4 33B	KXR7CR	00	=		
XA138	TQ2	E4 31B	(63)	01		KXR7CS KXRRSA 59 31B 61 32B	
XA139	TQ2	E4 33B	KXR7CS	00	=		
XA139	TQ2	E4 31B	(63)	01		KXR7CR KXA7CB4 59 31B 61 32B	
XA122	TQ2	F3 35A	KXSK0A	00	=		
XA122	TQ2	F3 34B	(69)	01		KXXC2Q KXXC3P 65 34B 74 35B	
XA127	TQ2	C4 19B	KXSK00	00	=		I/O BUFFER REGISTER CLOCK
XA127	TQ2	C4 17B	(39)	01		KXSK0A SPI0161 35 17B 37 18B	
XA127	TQ2	D2 21A	KXSST0	00	=		
XA127	TQ2	D2 22A	(46)	01		KXR097T SPI0161 48 22A 50 23A	
XA114	TS8	A1 05B	KXS50A	00	=		SOFTWARE STOP COMM
XA114	TS8	A1 02B	(11)	01		KXDVSS KXXB20 KXXA0P KXXA1Q KXSST0 KXROPA SPI0021 SPI0011 01 02B 04 04A 05 03B 06 05A 07 03A 08 06A 10 07A 13 06B	

CONNECTOR	CRYPST GROUP	GROUP	TEST POINTS AND/OR	EQUATION	TERJ DESIG- NATOR	FACTOR	COMMENT
XA128	TDD	EI	19A	KXSTOI ( )	00 = 01	KXGNIA 40 19A	
XA128	TDD	EN	20A	KXSTON ( )	00 = 01	KXST1A 42 20A	
XA128	TDD	EP	17B	KXSTOP (35 )	00 = 01	SPI0161 38 18A	
XA128	TDD	EQ	18B	KXSTOQ (37 )	00 = 01	KXST2A 39 19B	STOP SYNCOUNTER BIT 0
XA123	TQ2	D3	24B	KXST1A (45 )	00 = 01	KXST1Q KXCP3B 41 22B 43 23B	
XA129	TDD	EI	19A	KXSTII ( )	00 = 01	KXSTOQ 40 19A	
XA129	TDD	EN	20A	KXSTIN ( )	00 = 01	KXCP1B 42 20A	
XA129	TDD	EP	17B	KXST1P (35 )	00 = 01	SPI0161 38 18A	
XA129	TDD	EQ	18B	KXST1Q (37 )	00 = 01	SPI0141 39 19B	STOP SYNC COUNTER 1
XA126	TQ2	D3	24B	KXST2A (45 )	00 = 01	KXST20 SPI0151 41 22B 43 23B	
XA131	TSB	B1	11B	KXST20 (23 )	00 = 01	KXHSOA KXSSOA KXARSA KXBRSA KXPRSA KDEVIA DEVINH SPI0161 14 09A 18 10A 19 09B 20 11A 22 12A 24 13A 25 12B 26 14A	START MASTER RESETOR GATE
XA134	DBC	E1	34A	KXS03IU (70 )	00 = 01	KXDBOTA KXDBOTB KXDBOTC KXDBOTD KXGNIA 71 33B 73 34B 75 35B 77 36B 80 38B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM.	DESIGNATOR	FACTOR	COMMENT
			AND	OR					
XA134	DBC	E2	35A		KXS032U	00	=		
XA134	DBC	E2	32A		(72)	01		KXSK00 69 32A	
XA134	DBC	E3	36A		KXS033U	00	=		
XA134	DBC	E3	37B		(74)	01		KXGN2A 79 37B	
XA134	DBC	E4	37A		KXS034U	00	=		
XA134	DBC	E4	38A		(76)	01		KXGN3A 78 38A	
XA134	DBC	E5	33A		KXS035U	00	=		I/O BUFFER REG BITS 0-1-2-3
XA134	DBC	E5	32B		(68)	01		SPI0181 66 32B	
XA133	DBC	E1	34A		KXS471U	00	=		
XA133	DBC	E1	33B		(70)	01		KXDB4TA KXDB4TB KXDB4TC KXDB4TD KXGN1A 71 33B 73 34B 75 35B 77 36B 80 38B	
XA133	DBC	E2	35A		KXS472U	00	=		
XA133	DBC	E2	32A		(72)	01		KXSK00 69 32A	
XA133	DBC	E3	36A		KXS473U	00	=		
XA133	DBC	E3	37B		(74)	01		KXGN2A 79 37B	
XA133	DBC	E4	37A		KXS474U	00	=		
XA133	DBC	E4	38A		(76)	01		KXGN3A 78 38A	
XA133	DBC	E5	33A		KXS475U	00	=		I/O BUFFER REG BITS 4-5-6-7
XA133	DBC	E5	32B		(68)	01		SPI0181 66 32B	
XA139	TQ2	C4	19B		KXXACA	00	=		
XA139	TQ2	C4	17B		(39)	01		KXXACO SPI0201 35 17B 37 18B	
XA128	TDD	AI	06A		KXXACI	00	=		
XA128	TDD	AI	06A		( )	01		SPI0161 08 06A	
XA128	TDD	AN	07A		KXXACN	00	=		
XA128	TDD	AN	07A		( )	01		KXXA3P 10 07A	

CONNECTOR	GROUP TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATION	FACTOR	COMMENT
XA128	TDD	AP	05B	KXXACP	00 =			
XA128	TDD	AP	05A	(11)	01		KXXADA 06 05A	
XA128	TDD	AQ	06B	KXXACQ	00 =			I/O STATE COUNTER CONTROL F/F
XA128	TDD	AQ	07B	(13)	01		KXRS0B 15 07B	
XA131	TSB	D1	25B	KXXA00	00 =			
XA131	TSB	D1	23B	(47)	01		KXROCR KXR1CR KXR2CR KXR3CR KXR4CR KXR5CR KXR6CR KXR7CR 43 23B 46 21A 48 22A 49 26B 50 23A 52 24A 54 25A 56 26A	
XA135	TQ2	F3	35A	KXXADA	00 =			START I/O STATE COUNTER
XA135	TQ2	F3	34B	(69)	01		KXXA00 SPI0181 65 34B 74 35B	
XA137	TD4	D1	25B	KXXA00	00 =			
XA137	TD4	D1	26B	(47)	01		KXRCMR KXRENK KXRPCR KXXACA 49 26B 52 24A 54 25A 56 26A	
				KXXA01	00 =			
XA129	TDD	AI	06A	( )	01		KXXA3P 08 06A	
				KXXA0N	00 =			
XA129	TDD	AN	07A	( )	01		K16MH0 10 07A	
XA129	TDD	AP	05B	KXXA0P	00 =			
XA129	TDD	AP	05A	(11)	01		KXXACP 06 05A	
XA129	TDD	AQ	06B	KXXA0Q	00 =			I/O STATE COUNTER BIT 0
XA129	TDD	AQ	07B	(13)	01		SPI0161 15 07B	
				KXXA1I	00 =			
XA130	TDD	AI	06A	( )	01		KXXA0Q 08 06A	
				KXXA1N	00 =			
XA130	TDD	AN	07A	( )	01		K16MH0 10 07A	
XA130	TDD	AP	05B	KXXA1P	00 =			
XA130	TDD	AP	05A	(11)	01		KXXACP 06 05A	



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA130	TDD	AQ	06B	KXXA1Q	00	=		I/O STATE COUNTER BIT 1
XA130	TDD	AQ	07B	(13)	01		SPI0161 15 07B	
				KXXA2I	00	=		
XA129	TDD	BI	03B	( )	01		KXXA1Q 05 03B	
				KXXA2N	00	=		
XA129	TDD	BN	02B	( )	01		K16MH0 01 02B	
XA129	TDD	BP	04B	KXXA2P	00	=		
XA129	TDD	BP	04A	(09)	01		KXXACP 04 04A	
				KXXA2Q	00	=		I/O STATE COUNTER BIT 2
XA129	TDD	BQ	03A	(07)	01		SPI0141 03 02A	
				KXXA3I	00	=		
XA130	TDD	BI	03B	( )	01		KXXA2Q 05 03B	
				KXXA3N	00	=		
XA130	TDD	BN	02B	( )	01		K16MH0 01 02B	
XA130	TDD	BP	04B	KXXA3P	00	=		
XA130	TDD	BP	04A	(09)	01		KXXACP 04 04A	
				KXXA3Q	00	=		I/O STATE COUNTER BIT 3
XA130	TDD	BQ	03A	(07)	01		SPI0141 03 02A	
				KXXA4A	00	=		
XA135	TQ2	E1	31A	(66)	01		KXXA0Q KXXA3Q 68 32A 70 33A	
				KXXA40	00	=		I/O STATE COUNTER STATE 4
XA139	TQ2	F4	39A	(80)	01		KXXA4A SPI0201 76 37A 78 38A	
				KXXA5A	00	=		
XA135	TQ2	E2	28A	(60)	01		KXXA0P KXXA1Q 62 29A 64 30A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA138	TQ2	F1	37B	KXXA50	00	=		I/O STATE COUNTER STATE 5
XA138	TQ2	F1	38B	(75)	01		KXXA5A SPI0181 77 38B 79 39B	
XA135	TQ2	E3	30B	KXXA6A	00	=		I/O STATE COUNTER STATE 6
XA135	TQ2	E3	28B	(57)	01		KXXA1P KXXA2Q 53 28B 55 29B	
XA138	TQ2	C4	19B	KXXB8CA	00	=		RESET I/O BYTE COUNTER
XA138	TQ2	C4	17B	(39)	01		KXXB8C0 SPI0181 35 17B 37 18B	
XA132	TT3	D1	23A	KXXB8C0	00	=		
XA132	TT3	D1	24A	(50)	01		KXRCMR KXRENK KXRS0B 52 24A 54 25A 56 26A	
XA127	TQ2	B1	12A	KXXB8K0	00	=		I/O BYTE COUNTER CLOCK
XA127	TQ2	B1	13A	(22)	01		KXXA3Q SPI0151 24 13A 26 14A	
XA135	TQ2	D1	24A	KXXB0A	00	=		
XA135	TQ2	D1	25A	(52)	01		KXXB0P KXXB2P 54 25A 56 26A	
XA128	TDD	BI	03B	KXXB0I ( )	00	=		
XA128	TDD	BI	03B	( )	01		KXXB2P 05 03B	
XA128	TDD	BN	02B	KXXB0N ( )	00	=		
XA128	TDD	BN	02B	( )	01		KXXB8K0 01 02B	
XA128	TDD	BP	04B	KXXB0P	00	=		
XA128	TDD	BP	04A	(09)	01		KXXB8CA 04 04A	
XA128	TDD	BQ	03A	KXXB0Q	00	=		I/O BYTE COUNTER BIT 0
XA128	TDD	BQ	02A	(07)	01		SPI0141 03 02A	
XA139	TQ2	F1	37B	KXXB00	00	=		I/O BYTE COUNTER STATE 0
XA139	TQ2	F1	38B	(75)	01		KXXB0A SPI0201 77 38B 79 39B	
XA135	TQ2	D2	21A	KXXB1A	00	=		
XA135	TQ2	D2	22A	(46)	01		KXXB0Q KXXB1P 48 22A 50 23A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA129	TDD	CI	13A	KXXB1I ( )	00 01	=	KXXB0Q 24 13A	
XA129	TDD	CN	14A	KXXB1N ( )	00 01	=	KXXBK0 26 14A	
XA129	TDD	CP	11B	KXXB1P	00	=		
XA129	TDD	CP	12A	(23 )	01		KXXBCA 22 12A	
XA129	TDD	CQ	12B	KXXB1Q	00	=		I/O BYTE COUNTER BIT 1
XA129	TDD	CQ	13B	(25 )	01		SPI0161 27 13B	
XA139	TQ2	F2	34A	KXXB10	00	=		I/O BYTE COUNTER STATE 1
XA139	TQ2	F2	36A	(72 )	01		KXXB1A SPI0201 71 36A 73 36B	
XA135	TQ2	D3	24B	KXXB2A	00	=		
XA135	TQ2	D3	22B	(45 )	01		KXXB1Q KXXB2P 41 22B 43 23B	
XA130	TDD	CI	13A	KXXB2I ( )	00 01	=	KXXB1Q 24 13A	
XA130	TDD	CN	14A	KXXB2N ( )	00 01	=	KXXBK0 26 14A	
XA130	TDD	CP	11B	KXXB2P	00	=		
XA130	TDD	CP	12A	(23 )	01		KXXBCA 22 12A	
XA130	TDD	CQ	12B	KXXB2Q	00	=		I/O BYTE COUNTER BIT 2
XA130	TDD	CQ	13B	(25 )	01		SPI0161 27 13B	
XA139	TQ2	F3	35A	KXXB20	00	=		I/O BYTE COUNTER STATE 2
XA139	TQ2	F3	34B	(69 )	01		KXXB2A SPI0201 65 34B 74 35B	
XA135	TQ2	D4	27B	KXXB3A	00	=		I/O BYTE COUNTER STATE 3
XA135	TQ2	D4	25B	(51 )	01		KXXB0Q KXXB2Q 47 25B 49 26B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XAI26	TQ2	F2	34A	KXXCIA	00	=		
XAI26	TQ2	F2	36A	( 72 )	01		KXXCIO SPI0151 71 36A 73 36B	
				KXXCII	00	=		
XAI29	TDD	GI	25A	( )	01		KXGN3A 54 25A	
				KXXCIN	00	=		
XAI29	TDD	GN	26A	( )	01		KXXC4P 56 26A	
XAI29	TDD	GP	25B	KXXCIP	00	=		
XAI29	TDD	GP	24A	( 47 )	01		KXRS0B 52 24A	
XAI29	TDD	GQ	26B	KXXCIQ	00	=		
XAI29	TDD	GQ	27B	( 49 )	01		KXXCIA 51 27B	
XAI15	TD4	F2	35A	KXXCIO	00	=		INDICATOR INPUT CO
XAI15	TD4	F2	36A	( 69 )	01		KSYN1A KXDV3A KXSSOA KXOD0A 71 36A 72 34A 73 36B 74 35B	
XAI25	TD4	E1	31B	KXXCRO	00	=		IOU INPUT STROBE
XAI25	TD4	E1	32B	( 59 )	01		KXXCIP KXXDDP KXXDIP KXXDSP 61 32B 66 31A 68 32A 70 33A	
XAI21	TQ2	F2	34A	KXXCSA	00	=		IOU INPUT STROBE
XAI21	TQ2	F2	36A	( 72 )	01		KXXC2Q KXXC4Q 71 36A 73 36B	
XAI19	TQ2	F4	39A	KXXCSO	00	=		
XAI19	TQ2	F4	37A	( 80 )	01		KXXCSA SPI0131 76 37A 78 38A	
				KXXC0I	00	=		
XAI30	TDD	GI	25A	( )	01		KXXC4P 54 25A	
				KXXCON	00	=		
XAI30	TDD	GN	26A	( )	01		K16MIO 56 26A	
XAI30	TDD	GP	25B	KXXCOP	00	=		
XAI30	TDD	GP	24A	( 47 )	01		KXXCRO 52 24A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERMINAL	DESIGNATOR	FACTOR	COMMENT
XA130	TDD	GQ	26B	KXXC00	00	=		IOU INPUT STROBE COUNT BIT 0
XA130	TDD	GQ	27B	(49)	01		SPI0161 51 27B	
				KXXC1I	00	=		
XA118	TDD	LI	38B	( )	01		KXXC0Q 77 38B	
				KXXC1N	00	=		
XA118	TDD	LN	39B	( )	01		K16M10 79 39B	
XA118	TDD	LP	37A	KXXC1P	00	=		
XA118	TDD	LP	37B	(76)	01		KXXCRO 75 37B	
XA118	TDD	LQ	38A	KXXC1Q	00	=		
XA118	TDD	LQ	39A	(78)	01		SPI0011 80 39A	
				KXXC2I	00	=		
XA130	TDD	HI	22A	( )	01		KXXC1Q 48 22A	
				KXXC2N	00	=		
XA130	TDD	HN	21A	( )	01		K16M10 46 21A	
XA130	TDD	HP	24B	KXXC2P	00	=		
XA130	TDD	HP	23A	(45)	01		KXXCRO 50 23A	
XA130	TDD	HQ	23B	KXXC2Q	00	=		
XA130	TDD	HQ	22B	(43)	01		SPI0141 41 22B	
				KXXC3I	00	=		
XA129	TDD	HI	22A	( )	01		KXXC2Q 48 22A	
				KXXC3N	00	=		
XA129	TDD	HN	21A	( )	01		K16M10 46 21A	
XA129	TDD	HP	24B	KXXC3P	00	=		
XA129	TDD	HP	23A	(45)	01		KXXCRO 50 23A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERY	DESIG. MATR	FACTOR	COMMENT
XA129	TDD	HQ	23B	KXXC3Q	00	=		
XA129	TDD	HQ	22B	(43)	01		SPI0161 41 22B	
				KXXC4I	00	=		
XA128	TDD	GI	25A	( )	01		KXXC3Q 54 25A	
				KXXC4N	00	=		
XA128	TDD	GN	26A	( )	01		K16M10 56 26A	
XA128	TDD	GP	25B	KXXC4P	00	=		
XA128	TDD	GP	24A	(47)	01		KXXCRO 52 24A	
XA128	TDD	GQ	26B	KXXC4Q	00	=		
XA128	TDD	GQ	27B	(49)	01		SPI0161 51 27B	IQU INPUT STROBE COUNT BIT 4
XA126	TQ2	F4	39A	KXXDDA	00	=		
XA126	TQ2	F4	37A	(80)	01		KXXDDO SPI0151 76 37A 78 38A	
				KXXDDI	00	=		
XA117	TDD	LI	38B	( )	01		KXGN3A 77 38B	
				KXXDDN	00	=		
XA117	TDD	LN	39B	( )	01		KXXC4P 79 39B	
XA117	TDD	LP	37A	KXXDDP	00	=		
XA117	TDD	LP	37B	(76)	01		KXRS0B 75 37B	
XA117	TDD	LQ	38A	KXXDDQ	00	=		
XA117	TDD	LQ	39A	(78)	01		KXXDDA 80 39A	IQU INPUT DATA CON
XA119	TQ2	F3	35A	KXXDDO	00	=		
XA119	TQ2	F3	34B	(69)	01		KRTDEA KLTEOA 65 34B 74 35B	
XA126	TQ2	F3	35A	KXXDIA	00	=		
XA126	TQ2	F3	34B	(69)	01		KXXDIS KXEA00 65 34B 74 35B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DEFINITION	FACTOR	COMMENT
XA118	TDD	MI	36A	KXXDII ( )	00 = 01		KXGN3A 71 36A	
XA118	TDD	MN	34A	KXXDIN ( )	00 = 01		KXXC4P 72 34A	
XA118	TDD	MP	35A	KXXDIP (69 )	00 = 01		KXRS08 73 36B	
XA118	TDD	MP	36B					
XA118	TDD	MQ	35B	KXXDIQ (74 )	00 = 01			IQU INPUT INTERRUPT
XA118	TDD	MQ	34B				KXXDIA 65 34B	
XA124	TT3	F3	39A	KXXDIR (80 )	00 = 01		KXXDIS KXINHR KXXDIP 69 35A 76 37A 78 38A	
XA124	TT3	F3	35A					
XA123	TQ2	F3	35A	KXXDIS (69 )	00 = 01			INTERRUPT WAIT FOR
XA123	TQ2	F3	34B				KXXDIR KINT2A 65 34B 74 35B	
XA139	TQ2	A1	05A	KXXDRA (06 )	00 = 01			DATA RECEIVE INHIBIT IF SEND
XA139	TQ2	A1	06A				KXXDR0 SPI0181 08 06A 10 07A	
XA132	TT3	A1	04A	KXXDR0 (04 )	00 = 01			
XA132	TT3	A1	05A				KXXDDP KXXDIP KXXDSP 06 05A 08 06A 10 07A	
XA117	TDD	MI	36A	KXXDSI ( )	00 = 01		KXGN3A 71 36A	
XA117	TDD	MN	34A	KXXDSN ( )	00 = 01		KXXC4P 72 34A	
XA117	TDD	MP	35A	KXXDSP (69 )	00 = 01		KXRS08 73 36B	
XA117	TDD	MP	36B					
XA117	TDD	MQ	35B	KXXDSQ (74 )	00 = 01			IQU INPUT IIR DATA
XA117	TDD	MQ	34B				KXIRIA 65 34B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERMINAL	DESIGNATOR	FACTOR	COMMENT
XA124	TT3	D3	27B	KXXRCA	00	=		
XA124	TT3	D3	24B	( 51 )	01		KXXRCO KXXREP KDEVIR 45 24B 47 25B 49 26B	SET IOU REQUEST CO
				KXXRCI	00	=		
XA117	TDD	JI	32A	( )	01		KXGN3A 68 32A	
				KXXRCN	00	=		
XA117	TDD	JN	33A	( )	01		KXXR2P 70 33A	
XA117	TDD	JP	31B	KXXRCP	00	=		
XA117	TDD	JP	31A	( 59 )	01		KXRS0B 66 31A	
XA117	TDD	JQ	32B	KXXRCQ	00	=		
XA117	TDD	JQ	33B	( 61 )	01		KXXRCA 63 33B	IOU REQUEST CONTRD
XA115	TD4	F1	37A	KXXRCO	00	=		
XA115	TD4	F1	37B	( 76 )	01		KAEN1A KLPT1A KIEN1A KINT2A 75 37B 77 38B 78 38A 79 39B	IOU REQUEST DR GAT
				KXXREI	00	=		
XA118	TDD	JI	32A	( )	01		KXGN3A 68 32A	
				KXXREN	00	=		
XA118	TDD	JN	33A	( )	01		KXEA0A 70 33A	
XA118	TDD	JP	31B	KXXREP	00	=		
XA118	TDD	JP	31A	( 59 )	01		KXRS0B 66 31A	
XA118	TDD	JQ	32B	KXXREQ	00	=		
XA118	TDD	JQ	33B	( 61 )	01		KXXROP 63 33B	IOU REQUEST ENABLE
				KXXROI	00	=		
XA130	TDD	JI	32A	( )	01		KXXR2P 68 32A	
				KXXRON	00	=		
XA130	TDD	JN	33A	( )	01		K16MHO 70 33A	



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA130	TDD	JP	31B	KXXR0P	00	=		
XA130	TDD	JP	31A	(59)	01		KXXRCQ 66 31A	
XA130	TDD	JQ	32B	KXXR0Q	00	=		I/O REQUEST COUNTER BIT 0
XA130	TDD	JQ	33B	(61)	01		SPI0161 63 33B	
XA129	TDD	J1	32A	KXXR1I	00	=		
XA129	TDD	J1	32A	( )	01		KXXR0Q 68 32A	
XA129	TDD	JN	33A	KXXR1N	00	=		
XA129	TDD	JN	33A	( )	01		K16MH0 70 33A	
XA129	TDD	JP	31B	KXXR1P	00	=		
XA129	TDD	JP	31A	(59)	01		KXXRCQ 66 31A	
XA129	TDD	JQ	32B	KXXR1Q	00	=		
XA129	TDD	JQ	33B	(61)	01		SPI0161 63 33B	
XA128	TDD	J1	32A	KXXR2I	00	=		
XA128	TDD	J1	32A	( )	01		KXXR1Q 68 32A	
XA128	TDD	JN	33A	KXXR2N	00	=		
XA128	TDD	JN	33A	( )	01		K16MH0 70 33A	
XA128	TDD	JP	31B	KXXR2P	00	=		
XA128	TDD	JP	31A	(59)	01		KXXRCQ 66 31A	
XA128	TDD	JQ	32B	KXXR2Q	00	=		I/O RESET COUNTER BIT 2
XA128	TDD	JQ	33B	(61)	01		SPI0161 63 33B	
XA135	TQ2	B2	09A	KXX04A	00	=		
XA135	TQ2	B2	10A	(14)	01		KXXB00 KXXA40 18 10A 20 11A	
XA137	TD4	F2	35A	KXX05A	00	=		I/O STATE IS 5, I/O BYTE IS 0
XA137	TD4	F2	36A	(69)	01		KXXB0P KXXB2P KXXA0P KXXA1Q 71 36A 72 34A 73 36B 74 35B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	FERR	DEFINITION	FACTOR	COMMENT
XA138	TQ2	F2	34A	KXX050	00	=		
XA138	TQ2	F2	36A	(72)	01		KXX05A SPIO181 71 36A 73 36B	
XA132	TT3	C3	19B	KX0DEA	00	=		DER DATA PARITY ERROR
XA132	TT3	C3	16B	(39)	01		KX0FRS KXROPPR KXXA50 33 16B 35 17B 37 18B	
XA123	TQ2	C4	19B	KX0DRA	00	=		DER RESET
XA123	TQ2	C4	17B	(39)	01		KX0FRS KXXB10 35 17B 37 18B	
XA132	TT3	D3	27B	KX0D0A	00	=		DER DATA STROBE
XA132	TT3	D3	24B	(51)	01		KX0FRS KXXB20 KXXA50 45 24B 47 25B 49 26B	
XA137	TD4	D2	24B	KX0FRR	00	=		
XA137	TD4	D2	23B	(45)	01		KX0FRS KXXB0A KXXB3A KXRS0B 43 23B 46 21A 48 22A 50 23A	
XA135	TQ2	C3	16B	KX0FRS	00	=		DER COMMAND F/E
XA135	TQ2	C3	14B	(33)	01		KX0FRR KX0ROA 29 14B 31 15B	
XA127	TQ2	C3	16B	KX0FRO	00	=		
XA127	TQ2	C3	14B	(33)	01		KX098T SPIO161 29 14B 31 15B	
XA127	TQ2	D4	27B	KX0NLO	00	=		ON LINE CONTROL
XA127	TQ2	D4	25B	(51)	01		KXASLA KXBSLA 47 25B 49 26B	
XA125	TD4	C2	16B	KX0ROA	00	=		COMMAND IS DER
XA125	TD4	C2	15A	(33)	01		KXCA10 KXDEVA KX0FRO KBUSYA 30 15A 31 15B 34 16A 36 17A	
XA130	TDD	FI	16A	KX1MAI	00	=		
XA130	TDD	FI	16A	( )	01		KX1MBP 34 16A	
XA130	TDD	FN	15A	KX1MAN	00	=		
XA130	TDD	FN	15A	( )	01		K04M20 30 15A	
XA130	TDD	FP	16B	KX1MAP	00	=		
XA130	TDD	FP	17A	(33)	01		SPIO141 36 17A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM.	DESIGNATOR	FACTOR	COMMENT
			AND	OR					
XA130	TDD	FQ	15B		KX1MAQ	00	=		PHASE CLOCK BIT 0
XA130	TDD	FQ	14B		(31)	01		SPI0031 29 14B	
XA129	TDD	FI	16A		KX1MBI	00	=		
XA129	TDD	FN	15A		( )	01		KX1MAQ 34 16A	
XA129	TDD	FP	16B		KX1MBN	00	=		
XA129	TDD	FP	17A		( )	01		K04MZO 30 15A	
XA129	TDD	FP	17A		KX1MBP	00	=		
XA129	TDD	FP	17A		(33)	01		SPI0031 36 17A	
XA129	TDD	FQ	15B		KX1MBQ	00	=		PHASE CLOCK BIT 1
XA129	TDD	FQ	14B		(31)	01		SPI0041 29 14B	
XA126	TQ2	E4	33B		KOUT0A	00	=		SEND KEYBOARD DATA
XA126	TQ2	E4	31B		(63)	01		K1ENRA K1OUTR 59 31B 61 32B	
XA139	TQ2	B4	13B		K04MZO	00	=		4 MHZ RECEIVER
XA139	TQ2	B4	11B		(27)	01		T04MHK SPI0201 23 11B 25 12B	
XA126	TQ2	E1	31A		K16MHA	00	=		16 MHZ RECEIVER
XA126	TQ2	E1	32A		(66)	01		T16MHA SPI0151 68 32A 70 33A	
XA127	TQ2	E3	30B		K16MH0	00	=		
XA127	TQ2	E3	28B		(57)	01		K16MHA SPI0161 53 28B 55 29B	
XA127	TQ2	E4	33B		K16MIO	00	=		
XA127	TQ2	E4	31B		(63)	01		K16MHA SPI0161 59 31B 61 32B	
XA209	TQ2	F1	37B		PART10	00	=		START 1, CPU-A OR CPU-B
XA209	TQ2	F1	38B		(75)	01		PSTR1P QSTR1P 77 38B 79 39B	
XA209	TQ2	F2	34A		PART20	00	=		START 2, CPU-A OR CPU-B
XA209	TQ2	F2	36A		(72)	01		PSTR2P QSTR2P 71 36A 73 36B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	RESISTOR	FACTOR	COMMENT
XA332	TT3	E1	30A	PBUSYR	00	=		
XA332	TT3	E1	31A	(64)	01		PBUSYS P0809A PXRS2B 66 31A 68 32A 70 33A	
XA334	TQ2	E4	33B	PBUSYS	00	=		
XA334	TQ2	E4	31B	(63)	01		P0405A PBUSYR 59 31B 61 32B	DEV INHIBIT. STATE 5-9
XA205	TT3	D1	23A	PC01CA	00	=		
XA205	TT3	D1	24A	(50)	01		PXCP1B PXIROQ PST000 52 24A 54 25A 56 26A	ITR SYNC. CPU-A. STATE 0
XA205	TT3	B2	09B	P0ARQA	00	=		
XA205	TT3	B2	09A	(19)	01		P0NL10 QXXDIR QXXR10 14 09A 17 08B 18 10A	REQUEST TO RECEIVER. CPU-A
XA328	TQ2	A1	05A	PDER90	00	=		
XA328	TQ2	A1	06A	(06)	01		PXR095T SPI0193 08 06A 10 07A	
XA328	TQ2	A2	02B	PDLCKA	00	=		
XA328	TQ2	A2	04A	(01)	01		P20DLP PX1MBQ 04 04A 05 03B	1 MHZ CLOCK. REQUEST DELAY TIMER
XA334	TQ2	F4	39A	P0LENA	00	=		
XA334	TQ2	F4	37A	(80)	01		P20DLQ PXXREP 76 37A 78 38A	REQ. DELAY ENDED. ENABLE RECEIVED. CPU-A
XA335	TQ2	F3	35A	P0LENO	00	=		
XA335	TQ2	F3	34B	(69)	01		P0LENA SPI0223 65 34B 74 35B	
XA325	TDD	FI	16A	PDLQ1I	00	=		
XA325	TDD	FI	16A	( )	01		PDLQ5P 34 16A	
XA325	TDD	FN	15A	PDLQ1N	00	=		
XA325	TDD	FN	15A	( )	01		PDLCKA 30 15A	
XA325	TDD	FP	16B	PDLQ1P	00	=		
XA325	TDD	FP	17A	(33)	01		PQDLCA 36 17A	
XA325	TDD	FQ	15B	PDLQ10	00	=		
XA325	TDD	FQ	14B	(31)	01		SPI0183 29 14B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS (AND/OR)	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
				PDLQ2I	00	H		
XA326	TDD	FI	16A	( )	01		PDLQ1Q 34 16A	
				PDLQ2N	00	=		
XA326	TDD	FN	15A	( )	01		PDLCKA 30 15A	
XA326	TDD	FP	16B	PDLQ2P	00	=		
XA326	TDD	FP	17A	(33 )	01		PQDLCA 36 17A	
XA326	TDD	FQ	15B	PDLQ2Q	00	=		
XA326	TDD	FQ	14B	(31 )	01		SPT0183 29 14B	
				PDLQ3I	00	=		
XA325	TDD	GI	25A	( )	01		PDLQ2Q 54 25A	
				PDLQ3N	00	=		
XA325	TDD	GN	26A	( )	01		PDLCKA 56 26A	
XA325	TDD	GP	25B	PDLQ3P	00	=		
XA325	TDD	GP	24A	(47 )	01		PQDLCA 52 24A	
XA325	TDD	GQ	26B	PDLQ3Q	00	=		
XA325	TDD	GQ	27B	(49 )	01		SPT0173 51 27B	
				PDLQ4I	00	=		
XA326	TDD	GI	25A	( )	01		PDLQ3Q 54 25A	
				PDLQ4N	00	=		
XA326	TDD	GN	26A	( )	01		PDLCKA 56 26A	
XA326	TDD	GP	25B	PDLQ4P	00	=		
XA326	TDD	GP	24A	(47 )	01		PQDLCA 52 24A	
XA326	TDD	GQ	26B	PDLQ4Q	00	=		
XA326	TDD	GQ	27B	(49 )	01		SPT0193 51 27B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA329	TDD	LI	38B	PDLQ5I ( )	00 01	=	PDLQ4Q 77 38B	
XA329	TDD	LN	39B	PDLQ5N ( )	00 01	=	PDLCKA 79 39B	
XA329	TDD	LP	37A	PDLQ5P (76 )	00 01	=	PQDLCA 75 37B	
XA329	TDD	LQ	38A	PDLQ5Q (78 )	00 01	=	SPI0203 80 39A	
XA328	TQ2	A3	04B	PDLQ6A (09 )	00 01	=	PDLQ6Q SPI0193 03 02A 07 03A	
XA326	TDD	HI	22A	PDLQ6I ( )	00 01	=	PDLQ6A 48 22A	
XA326	TDD	HN	21A	PDLQ6N ( )	00 01	=	PDLQ5P 46 21A	
XA326	TDD	HP	24B	PDLQ6P (45 )	00 01	=	PQDLCA 50 23A	
XA326	TDD	HQ	23B	PDLQ6Q (43 )	00 01	=	SPI0183 41 22B	
XA206	TQ2	B3	10B	PDLQ6R (21 )	00 01	=	PXR096T PXR095T 17 08B 19 09B	DATA ACCEPT OR REJECT, CPU=B
XA205	TT3	A3	07B	PDLQ6S (15 )	00 01	=	PONL10 PXXDIR PXXR1Q 09 04B 11 05B 13 06B	DATA REQ TO SENDER, CPU=A
XA206	TQ2	A4	07B	PDLQ6T (15 )	00 01	=	PLSD0A QLSD0A 11 05B 13 06B	SUM. LAST DATA BYTES TO RECEIVER CPU.

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERN	DESIGNATOR	FACTOR	COMMENT
XA206	TQ2	C1	18A	PDVAEO	00 =			SUM, DEV ACCEPT OR DEV ERROR, CPU-A
XA206	TQ2	C1	19A	(38)	01	PXR095T PXR094T 40 19A 42 20A		
XA201	TQ2	E1	31A	PDVCLA	00 =			
XA201	TQ2	E1	32A	(66)	01	PDVCL0 SPI0013 68 32A 70 33A		
XA202	TQ2	E1	31A	PDVCL0	00 =			CPU-A SENDER OR MASTER CLEAR
XA202	TQ2	E1	32A	(66)	01	PONLNP PXR518 68 32A 70 33A		
XA328	TQ2	A4	07B	PDVOFA	00 =			CPU-A RECEIVING SEND INDICAT- OR
XA328	TQ2	A4	05B	(15)	01	PRQ0FO QONL10 11 05B 13 06B		
XA328	TQ2	B1	12A	PDVONA	00 =			CPU-A SENDING SEND INDICATOR
XA328	TQ2	B1	13A	(22)	01	PONL10 PRQ0NO 24 13A 26 14A		
XA327	TQ2	D1	24A	PEBCAA	00 =			
XA327	TQ2	D1	25A	(52)	01	PEBCTTA SPI0193 54 25A 56 26A		
XA327	TQ2	D2	21A	PEBCBA	00 =			
XA327	TQ2	D2	22A	(46)	01	PEBCTTB SPI0193 48 22A 50 23A		
XA327	TQ2	D3	24B	PEBCCA	00 =			
XA327	TQ2	D3	22B	(45)	01	PEBCTTC SPI0193 41 22B 43 23B		
XA327	TQ2	D4	27B	PEBCDA	00 =			
XA327	TQ2	D4	25B	(51)	01	PEBCTTD SPI0193 47 25B 49 26B		
XA209	TQ2	E3	30B	PEBCKA	00 =			
XA209	TQ2	E3	28B	(57)	01	PEBCKO SPI0043 53 28B 55 29B		
XA327	TQ2	E4	33B	PEBCKO	00 =			
XA327	TQ2	E4	31B	(63)	01	PSTRBA PXEB1A 59 31B 61 32B		
XA338	MUX	E1	34B	PEBCTTA	00 =			MULTIPLEX FOB COUNTERS BIT 1, CPU-A OR CPU-B
XA338	MUX	E1	32A	(73)	01	QEBCT1U PEBCT1U PXGN1A PONL20 69 32A 71 33B 68 33A 66 32B		

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DEFIC FACTOR	FACTOR	COMMENT
			AND	OR					
XA338	MUX	E2	37B		PEBCTTB	00	=		
XA338	MUX	E2	35B		(79)	01		QEBCT2U PEBCT2U 75 35B 77 36B	
XA338	MUX	E3	36A		PEBCTTC	00	=		
XA338	MUX	E3	34A		(74)	01		QEBCT3U PEBCT3U 70 34A 72 35A	
XA338	MUX	E4	38B		PEBCTTD	00	=		
XA338	MUX	E4	37A		(80)	01		QEBCT4U PEBCT4U 76 37A 78 38A	
XA315	DBC	D1	26A		PEBCT1U	00	=		EQB COUNTER, CPU-A, BIT 1
XA315	DBC	D1	26B		(54)	01		PXR3CR PXR2CR PXR1CR PXROCR PXDEVR 53 26B 55 27B 57 29B 59 30B 63 31A	
XA315	DBC	D2	28B		PEBCT2U	00	=		
XA315	DBC	D2	25B		(56)	01		PEBCKA 51 25B	
XA315	DBC	D3	28A		PEBCT3U	00	=		
XA315	DBC	D3	31B		(60)	01		SPI0073 61 31B	
XA315	DBC	D4	29A		PEBCT4U	00	=		
XA315	DBC	D4	30A		(62)	01		SPI0233 64 30A	
XA315	DBC	D5	25A		PEBCT5U	00	=		
XA315	DBC	D5	24B		(52)	01		PONL20 49 24B	
XA209	TQ2	F4	39A		PEBSRA	00	=		
XA209	TQ2	F4	37A		(80)	01		PEBSR0 SPI0043 76 37A 78 38A	
XA208	TQ2	F2	34A		PEBSR0	00	=		STATE 2 OR CPU-A NOT SENDER
XA208	TQ2	F2	36A		(72)	01		PONL30 PSTDE2T 71 36A 73 36B	
XA206	TQ2	A1	05A		PECO40	00	=		SUM. ACCEPT OR REJECT DEV
XA206	TQ2	A1	06A		(06)	01		PTH04A QTH04A 08 06A 10 07A	STROBES
XA206	TQ2	A2	02B		PECO50	00	=		SUM. DATA REQUESTS TO SENDER
XA206	TQ2	A2	04A		(01)	01		PDTRQA QDTRQA 04 04A 05 03B	



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA206	TQ2	A3	04B	PEC070	00	=		
XA206	TQ2	A3	02A	(09)	01		QDARQA P DARQA 03 02A 07 03A	SUM, REQUESTS TO RECEIVER
XA206	TQ2	B1	12A	PEC100	00	=		
XA206	TQ2	B1	13A	(22)	01		PENDDA QENDDA 24 13A 26 14A	SUM, DEV STROBES, DEV ACCEPT OR DEV ERROR
XA206	TQ2	B2	09A	PEC110	00	=		
XA206	TQ2	B2	10A	(14)	01		PENFCA QENFCA 18 10A 20 11A	SUM, SENDER INTERRUPTS
XA209	TQ2	B1	12A	PEENBA	00	=		
XA209	TQ2	B1	13A	(22)	01		PEENBO SPI0033 24 13A 26 14A	
XA319	TD4	B1	11B	PEENBO	00	=		
XA319	TD4	B1	12A	(23)	01		PEN11A PST11A PEL05A PSTDE9T 22 12A 24 13A 25 12B 26 14A	SUM, ALL PRESETS
XA208	TQ2	A3	04B	PEL05A	00	=		
XA208	TQ2	A3	02A	(09)	01		PST080 PEN050 03 02A 07 03A	ENABLE XFER TO STATE 5
XA205	TT3	C1	17A	PENDDA	00	=		
XA205	TT3	C1	18A	(36)	01		QDVAE0 PONL10 QSTRBO 38 18A 40 19A 42 20A	DEV STROBE, ACCEPT OR REJECT, CPU-B RECEIVER
XA205	TT3	C3	19B	PENFCA	00	=		
XA205	TT3	C3	16B	(39)	01		PONL10 PXXDIS PXXR10 33 16B 35 17B 37 18B	INTERRUPT SENDER, CPU-A
XA209	TQ2	A2	02B	PEN050	00	=		
XA209	TQ2	A2	04A	(01)	01		PST05A QST05A 04 04A 05 03B	SUM, NO INTERRUPT
XA208	TQ2	A1	05A	PEN11A	00	=		
XA208	TQ2	A1	06A	(06)	01		PEN110 SPI0033 08 06A 10 07A	
XA323	TD4	A2	04B	PEN110	00	=		
XA323	TD4	A2	02B	(09)	01		PRJCTA QRJCTA POFRTA QOFRTA 01 02B 04 04A 05 03B 07 03A	SUM, CONDITIONS TO GO TO STATE 11
XA326	TDD	MI	36A	PE0B01	00	=		
XA326	TDD	MI	( )	( )	01		PEBCT5U 71 36A	CPU-A

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERN	DESIGNATOR	FACTOR	COMMENT
XA326	TDD	MN	34A	PEOB0N ( )	00 01	=	PXE810 72 34A	
XA326	TDD	MP	35A	PEOB0P (69 )	00 01	=	PEBSRA 73 36B	
XA326	TDD	MQ	35B	PEOB0Q (74 )	00 01	=	SPI0183 65 34B	
XA209	TQ2	F3	35A	PEOMSO (69 )	00 01	=	PEOB0P QEOB0P 65 34B 74 35B	END OF MESSAGE, CPU-A OR CPU-B
XA325	TDD	HI	22A	PLPTSI ( )	00 01	=	SPI0183 48 22A	
XA325	TDD	HN	21A	PLPTSN ( )	00 01	=	PLPT4Q 46 21A	
XA325	TDD	HP	24B	PLPTSP (45 )	00 01	=	PST110 50 23A	
XA325	TDD	HQ	23B	PLPTSQ (43 )	00 01	=	SPI0053 41 22B	
XA332	TT3	D3	27B	PLPT1A (51 )	00 01	=	PLTS2Q PDLEN0 PXCP3B 45 24B 47 25B 49 26B	SYNCED LOOP TEST PULSE, CPU-A
XA326	TDD	JI	32A	PLPT4I ( )	00 01	=	OXGN5A 68 32A	
XA326	TDD	JN	33A	PLPT4N ( )	00 01	=	PXEA0A 70 33A	
XA326	TDD	JP	31B	PLPT4P (59 )	00 01	=	PXRS0B 66 31A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERN	DESIGNATOR	FACTOR	COMMENT
XA326	TDD	JQ	32B	PLPT4Q	00	=		
XA326	TDD	JQ	33B	(61)	01		PLPT1A 63 33B	
XA319	TD4	D1	25B	PLSD0A	00	=		LAST DATA BYTE TO RECEIVER.
XA319	TD4	D1	26B	(47)	01		QONL10 PXXD50 PXXD0P PXXD1P 49 26B 52 24A 54 25A 56 26A	CPU-A
XA206	TQ2	E4	33B	PLSTRO	00	=		SUM, STROBES LAST BYTE FROM
XA206	TQ2	E4	31B	(63)	01		PXED3A QXED3A 59 31B 61 32B	SENDER
XA208	TQ2	E1	31A	PLST00	00	=		
XA208	TQ2	E1	32A	(66)	01		PII2EA QII2EA 68 32A 70 33A	
XA328	TQ2	B2	09A	PLTE0A	00	=		ENABLE DATA TO SENDER. (LOOP
XA328	TQ2	B2	10A	(14)	01		PXE00 PLPT4Q 18 10A 20 11A	TEST)
XA325	TDD	J1	32A	PLTS1I	00	=		
XA325	TDD	J1	32A	( )	01		PXGN4A 68 32A	
XA325	TDD	JN	33A	PLTS1N	00	=		
XA325	TDD	JN	33A	( )	01		PLPT1A 70 33A	
XA325	TDD	JP	31B	PLTS1P	00	=		
XA325	TDD	JP	31A	(59)	01		PXRS0B 66 31A	
XA325	TDD	JQ	32B	PLTS1Q	00	=		
XA325	TDD	JQ	33B	(61)	01		PX0D3A 63 33B	
XA326	TDD	K1	29A	PLTS2I	00	=		
XA326	TDD	K1	29A	( )	01		PLTS1Q 62 29A	
XA326	TDD	KN	28A	PLTS2N	00	=		
XA326	TDD	KN	28A	( )	01		PXCP1B 60 28A	
XA326	TDD	KP	30B	PLTS2P	00	=		
XA326	TDD	KP	30A	(57)	01		PXRS0B 64 30A	

CONNECTOR	CPU	GROUP	TEST POINTS		EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
			AND	OR					
XA326	TDD	KQ	29B		PLTS2Q	00	=		
XA326	TDD	KQ	28B		(55)	01		SPI0183 53 28B	
XA327	TQ2	A1	05A		PNLCMA	00	=		
XA327	TQ2	A1	06A		(06)	01		PXXCIO PPNLNQ 08 06A 1Q 07A	VALID COMMAND, CPU-A
XA332	TT3	D1	23A		PPERRR	00	=		
XA332	TT3	D1	24A		(50)	01		PPERRS PXR51B PPER1A 52 24A 54 25A 56 26A	
XA333	TD4	D2	24B		PPERRS	00	=		
XA333	TD4	D2	23B		(45)	01		QOLPEA PXDPEA PXODEA PPERRR 43 23B 46 21A 48 22A 50 23A	PARITY ERROR BIT, CPU-A
XA208	TQ2	A2	02B		PPER1A	00	=		
XA208	TQ2	A2	04A		(01)	01		PPER10 SPI0092 04 04A 05 03B	ENABLE XFER TO STATE 11
XA209	TQ2	C4	19B		PPER10	00	=		
XA209	TQ2	C4	17B		(39)	01		PNLCMA QNLCA 35 17B 37 18B	
XA336	TT3	E1	30A		PP10NA	00	=		
XA336	TT3	E1	31A		(64)	01		PDLENO PPNL20 PRJ100 66 31A 68 32A 70 33A	INTERRUPT CPU-A, CPU-A SENDER STATE 10
XA333	TD4	E1	31B		PP48QA	00	=		
XA333	TD4	E1	32B		(59)	01		PST480 PDLENO PE080P PPNL20 61 32B 66 31A 68 32A 70 33A	DATA REQUEST TO CPU-A, SENDER, STATE 4 OR 8
XA327	TQ2	A2	02B		PQDLCA	00	=		
XA327	TQ2	A2	04A		(01)	01		PXXR0Q SPI0193 04 04A 05 03B	START REQ. DELAY TIMER, CPU-A
XA206	TQ2	E2	28A		PQ0FNO	00	=		
XA206	TQ2	E2	29A		(60)	01		POFINA QQFINA 62 29A 64 30A	SUM. RECEIVER INTERRUPTS
XA336	TT3	E2	29B		PQ02NA	00	=		
XA336	TT3	E2	28B		(55)	01		PPNL20 QDLENO PST020 53 28B 60 28A 62 29A	INTERRUPT CPU-B, CPU-A SENDER STATE 2
XA336	TT3	E3	33B		PQ06QA	00	=		
XA336	TT3	E3	30B		(63)	01		QDLENO PPNL20 PST060 57 30B 59 31B 61 32B	DATA REQ. TO CPU-B, CPU-A SENDER, STATE 6

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DEFINITION	FACTOR	COMMENT
XA340	TD4	E1	31B	PQO8NA	00	=		INTERRUPT CPU-B, CPU-A SENDER
XA340	TD4	E1	32B	(59)	01		00LENO PST080 PE080Q P0NL20 61 32B 66 31A 68 32A 70 33A	STATE 8
XA323	TD4	B1	11B	PRCP00	00	=		
XA323	TD4	B1	12A	(23)	01		PXED0A PX0D0A QXED0A QX0D0A 22 12A 24 13A 25 12B 26 14A	
XA323	TD4	B2	10B	PRCP10	00	=		
XA323	TD4	B2	09A	(21)	01		PXED0A PX0D0A QXED0A QX0D0A 14 09A 18 10A 19 09B 20 11A	
XA323	TD4	C1	17B	PRCP20	00	=		
XA323	TD4	C1	18B	(35)	01		PXEDIA PX0D1A QXED1A QX0D1A 37 18B 38 18A 40 19A 42 20A	
XA323	TD4	C2	16B	PRCP30	00	=		
XA323	TD4	C2	15A	(33)	01		PXEDIA PX0D1A QXED1A QX0D1A 30 15A 31 15B 34 16A 36 17A	
XA323	TD4	D1	25B	PRCP40	00	=		
XA323	TD4	D1	26B	(47)	01		PXED2A PX0D2A QXED2A QX0D2A 49 26B 52 24A 54 25A 56 26A	
XA323	TD4	D2	24B	PRCP50	00	=		
XA323	TD4	D2	23B	(45)	01		PXED2A PX0D2A QXED2A QX0D2A 43 23B 46 21A 48 22A 50 23A	
XA323	TD4	E1	31B	PRCP60	00	=		
XA323	TD4	E1	32B	(59)	01		PXED3A PX0D3A QXED3A QX0D3A 61 32B 66 31A 68 32A 70 33A	
XA323	TD4	E2	30B	PRCP70	00	=		
XA323	TD4	E2	29B	(57)	01		PXED3A PX0D3A QXED3A QX0D3A 55 29B 60 28A 62 29A 64 30A	
XA337	MUX	D1	27B	PRDBOTA	00	=		
XA337	MUX	D1	25B	(55)	01		QXR0CS PXR0CS PXGNIA P0NLNQ 51 25B 53 26B 52 25A 49 24B	
XA337	MUX	D2	31B	PRDBOTB	00	=		
XA337	MUX	D2	29B	(61)	01		QXR1CS PXR1CS 57 29B 59 30B	
XA337	MUX	D3	28A	PRDBOTC	00	=		
XA337	MUX	D3	26A	(60)	01		QXR2CS PXR2CS 54 26A 56 28B	

CONNECTOR	CPU	GROUP	TEST POINTS AND OR	EQUATION	TERMINAL	DESIGNATION	FACTOR	COMMENT
XA337	MUX	D4	31A	PRDB0TD	00 =			
XA337	MUX	D4	29A	(63)	01		QXR3CS PXR3CS 62 29A 64 30A	
XA337	MUX	E1	34B	PRDB4TA	00 =			
XA337	MUX	E1	32A	(73)	01		QXR4CS PXR4CS PXGN1A PONLNQ 69 32A 71 33B 68 33A 66 32B	
XA337	MUX	E2	37B	PRDB4TB	00 =			
XA337	MUX	E2	35B	(79)	01		QXR5CS PXR5CS 75 35B 77 36B	
XA337	MUX	E3	36A	PRDB4TC	00 =			
XA337	MUX	E3	34A	(74)	01		QXR6CS PXR6CS 70 34A 72 35A	
XA337	MUX	E4	38B	PRDB4TD	00 =			
XA337	MUX	E4	37A	(80)	01		QXR7CS PXR7CS 76 37A 78 38A	
XA327	TQ2	A3	04B	PREL90	00 =			
XA327	TQ2	A3	02A	(09)	01		PXRAF5T SPI0193 03 02A 07 03A	
XA332	TT3	B2	09B	PRJCTA	00 =			RECEIVER REJECT, CPU-A SENDER
XA332	TT3	B2	09A	(19)	01		PONLNQ QPERRS PST040 14 09A 17 08B 18 10A	
XA327	TQ2	A4	07B	PRJ100	00 =			DATA REJECT, CPU-B RECEIVING
XA327	TQ2	A4	05B	(15)	01		PRJCTA PST10A 11 05B 13 06B	
XA327	TQ2	B1	12A	PRG0F0	00 =			ENABLE INDICATOR TO RECEIVER
XA327	TQ2	B1	13A	(22)	01		P0304A P0910A 24 13A 26 14A	CPU
XA336	TT3	F2	35B	PRQ0N0	00 =			ENABLE INDICATOR TO SENDER
XA336	TT3	F2	34B	(74)	01		P0102A P1102A P1100A 65 34B 71 36A 72 34A	CPU
XA202	TQ2	F1	37B	PRRS0A	00 =			
XA202	TQ2	F1	38B	(75)	01		PRRS00 SPI0033 77 38B 79 39B	
XA203	TT3	C2	15B	PRRS00	00 =			
XA203	TT3	C2	14B	(31)	01		PXODRA QXODRA PXR52B 29 14B 30 15A 34 16A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA202	TQ2	F2	34A	PRRS1A	00	=		
XA202	TQ2	F2	36A	(72)	01		PRRS00 SPI0033 71 36A 73 36B	
XA202	TQ2	F3	35A	PRRS2A	00	=		
XA202	TQ2	F3	34B	(69)	01		PRRS00 SPI0033 65 34B 74 35B	
XA202	TQ2	F4	39A	PRRS3A	00	=		
XA202	TQ2	F4	37A	(80)	01		PRRS00 SPI0033 76 37A 78 38A	
XA201	TQ2	F1	37B	PRRS4A	00	=		
XA201	TQ2	F1	38B	(75)	01		PRRS00 SPI0013 77 38B 79 39B	
XA201	TQ2	F2	34A	PRRS5A	00	=		
XA201	TQ2	F2	36A	(72)	01		PRRS00 SPI0013 71 36A 73 36B	
XA201	TQ2	F3	35A	PRRS6A	00	=		
XA201	TQ2	F3	34B	(69)	01		PRRS00 SPI0013 65 34B 74 35B	
XA201	TQ2	F4	39A	PRRS7A	00	=		
XA201	TQ2	F4	37A	(80)	01		PRRS00 SPI0013 76 37A 78 38A	
XA327	TQ2	B2	09A	PRTDEA	00	=		ENABLE DATA TO RECEIVER, CPU-B
XA327	TQ2	B2	10A	(14)	01		PXEAO0 PST070 18 10A 20 11A	
XA205	TT3	E2	29B	PRT12A	00	=		START DEV STROBE, CPU-A
XA205	TT3	E2	28B	(55)	01		PSTRTO PONLNQ PXDVB 53 28B 60 28A 62 29A	
XA322	TDD	AI	06A	PROOBI	00	=		
XA322	TDD	AI		( )	01		PRDBOTA 08 06A	
XA322	TDD	AN	07A	PROOBN	00	=		
XA322	TDD	AN		( )	01		PRCP00 10 07A	
XA322	TDD	AP	05B	PROOBP	00	=		
XA322	TDD	AP	05A	(11)	01		PRRS0A 06 05A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	REGIS. FACTOR	FACTOR	COMMENT
XA322	TDD	AQ	06B	PR00BQ	00	=		
XA322	TDD	AQ	07B	(13)	01		SPI0173 15 07B	
				PR01BI	00	=		
XA322	TDD	BI	03B	( )	01		PRDB0TB 05 03B	
				PR01BN	00	=		
XA322	TDD	BN	02B	( )	01		PRCP00 01 02B	
XA322	TDD	BP	04B	PR01BP	00	=		
XA322	TDD	BP	04A	(09)	01		PRRSOA 04 04A	
XA322	TDD	BQ	03A	PR01BQ	00	=		
XA322	TDD	BQ	02A	(07)	01		SPI0183 03 02A	
				PR02BI	00	=		
XA320	TDD	CI	13A	( )	01		PRDB0TC 24 13A	
				PR02BN	00	=		
XA320	TDD	CN	14A	( )	01		PRCP00 26 14A	
XA320	TDD	CP	11B	PR02BP	00	=		
XA320	TDD	CP	12A	(23)	01		PRRSOA 22 12A	
XA320	TDD	CQ	12B	PR02BQ	00	=		
XA320	TDD	CQ	13B	(25)	01		SPI0173 27 13B	
				PR03BI	00	=		
XA320	TDD	DI	10A	( )	01		PRDB0TD 18 10A	
				PR03BN	00	=		
XA320	TDD	DN	09A	( )	01		PRCP00 14 09A	
XA320	TDD	DP	10B	PR03BP	00	=		
XA320	TDD	DP	11A	(21)	01		PRRSOA 20 11A	



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
			AND	OR					
XA320	TDD	DQ	09B		PRO38Q	00	=		
XA320	TDD	DQ	08B		(19)	01		SPI0023 17 08B	
					PRO4BI	00	=		
XA321	TDD	GI	25A		( )	01		PRDB4TA 54 25A	
					PRO4BN	00	=		
XA321	TDD	GN	26A		( )	01		PRCP10 56 26A	
XA321	TDD	GP	25B		PRO4BP	00	=		
XA321	TDD	GP	24A		(47)	01		PRRSIA 52 24A	
XA321	TDD	GQ	26B		PRO4BQ	00	=		
XA321	TDD	GQ	27B		(49)	01		SPI0173 51 27B	
					PRO5BI	00	=		
XA321	TDD	HI	22A		( )	01		PRDB4TB 48 22A	
					PRO5BN	00	=		
XA321	TDD	HN	21A		( )	01		PRCP10 46 21A	
XA321	TDD	HP	24B		PRO5BP	00	=		
XA321	TDD	HP	23A		(45)	01		PRRSIA 50 23A	
XA321	TDD	HQ	23B		PRO5BQ	00	=		
XA321	TDD	HQ	22B		(43)	01		SPI0183 41 22B	
					PRO6BI	00	=		
XA322	TDD	GI	25A		( )	01		PRDB4TC 54 25A	
					PRO6BN	00	=		
XA322	TDD	GN	26A		( )	01		PRCP10 56 26A	
XA322	TDD	GP	25B		PRO6BP	00	=		
XA322	TDD	GP	24A		(47)	01		PRRSIA 52 24A	

CONNECTOR	CONNECTOR TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERMINAL	DESIGNATION	FACTOR	COMMENT
XA322	TDD	GQ	26B	PR06BQ	00 =			
XA322	TDD	GQ	27B	( 49 )	01		SPI0173 51 27B	
				PR07BI	00 =			
XA322	TDD	HI	22A	( )	01		PRDB4TD 48 22A	
				PR07BN	00 =			
XA322	TDD	HN	21A	( )	01		PRCP10 46 21A	
				PR07BP	00 =			
XA322	TDD	HP	24B	( 45 )	01		PRRS1A 50 23A	
				PR07BQ	00 =			
XA322	TDD	HQ	23B	( 43 )	01		SPI0183 41 22B	
				PR08BI	00 =			
XA321	TDD	CI	13A	( )	01		PRDB0TA 24 13A	
				PR08BN	00 =			
XA321	TDD	CN	14A	( )	01		PRCP20 26 14A	
				PR08BP	00 =			
XA321	TDD	CP	11B	( 23 )	01		PRRS2A 22 12A	
				PR08BQ	00 =			
XA321	TDD	CQ	12B	( 25 )	01		SPI0173 27 13B	
				PR09BI	00 =			
XA321	TDD	DI	10A	( )	01		PRDB0TB 18 10A	
				PR09BN	00 =			
XA321	TDD	DN	09A	( )	01		PRCP20 14 09A	
				PR09BP	00 =			
XA321	TDD	DP	10B	( 21 )	01		PRRS2A 20 11A	
XA321	TDD	DP	11A	( 21 )	01			

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
			AND	OR					
XA321	TDD	DQ	09B		PR09BQ	00	=		
XA321	TDD	DQ	08B		(19)	01		SPI0183 17 08B	
					PR10BI	00	=		
XA322	TDD	CI	13A		( )	01		PRDB0TC 24 13A	
					PR10BN	00	=		
XA322	TDD	CN	14A		( )	01		PRCP20 26 14A	
XA322	TDD	CP	11B		PR10BP	00	=		
XA322	TDD	CP	12A		(23)	01		PRRS2A 22 12A	
XA322	TDD	CQ	12B		PR10BQ	00	=		
XA322	TDD	CQ	13B		(25)	01		SPI0173 27 13B	
					PR11BI	00	=		
XA322	TDD	DI	10A		( )	01		PRDB0TD 18 10A	
					PR11BN	00	=		
XA322	TDD	DN	09A		( )	01		PRCP20 14 09A	
XA322	TDD	DP	10B		PR11BP	00	=		
XA322	TDD	DP	11A		(21)	01		PRRS2A 20 11A	
XA322	TDD	DQ	09B		PR11BQ	00	=		
XA322	TDD	DQ	08B		(19)	01		SPI0183 17 08B	
					PR12BI	00	=		
XA320	TDD	J1	32A		( )	01		PRDB4TA 68 32A	
					PR12BN	00	=		
XA320	TDD	JN	33A		( )	01		PRCP30 70 33A	
XA320	TDD	JP	31B		PR12BP	00	=		
XA320	TDD	JP	31A		(59)	01		PRRS3A 66 31A	

CONNECTOR	TEST POINT	GROUP	TEST POINTS AND/OR	EQUATION	TEST POINT	DESIGNATOR	FACTOR	COMMENT
XA320	TDD	JQ	32B	PR12BQ	00 =			
XA320	TDD	JQ	33B	( 61 )	01		SPI0173 63 33B	
				PR13BI	00 =			
XA320	TDD	KI	29A	( )	01		PRDB4TB 62 29A	
				PR13BN	00 =			
XA320	TDD	KN	28A	( )	01		PRCP30 60 28A	
XA320	TDD	KP	30B	PR13BP	00 =			
XA320	TDD	KP	30A	( 57 )	01		PRRS3A 64 30A	
XA320	TDD	KQ	29B	PR13BQ	00 =			
XA320	TDD	KQ	28B	( 55 )	01		SPI0183 53 28B	
				PR14BI	00 =			
XA321	TDD	JI	32A	( )	01		PRDB4TC 68 32A	
				PR14BN	00 =			
XA321	TDD	JN	33A	( )	01		PRCP30 70 33A	
XA321	TDD	JP	31B	PR14BP	00 =			
XA321	TDD	JP	31A	( 59 )	01		PRRS3A 66 31A	
XA321	TDD	JQ	32B	PR14BQ	00 =			
XA321	TDD	JQ	33B	( 61 )	01		SPI0173 63 33B	
				PR15BI	00 =			
XA321	TDD	KI	29A	( )	01		PRDB4TD 62 29A	
				PR15BN	00 =			
XA321	TDD	KN	28A	( )	01		PRCP30 60 28A	
XA321	TDD	KP	30B	PR15BP	00 =			
XA321	TDD	KP	30A	( 57 )	01		PRRS3A 64 30A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA321	TDD	KQ	29B	PR15BQ	00	=		
XA321	TDD	KQ	28B	(55)	01		SPI0183 53 28B	
				PR16BI	00	=		
XA320	TDD	EI	19A	( )	01		PRD80TA 40 19A	
				PR16BN	00	=		
XA320	TDD	EN	20A	( )	01		PRCP40 42 20A	
XA320	TDD	EP	17B	PR16BP	00	=		
XA320	TDD	EP	18A	(35)	01		PRRS4A 38 18A	
XA320	TDD	EQ	18B	PR16BQ	00	=		
XA320	TDD	EQ	19B	(37)	01		SPI0173 39 19B	
				PR17BI	00	=		
XA320	TDD	FI	16A	( )	01		PRD80TB 34 16A	
				PR17BN	00	=		
XA320	TDD	FN	15A	( )	01		PRCP40 30 15A	
XA320	TDD	FP	16B	PR17BP	00	=		
XA320	TDD	FP	17A	(33)	01		PRRS4A 36 17A	
XA320	TDD	FQ	15B	PR17BQ	00	=		
XA320	TDD	FQ	14B	(31)	01		SPI0023 29 14B	
				PR18BI	00	=		
XA321	TDD	EI	19A	( )	01		PRD80TC 40 19A	
				PR18BN	00	=		
XA321	TDD	EN	20A	( )	01		PRCP40 42 20A	
XA321	TDD	EP	17B	PR18BP	00	=		
XA321	TDD	EP	18A	(35)	01		PRRS4A 38 18A	

CONNECTOR	TEST POINTS AND/OR	EQUATION	TERMINAL	DESIGNATOR	FACTOR	COMMENT
XA321	TDD EQ 18B	PR188C	00 =			
XA321	TDD EQ 19B	(37)	01		SPI0173 39 19B	
		PR198I	00 =			
XA321	TDD FI 16A	( )	01		PRDB0TD 34 16A	
		PR198N	00 =			
XA321	TDD FN 15A	( )	01		PRCP40 30 15A	
		PR198P	00 =			
XA321	TDD FP 16B	(33)	01		PRRS4A 36 17A	
XA321	TDD FP 17A					
		PR198Q	00 =			
XA321	TDD FQ 15B	(31)	01		SPI0183 29 14B	
XA321	TDD FQ 14B					
		PR208I	00 =			
XA322	TDD JI 32A	( )	01		PRDB4TA 68 32A	
		PR208N	00 =			
XA322	TDD JN 33A	( )	01		PRCP50 70 33A	
		PR208P	00 =			
XA322	TDD JP 31B	(59)	01		PRRS5A 66 31A	
XA322	TDD JP 31A					
		PR208Q	00 =			
XA322	TDD JQ 32B	(61)	01		SPI0173 63 33B	
XA322	TDD JQ 33B					
		PR218I	00 =			
XA322	TDD KI 29A	( )	01		PRDB4TB 62 29A	
		PR218N	00 =			
XA322	TDD KN 28A	( )	01		PRCP50 60 28A	
		PR218P	00 =			
XA322	TDD KP 30B	(57)	01		PRRS5A 64 30A	
XA322	TDD KP 30A					

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA322	TDD	KQ	29B	PR218Q	00	#		
XA322	TDD	KQ	28B	(55)	01		SPI0183 53 28B	
				PR228I	00	=		
XA320	TDD	LI	38B	( )	01		PRDB4TC 77 38B	
				PR228N	00	=		
XA320	TDD	LN	39B	( )	01		PRCP50 79 39B	
XA320	TDD	LP	37A	PR228P	00	=		
XA320	TDD	LP	37B	(76)	01		PRRS5A 75 37B	
XA320	TDD	LQ	38A	PR228Q	00	=		
XA320	TDD	LQ	39A	(78)	01		SPI0173 80 39A	
				PR238I	00	=		
XA320	TDD	MI	36A	( )	01		PRDB4TD 71 36A	
				PR238N	00	=		
XA320	TDD	MN	34A	( )	01		PRCP50 72 34A	
XA320	TDD	MP	35A	PR238P	00	=		
XA320	TDD	MP	36B	(69)	01		PRRS5A 73 36B	
XA320	TDD	MQ	35B	PR238Q	00	=		
XA320	TDD	MQ	34B	(74)	01		SPI0183 65 34B	
				PR248I	00	=		
XA322	TDD	EI	19A	( )	01		PRDB0TA 40 19A	
				PR248N	00	=		
XA322	TDD	EN	20A	( )	01		PRCP60 42 20A	
XA322	TDD	EP	17B	PR248P	00	=		
XA322	TDD	EP	18A	(35)	01		PRRS6A 38 18A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA322	TDD	EQ	18B	PR248C	00	=		
XA322	TDD	EQ	19B	( 37 )	01		SPI0173 39 19B	
				PR258I	00	=		
XA322	TDD	FI	16A	( )	01		PRDB0TB 34 16A	
				PR258N	00	=		
XA322	TDD	FN	15A	( )	01		PRCP60 30 15A	
XA322	TDD	FP	16B	PR258P	00	=		
XA322	TDD	FP	17A	( 33 )	01		PRRS6A 36 17A	
XA322	TDD	FQ	15B	PR258C	00	=		
XA322	TDD	FQ	14B	( 31 )	01		SPI0183 29 14B	
				PR268I	00	=		
XA320	TDD	GI	25A	( )	01		PRDB0TC 54 25A	
				PR268N	00	=		
XA320	TDD	GN	26A	( )	01		PRCP60 56 26A	
XA320	TDD	GP	25B	PR268P	00	=		
XA320	TDD	GP	24A	( 47 )	01		PRRS6A 52 24A	
XA320	TDD	GQ	26B	PR268C	00	=		
XA320	TDD	GQ	27B	( 49 )	01		SPI0173 51 27B	
				PR278I	00	=		
XA320	TDD	HI	22A	( )	01		PRDB0TD 48 22A	
				PR278N	00	=		
XA320	TDD	HN	21A	( )	01		PRCP60 46 21A	
XA320	TDD	HP	24B	PR278P	00	=		
XA320	TDD	HP	23A	( 45 )	01		PRRS6A 50 23A	



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIG-NATOR	FACTOR	COMMENT
XA320	TDD	HQ	23B	PR27BQ	00	=		
XA320	TDD	HQ	22B	(43)	01		SPI0183 41 22B	
				PR28BI	00	=		
XA321	TDD	LI	38B	( )	01		PRDB4TA 77 38B	
				PR28BN	00	=		
XA321	TDD	LN	39B	( )	01		PRCP70 79 39B	
XA321	TDD	LP	37A	PR28BP	00	=		
XA321	TDD	LP	37B	(76)	01		PRRS7A 75 37B	
XA321	TDD	LQ	38A	PR28BQ	00	=		
XA321	TDD	LQ	39A	(78)	01		SPI0173 80 39A	
				PR29BI	00	=		
XA321	TDD	MI	36A	( )	01		PRDB4TB 71 36A	
				PR29BN	00	=		
XA321	TDD	MN	34A	( )	01		PRCP70 72 34A	
XA321	TDD	MP	35A	PR29BP	00	=		
XA321	TDD	MP	36B	(69)	01		PRRS7A 73 36B	
XA321	TDD	MQ	35B	PR29BQ	00	=		
XA321	TDD	MQ	34B	(74)	01		SPI0183 65 34B	
				PR30BI	00	=		
XA322	TDD	LI	38B	( )	01		PRDB4TC 77 38B	
				PR30BN	00	=		
XA322	TDD	LN	39B	( )	01		PRCP70 79 39B	
XA322	TDD	LP	37A	PR30BP	00	=		
XA322	TDD	LP	37B	(76)	01		PRRS7A 75 37B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA322	TDD	LQ	38A	PR30BQ	00	=		
XA322	TDD	LQ	39A	(78)	01		SPI0173 80 39A	
				PR31BI	00	=		
XA322	TDD	MI	36A	( )	01		PRDB4TD 71 36A	
				PR31BN	00	=		
XA322	TDD	MN	34A	( )	01		PRCP70 72 34A	
XA322	TDD	MP	35A	PR31BP	00	=		
XA322	TDD	MP	36B	(69)	01		PRRS7A 73 36B	
XA322	TDD	MQ	35B	PR31BQ	00	=		
XA322	TDD	MQ	34B	(74)	01		SPI0183 65 34B	
XA327	TQ2	C4	19B	PSCQ00	00	=		
XA327	TQ2	C4	17B	(39)	01		PSTCT1U SPI0193 35 17B 37 18B	BIT 1, STATE COUNTER
XA209	TQ2	B4	13B	PSCSMA	00	=		
XA209	TQ2	B4	11B	(27)	01		PSCSMO SPI0033 23 11B 25 12B	
XA208	TQ2	C2	15A	PSCSMO	00	=		
XA208	TQ2	C2	16A	(30)	01		P1CSMA P2CSMA 34 16A 36 17A	SUM, STATE COUNTER CLOCKS
XA206	TQ2	E1	31A	PSR120	00	=		
XA206	TQ2	E1	32A	(66)	01		PRT12A QRT12A 68 32A 70 33A	SUM, START 1, START 2 DEV'S
XA315	DBC	C1	18A	PSTCT1U	00	=		
XA315	DBC	C1	18B	(38)	01		POENBA PEL05A PST11A P3ENBA PEENBA 39 18B 41 19B 43 22B 45 23B 50 24A	BIT 1, STATE COUNTER
XA315	DBC	C2	19A	PSTCT2U	00	=		
XA315	DBC	C2	17B	(40)	01		PSCSMA 37 17B	
XA315	DBC	C3	20A	PSTCT3U	00	=		
XA315	DBC	C3	23A	(42)	01		SPI0073 47 23A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA315	DBC	C4	21A	PSTCT4U	00	=		
XA315	DBC	C4	22A	(46)	01		SPIG233 48 22A	
XA315	DBC	C5	17A	PSTCT5U	00	=		
XA315	DBC	C5	16B	(36)	01		PSTRSA 35 16B	
XA316	DBC	A1	04A	PSTDE0T	00	=		STATE 0 DECODE
XA316	DBC	A1	02A	(08)	01		PSTCT1U 04 02A	
XA316	DBC	A2	05A	PSTDE1T	00	=		
XA316	DBC	A2	03A	(10)	01		PSTCT2U 06 03A	
XA316	DBC	A3	06A	PSTDE2T	00	=		
XA316	DBC	A3	02B	(14)	01		PSTCT3U 03 02B	
XA316	DBC	A4	07A	PSTDE3T	00	=		
XA316	DBC	A4	03B	(13)	01		PSTCT4U 05 03B	
XA316	DBC	A5	08B	PSTDE4T	00	=		
XA316	DBC	A5		(17)	01		SPA 4T	
XA316	DBC	A6	04B	PSTDE5T	00	=		
XA316	DBC	A6		(07)	01		SPA 5T	
XA316	DBC	A7	05B	PSTDE6T	00	=		
XA316	DBC	A7		(09)	01		SPA 6T	
XA316	DBC	A8	06B	PSTDE7T	00	=		
XA316	DBC	A8		(11)	01		SPA 7T	
XA316	DBC	A9	07B	PSTDE8T	00	=		
XA316	DBC	A9		(15)	01		SPA 8T	
XA316	DBC	A0	09A	PSTDE9T	00	=		
XA316	DBC	A0		(18)	01		SPA 9T	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERMINAL	DESIGNATOR	FACTOR	COMMENT
XA324	TS8	A1	05B	PSTRBA	00	=		
XA324	TS8	A1	02B	(11)	01	.	PXXA0Q PXXA3Q PXXB20 PXDEVS PXROPA SPI0253 SPI0263 SPI0153 01 02B 04 04A 05 03B 06 05A 07 03A 08 06A 10 07A 13 06B	
XA208	TQ2	E3	30B	PSTR80	00	=		
XA208	TQ2	E3	28B	(57)	01	.	PSTRBA SPI0033 53 28B 55 29B	
XA334	TQ2	F3	35A	PSTRSA	00	=		
XA334	TQ2	F3	34B	(69)	01	.	PONLNP QONLNP 65 34B 74 35B	
XA206	TQ2	F2	34A	PSTRTO	00	=		START 1 OR START 2. CPU-A
XA206	TQ2	F2	36A	(72)	01	.	PXRAF6T PXRAF7T 71 36A 73 36B	
				PSTR1I	00	=		
XA325	TDD	LI	33B	( )	01	.	PSTR80 77 38B	
				PSTR1N	00	=		
XA325	TDD	LN	39B	( )	01	.	PX1110 79 39B	
XA325	TDD	LP	37A	PSTR1P	00	=		
XA325	TDD	LP	37B	(76)	01	.	PXRS0B 75 37B	
XA325	TDD	LQ	38A	PSTR1Q	00	=		
XA325	TDD	LQ	39A	(78)	01	.	SPI0193 80 39A	
				PSTR2I	00	=		
XA325	TDD	MI	36A	( )	01	.	PSTR90 71 36A	
				PSTR2N	00	=		
XA325	TDD	MN	34A	( )	01	.	PX1110 72 34A	
XA325	TDD	MP	35A	PSTR2P	00	=		
XA325	TDD	MP	36B	(69)	01	.	QDVCLA 73 36B	
XA325	TDD	MQ	35B	PSTR2Q	00	=		
XA325	TDD	MQ	34B	(74)	01	.	SPI0183 65 34B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA328	TQ2	B3	10B	PSTR80	00	=		
XA328	TQ2	B3	08B	(21)	01		PXRAF6T SPI0193 17 08B 19 09B	
XA328	TQ2	B4	13B	PSTR90	00	=		
XA328	TQ2	B4	11B	(27)	01		PXRAF7T SPI0193 23 11B 25 12B	
XA328	TQ2	D1	24A	PST000	00	=		STATE 0
XA328	TQ2	D1	25A	(52)	01		PSTDE0T SPI0193 54 25A 56 26A	
XA328	TQ2	D2	21A	PST010	00	=		STATE 1
XA328	TQ2	D2	22A	(46)	01		PSTDE1T SPI0193 48 22A 50 23A	
XA328	TQ2	D3	24B	PST020	00	=		STATE 2
XA328	TQ2	D3	22B	(45)	01		PSTDE2T SPI0193 41 22B 43 23B	
XA328	TQ2	D4	27B	PST030	00	=		STATE 3
XA328	TQ2	D4	25B	(51)	01		PSTDE3T SPI0193 47 25B 49 26B	
XA328	TQ2	E1	31A	PST040	00	=		STATE 4
XA328	TQ2	E1	32A	(66)	01		PSTDE4T SPI0193 68 32A 70 33A	
XA208	TQ2	B2	09A	PST05A	00	=		NO INTERRUPT.CPU-A
XA208	TQ2	B2	10A	(14)	01		P0NL20 QXXDIR 18 10A 20 11A	
XA328	TQ2	E2	28A	PST050	00	=		STATE 5
XA328	TQ2	E2	29A	(60)	01		PSTDE5T SPI0193 62 29A 64 30A	
XA328	TQ2	E3	30B	PST060	00	=		STATE 6
XA328	TQ2	E3	28B	(57)	01		PSTDE6T SPI0203 53 28B 55 29B	
XA328	TQ2	E4	33B	PST070	00	=		STATE 7
XA328	TQ2	E4	31B	(63)	01		PSTDE7T SPI0203 59 31B 61 32B	
XA328	TQ2	F1	37B	PST080	00	=		STATE 8
XA328	TQ2	F1	38B	(75)	01		PSTDE8T SPI0203 77 38B 79 39B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
			AND	OR					
XA328	TQ2	F2	34A		PST090	00	=		STATE 9
XA328	TQ2	F2	36A		(72)	01		PSTDE9T SPIO203 71 36A 73 36B	
XA319	TD4	B2	10B		PST10A	00	=		STATE 10
XA319	TD4	B2	09A		(21)	01		PSCQ00 PSTCT2U PSTCT3U PSTCT4U 14 09A 18 10A 19 09B 20 11A	
XA328	TQ2	F3	35A		PST100	00	=		STATE 10
XA328	TQ2	F3	34B		(69)	01		PST10A SPIO203 65 34B 74 35B	
XA328	TQ2	F4	39A		PST11A	00	=		STATE 11
XA328	TQ2	F4	37A		(80)	01		PSTCT5U SPIO203 76 37A 78 38A	
XA327	TQ2	B4	13B		PST110	00	=		STATE 11
XA327	TQ2	B4	11B		(27)	01		PST11A SPIO193 23 11B 25 12B	
XA327	TQ2	C1	18A		PST390	00	=		STATE 3 OR 9
XA327	TQ2	C1	19A		(38)	01		PSTDE3T PSTDE9T 40 19A 42 20A	
XA327	TQ2	C2	15A		PST480	00	=		STATE 4 OR 8
XA327	TQ2	C2	16A		(30)	01		PSTDE4T PSTDE8T 34 16A 36 17A	
XA209	TQ2	C1	18A		PS1110	00	=		STATE 1 OR 11
XA209	TQ2	C1	19A		(38)	01		PSTDE1T PST11A 40 19A 42 20A	
XA205	TT3	A1	04A		PTH04A	00	=		CPU-A SENDER, CPU-B DEV STROBE
XA205	TT3	A1	05A		(04)	01		QDTERO PONL10 QSTRB0 06 05A 08 06A 10 07A	
XA209	TQ2	E4	33B		PTSMD0	00	=		TEST MODE, CPU-A OR CPU-B
XA209	TQ2	E4	31B		(63)	01		PLPTSP QLPTSP 59 31B 61 32B	
XA327	TQ2	C3	16B		PWRITA	00	=		STATE 5, CPU-A SENDING
XA327	TQ2	C3	14B		(33)	01		PONLNQ PST050 29 14B 31 15B	
XA328	TQ2	C1	18A		PWRITO	00	=		
XA328	TQ2	C1	19A		(38)	01		PWRITA SPIO193 40 19A 42 20A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA343	TLD	A1	05A	PXACMB4 ( )	00 01	=	PXACMD 06 05A	
XA343	TLD	A1	05A	PXACMD	00	=		PXACMB4 BUSS
XA343	TLD	A1	06A	(06 )	01		PXAIFO PXACMOX 08 06A 10 07A	
XA345	DCF	A1	02B	PXACMDX	00	=		TACMAB BUSS
XA345	DCF	A1	05A	(07 )	01		PXA0EA 06 05A	
XA345	DCF	A2	03B	PXACMOX	00	=		
XA345	DCF	A2	04B	(09 )	01		PXGNIA 11 04B	
				PXAENB4	00	=		
XA343	TLD	A2	02B	( )	01		PXAEND 01 02B	
XA343	TLD	A2	02B	PXAEND	00	=		PXAENB4 BUSS
XA343	TLD	A2	04A	(01 )	01		PXAIFO PXAENOX 04 04A 05 03B	
XA345	DCF	A3	07B	PXAENDX	00	=		TAENAB BUSS
XA345	DCF	A3	05A	(17 )	01		PXA0EA 06 05A	
XA345	DCF	A4	06B	PXAENOX	00	=		
XA345	DCF	A4	05B	(15 )	01		PXGNIA 13 05B	
XA342	TQ2	A1	05A	PXAIEA	00	=		PORT A INPUT ENABLE
XA342	TQ2	A1	06A	(06 )	01		PXASLOX PXXORA 08 06A 10 07A	
XA341	TQ2	A1	05A	PXAIE0	00	=		
XA341	TQ2	A1	06A	(06 )	01		PXAIEA SPI0223 08 06A 10 07A	
XA341	TQ2	A2	02B	PXAIFO	00	=		
XA341	TQ2	A2	04A	(01 )	01		PXAIEA SPI0223 04 04A 05 03B	
XA345	DCF	A5	08B	PXAINDX	00	=		TAINAB BUSS
XA345	DCF	A5	05A	(14 )	01		PXA0EA 06 05A	

CONNECTOR	CIRCUIT TYPE	PROD	TEST POINTS AND OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA345	DCF	A6	07A	PXAINOX	00	=		
XA345	DCF	A6	06A	(10)	01		PXDBIO 08 06A	
				PXAPCB4	00	=		
XA343	TLD	A3	04B	( )	01		PXAPCD 09 04B	
XA343	TLD	A3	04B	PXAPCD	00	=		PXAPCB4 BUSS
XA343	TLD	A3	02A	(09)	01		PXAIFO PXAPCOX 03 02A 07 03A	
XA345	DCF	A7	02A	PXAPCOX	00	=		TAOPAB BUSS
XA345	DCF	A7	05A	(01)	01		PXA0EA 06 05A	
XA345	DCF	A8	03A	PXAPCOX	00	=		
XA345	DCF	A8	04A	(03)	01		PXDSBPR 04 04A	
XA340	TD4	A1	05B	PXARQA	00	=		
XA340	TD4	A1	05A	(11)	01		PXASLOX PXINHR DEVINH PXXROO 06 05A 08 06A 10 07A 13 06B	
XA345	DCF	B1	10B	PXARQDX	00	=		REQUEST TO CPU-A
XA345	DCF	B1	13A	(27)	01		PXGN1A 36 13A	
XA342	TQ2	A2	02B	PXARQO	00	=		
XA342	TQ2	A2	04A	(01)	01		PXARQA SPI0223 04 04A 05 03B	
XA345	DCF	B2	11B	PXARQOX	00	=		
XA345	DCF	B2	12B	(29)	01		PXARQO 31 12B	
XA203	TT3	B3	13B	PXARSA	00	=		PORT A RESET
XA203	TT3	B3	10B	(27)	01		PXACMOX PXAENOX PXASLOX 21 10B 23 11B 25 12B	
XA342	TQ2	A3	04B	PXASLA	00	=		
XA342	TQ2	A3	02A	(09)	01		PXASLOX SPI0223 03 02A 07 03A	
XA346	DCF	B3	15B	PXASLDX	00	=		
XA346	DCF	B3	13A	(37)	01		SPI0233 36 13A	



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERMINAL	DESIGNATOR	FACTOR	COMMENT
XA346	DCF	B4	14B	PXASLOX	00 =			
XA346	DCF	B4	13B	(13)	01		SPT00B3 33 13B	
XA345	DCF	B5	16A	PXASTDX4	00 =			PORT A STATUS DR1/RCLR
XA345	DCF	B5	13A	(14)	01		PXGNIA 36 13A	
XA345	DCF	B6	15A	PXASTOX	00 =			
XA345	DCF	B6	14A	(14)	01		PXASLOX 38 14A	
				PXAOCB4	00 =			
XA343	TLD	B1	12A	( )	01		PXAOC0 22 12A	
XA343	TLD	B1	12A	PXAOC0	00 =			PXAOCB4 BUSS
XA343	TLD	B1	13A	(22)	01		PXAIE0 PXAOC0X 24 13A 26 14A	
XA345	DCF	C1	25B	PXAOC0X	00 =			TA00AB BUSS
XA345	DCF	C1	29A	(16)	01		PXA0EA 52 29A	
XA345	DCF	C2	26B	PXAOC0X	00 =			
XA345	DCF	C2	27B	(17)	01		PXS031U 49 27B	
XA340	TD4	A2	04B	PXA0EA	00 =			PORT A OUTPUT ENABLE
XA340	TD4	A2	02B	(09)	01		PXASLOX PXINHR DEVINH PXXCS0 01 02B 04 04A 05 03B 07 03A	
				PXA1CB4	00 =			
XA343	TLD	B2	09A	( )	01		PXA1C0 14 09A	
XA343	TLD	B2	09A	PXA1C0	00 =			PXA1CB4 BUSS
XA343	TLD	B2	10A	(14)	01		PXAIE0 PXA1C0X 18 10A 20 11A	
XA345	DCF	C3	30B	PXA1C0X	00 =			TA01AB BUSS
XA345	DCF	C3	29A	(55)	01		PXA0EA 52 29A	
XA345	DCF	C4	29B	PXA1C0X	00 =			
XA345	DCF	C4	28B	(56)	01		PXS032U 51 28B	

CONNECTOR	TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERMINAL	DESIGNATION	FACTOR	COMMENT
XA343	TLD	B3	10B	PXA2CB4 ( )	00 = 01		PXA2CD 21 10B	
XA343	TLD	B3	10B	PXA2CD (21 )	00 = 01		PXA2EO PXA2COX 17 08B 19 09B	PXA2CB4 BUSS
XA345	DCF	C5	31B	PXA2CDX (60 )	00 = 01		PXA0EA 52 29A	TA02AB BUSS
XA345	DCF	C6	31A	PXA2COX (57 )	00 = 01		PXS033U 54 30A	
XA343	TLD	B4	13B	PXA3CB4 ( )	00 = 01		PXA3CD 27 13B	
XA343	TLD	B4	13B	PXA3CD (27 )	00 = 01		PXA3EO PXA3COX 23 11B 25 12B	PXA3CB4 BUSS
XA345	DCF	C7	25A	PXA3CDX (43 )	00 = 01		PXA0EA 52 29A	TA03AB BUSS
XA345	DCF	C8	26A	PXA3COX (48 )	00 = 01		PXS034U 50 28A	
XA343	TLD	C1	18A	PXA4CB4 ( )	00 = 01		PXA4CD 38 18A	
XA343	TLD	C1	18A	PXA4CD (38 )	00 = 01		PXA4EO PXA4COX 40 19A 42 20A	PXA4CB4 BUSS
XA345	DCF	D1	32B	PXA4CDX (65 )	00 = 01		PXA0EA 72 36A	TA04AB BUSS
XA345	DCF	D2	33B	PXA4COX (69 )	00 = 01		PXS471U 71 34B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERN	DESIGNATOR	FACTOR	COMMENT
XA343	TLD	C2	15A	PXA5CB4 ( )	00 01	=	PXA5CD 30 15A	
XA343	TLD	C2	15A	PXA5CD	00	=		PXA5CB4 BUSS
XA343	TLD	C2	16A	PXA5CD (30 )	01		PXA1E0 PXA5COX 34 16A 36 17A	
XA345	DCF	D3	37B	PXA5CDX	00	=		TA05AB BUSS
XA345	DCF	D3	36A	PXA5CDX (78 )	01		PXA0EA 72 36A	
XA345	DCF	D4	36B	PXA5COX	00	=		
XA345	DCF	D4	35B	PXA5COX (75 )	01		PXS472U 73 35B	
				PXA6CB4	00	=		
XA343	TLD	C3	16B	PXA6CB4 ( )	01		PXA6CD 33 16B	
XA343	TLD	C3	16B	PXA6CD	00	=		PXA6CB4 BUSS
XA343	TLD	C3	14B	PXA6CD (33 )	01		PXA1E0 PXA6COX 29 14B 31 15B	
XA345	DCF	D5	38B	PXA6CDX	00	=		TA06AB BUSS
XA345	DCF	D5	36A	PXA6CDX (80 )	01		PXA0EA 72 36A	
XA345	DCF	D6	38A	PXA6COX	00	=		
XA345	DCF	D6	37A	PXA6COX (76 )	01		PXS473U 74 37A	
				PXA7CB4	00	=		
XA343	TLD	C4	19B	PXA7CB4 ( )	01		PXA7CD 39 19B	
XA343	TLD	C4	19B	PXA7CD	00	=		PXA7CB4 BUSS
XA343	TLD	C4	17B	PXA7CD (39 )	01		PXA1E0 PXA7COX 35 17B 37 18B	
XA345	DCF	D7	33A	PXA7CDX	00	=		TA07AB BUSS
XA345	DCF	D7	36A	PXA7CDX (61 )	01		PXA0EA 72 36A	
XA345	DCF	D8	34A	PXA7COX	00	=		
XA345	DCF	D8	35A	PXA7COX (68 )	01		PXS474U 70 35A	

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA340	TD4	B1	11B	PXCA0A	00	=		
XA340	TD4	B1	12A	(23)	01		PXR6CS 22 12A PXRCS 24 13A PXX050 25 12B PXROPA 26 14A	
XA331	TS8	A1	05B	PXCA1A	00	=		
XA331	TS8	A1	02B	(11)	01		PXCMA5 01 02B PXXB10 04 04A PXXA0P 05 03B PXXA1Q 06 05A PXRPCS 07 03A PXROCS 08 06A PXR1CR 10 07A PXR2CR 13 06B	
XA341	TQ2	A3	04B	PXCA10	00	=		
XA341	TQ2	A3	02A	(09)	01		PXCA1A 03 02A SPI0223 07 03A	
XA340	TD4	B2	10B	PXCMA5	00	=		
XA340	TD4	B2	09A	(21)	01		PXCMA5 14 09A PXXB2A 18 10A PXX04A 19 09B PXRS2B 20 11A	
XA341	TQ2	B1	12A	PXCMA5	00	=		ADDRESS STORE E/F
XA341	TQ2	B1	13A	(22)	01		PXCMA5 24 13A PXCA0A 26 14A	
XA201	TQ2	D1	24A	PXCP00	00	=		PXCP1B BUSS
XA201	TQ2	D1	25A	(52)	01		PXCP1A 54 25A SPI0013 56 26A	
XA202	TQ2	D1	24A	PXCP1A	00	=		CLOCK PHASE 1
XA202	TQ2	D1	25A	(52)	01		PXIMAP 54 25A PXIMBQ 56 26A	
XA201	TQ2	D1	24A	PXCP1B	00	=		
XA201	TQ2	D1	24A	( )	01		PXCP00 52 24A PXCP10 46 21A	
XA201	TQ2	D2	21A	PXCP10	00	=		PXCP1B BUSS
XA201	TQ2	D2	22A	(46)	01		PXCP1A 48 22A SPI0013 50 23A	
XA202	TQ2	D2	21A	PXCP3A	00	=		CLOCK PHASE 3
XA202	TQ2	D2	22A	(46)	01		PXIMAP 48 22A PXIMBP 50 23A	
XA201	TQ2	D3	24B	PXCP3B	00	=		
XA201	TQ2	D3	24B	( )	01		PXCP30 45 24B PXCP40 51 27B	
XA201	TQ2	D3	24B	PXCP30	00	=		PXCP3B BUSS
XA201	TQ2	D3	22B	(45)	01		PXCP3A 41 22B SPI0013 43 23B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TEMP	DESIGNATION	FACTOR	COMMENT
XA201	TQ2	04	27B	PXCP40	00	=		
XA201	TQ2	04	25B	(51)	01		PXCP3A SPT0013 47 25B 49 26B	PXCP3B BUSS
XA336	TT3	A2	03A	PXDBIA	00	=		
XA336	TT3	A2	02B	(07)	01		PXXDSQ PXXDOP PXXDIQ 01 02B 03 02A 05 03B	
XA332	TT3	A1	04A	PXDBIO	00	=		
XA332	TT3	A1	05A	(04)	01		PXXCIP PXXDIP PXDBIA 06 05A 08 06A 10 07A	
XA339	MUX	01	27B	PXDB0TA	00	=		
XA339	MUX	01	25B	(55)	01		PONL10 PXDB01X PXXCIQ PXXDIP 51 25B 53 26B 52 25A 49 24B	
XA339	MUX	02	31B	PXDB0TB	00	=		
XA339	MUX	02	29B	(61)	01		QONL10 PXDB11X 57 29B 59 30B	
XA339	MUX	03	28A	PXDB0TC	00	=		
XA339	MUX	03	26A	(60)	01		PPERRS PXDB21X 54 26A 56 28B	
XA339	MUX	04	31A	PXDB0TD	00	=		
XA339	MUX	04	29A	(63)	01		QPERRS PXDB31X 62 29A 64 30A	
XA338	MUX	A1	02A	PXDB01X	00	=		
XA338	MUX	A1	02B	(04)	01		PR00BQ PR24BQ PR08BQ PR16BQ PXGN1A PONL10 PXXREQ PEBCCA 03 02B 05 03B 07 04B 09 05B 11 06B 15 07B 18 09A 17 08B	
XA338	MUX	A2	03A	PXDB02X	00	=		
XA338	MUX	A2	06A	(06)	01		PXXDIQ PXXD0Q PXXDSQ PXGN5A 14 06A 10 05A 08 04A 13 07A	
XA338	MUX	B1	10A	PXDB11X	00	=		
XA338	MUX	B1	09B	(20)	01		PR01BQ PR25BQ PR09BQ PR17BQ PXGN1A QONL10 QXXREQ PEBCCA 19 09B 21 10B 23 11B 25 12B 29 13B 31 14B 34 15B 33 16A	
XA338	MUX	B2	11A	PXCB12X	00	=		
XA338	MUX	B2	14A	(22)	01		PXXDIQ PXXD0Q PXXDSQ PXGN5A 27 14A 26 13A 24 12A 30 15A	
XA338	MUX	C1	17A	PXDB21X	00	=		
XA338	MUX	C1	16B	(36)	01		PR02BQ PR26BQ PR10BQ PR18BQ PXGN1A PPERRS P1PNDQ PEBCCA 35 16B 37 17B 39 18B 41 19B 43 22B 45 23B 47 23A 50 24A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA338	MUX	C2	18A	PXDB22X	00	=		
XA338	MUX	C2	21A	(38)	01		PXXD1Q PXXD0Q PXXDSQ PXGN5A 46 21A 42 20A 40 19A 48 22A	
XA337	MUX	A1	02A	PXDB31X	00	=		
XA337	MUX	A1	02B	(04)	01		PR03BQ PR27BQ PR11BQ PR19BQ PXGN1A QPERRS Q1PND0 PEBCAA 03 02B 05 03B 07 04B 09 05B 11 06B 15 07B 18 09A 17 08B	
XA337	MUX	A2	03A	PXDB32X	00	=		
XA337	MUX	A2	06A	(06)	01		PXXD1Q PXXD0Q PXXDSQ PXGN5A 14 06A 10 05A 08 04A 13 07A	
XA338	MUX	D1	27B	PXDB4TA	00	=		
XA338	MUX	D1	25B	(55)	01		PART10 PXDB41X PXXCIQ PXXDIP 51 25B 53 26B 52 25A 49 24B	
XA338	MUX	D2	31B	PXDB4TB	00	=		
XA338	MUX	D2	29B	(61)	01		PART20 PXDB51X 57 29B 59 30B	
XA338	MUX	D3	28A	PXDB4TC	00	=		
XA338	MUX	D3	26A	(60)	01		PEOMSO PXDB61X 54 26A 56 28B	
XA338	MUX	D4	31A	PXDB4TD	00	=		
XA338	MUX	D4	29A	(63)	01		PTSMDO PXDB71X 62 29A 64 30A	
XA339	MUX	A1	02A	PXDB41X	00	=		
XA339	MUX	A1	02B	(04)	01		PR04BQ PR28BQ PR12BQ PR20BQ PXGN4A PART10 P2ODLQ PSTCT4U 03 02B 05 03B 07 04B 09 05B 11 06B 15 07B 18 09A 17 08B	
XA339	MUX	A2	03A	PXDB42X	00	=		
XA339	MUX	A2	06A	(06)	01		PXXD1Q PXXD0Q PXXDSQ PXGN5A 14 06A 10 05A 08 04A 13 07A	
XA339	MUX	B1	10A	PXDB51X	00	=		
XA339	MUX	B1	09B	(20)	01		PR05BQ PR29BQ PR13BQ PR21BQ PXGN4A PART20 Q2ODLQ PSTCT3U 19 09B 21 10B 23 11B 25 12B 29 13B 31 14B 34 15B 33 16A	
XA339	MUX	B2	11A	PXDB52X	00	=		
XA339	MUX	B2	14A	(22)	01		PXXD1Q PXXD0Q PXXDSQ PXGN5A 27 14A 26 13A 24 12A 30 15A	
XA339	MUX	C1	17A	PXDB61X	00	=		
XA339	MUX	C1	16B	(36)	01		PR06BQ PR30BQ PR14BQ PR22BQ PXGN4A PEOMSO PXGN1A PSTCT2U 35 16B 37 17B 39 18B 41 19B 43 22B 45 23B 47 23A 50 24A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR								COMMENT
XA339	MUX	C2	18A	PXDB62X	00	=									
XA339	MUX	C2	21A	(38)	01		PXXDIQ	PXX00Q	PXXDSQ	PXGN5A					
							46 21A	42 20A	40 19A	48 22A					
XA337	MUX	B1	10A	PXDB71X	00	=									
XA337	MUX	B1	09B	(20)	01		PRO7BQ	PR31BQ	PR15BQ	PR23BQ	PXGN4A	PTSM00	PXGN1A	PSTCT1U	
							19 09B	21 10B	23 11B	25 12B	29 13B	31 14B	34 15B	33 16A	
XA337	MUX	B2	11A	PXDB72X	00	=									
XA337	MUX	B2	14A	(22)	01		PXXDIQ	PXX00Q	PXXDSQ	PXGN5A					
							27 14A	26 13A	24 12A	30 15A					
XA341	TQ2	B2	09A	PXDEVA	00	=									
XA341	TQ2	B2	10A	(14)	01		PXR3CS	SPI0223						*	
							18 10A	20 11A							
XA340	TD4	C1	17B	PXDEVR	00	=									
XA340	TD4	C1	18B	(35)	01		PXDEVS	PXX80A	PXX83A	PXRS2B					
							37 18B	38 18A	40 19A	42 20A					
XA341	TQ2	C1	18A	PXDEVS	00	=									DEVICE COMMAND STORE F/F
XA341	TQ2	C1	19A	(38)	01		PXDEVR	PXDVOA							
							40 19A	42 20A							
XA342	TQ2	A4	07B	PXDEVO	00	=									
XA342	TQ2	A4	05B	(15)	01		PXDEVA	SPI0223							
							11 05B	13 06B							
XA336	TT3	A3	07B	PXDIRA	00	=									INTERRUPT COMPLETED TO CPU-A
XA336	TT3	A3	04B	(15)	01		PXXDIQ	PXXC2Q	PXXC4Q						
							09 04B	11 05B	13 06B						
XA340	TD4	D1	25B	PXDPEA	00	=									RECEIVE DATA PARITY ERROR
XA340	TD4	D1	26B	(47)	01		PWRITO	PXENAS	PXR0PPR	PXXA50					
							49 26B	52 24A	54 25A	56 26A					
XA318	PAR	A1	07A	PXDSBPR	00	=									
XA318	PAR	A1	03B	(13)	01		PXS031U	PXS032U	PXS033U	PXS034U	PXS471U	PXS472U	PXS473U	PXS474U	
							03 03B	05 04B	07 05B	09 06B	10 05A	08 04A	06 03A	04 02A	
XA318	PAR	A1	07B	( )	02	+	PXXCIQ								
							11 07B								
XA342	TQ2	B1	12A	PXDVCO	00	=									
XA342	TQ2	B1	13A	(22)	01		PXR090T	SPI0223							
							24 13A	26 14A							
XA202	TQ2	C2	15A	PXDVE0	00	=									DEV STORE EITHER CPU
XA202	TQ2	C2	16A	(30)	01		PXCA0A	QXCA0A							
							34 16A	36 17A							

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA340	TD4	C2	16B	PXDVS	00	II		
XA340	TD4	C2	15A	(33)	01		PXDVS PXXB0A PXXB3A PXS2B 30 15A 31 15B 34 16A 36 17A	
XA341	TQ2	C2	15A	PXDVS	00	=		DEV COMMAND STOP STORE E/E
XA341	TQ2	C2	16A	(30)	01		PXDVS PXDV5A 34 16A 36 17A	
XA333	TD4	D1	25B	PXDVOA	00	=		
XA333	TD4	D1	26B	(47)	01		PXCA10 PXDEVO PXDVC0 PBUSYR 49 26B 52 24A 54 25A 56 26A	
XA331	TS8	B1	11B	PXDV1A	00	=		
XA331	TS8	B1	09A	(23)	01		PXDEVS PXXB20 PXXA0P PXXA1Q PXROPA SPI0183 SPI0053 SPI0203 14 09A 18 10A 19 09B 20 11A 22 12A 24 13A 25 12B 26 14A	
				PXDV1B	00	=		
XA342	TQ2	C1	18A	( )	01		PXDV10 PXDV20 PXDV30 PXDV40 38 18A 30 15A 33 16B 39 19B	
XA342	TQ2	C1	18A	PXDV10	00	=		PXDV1B BUSS
XA342	TQ2	C1	19A	(38)	01		PXDV1A SPI0223 40 19A 42 20A	
XA342	TQ2	C2	15A	PXDV20	00	=		PXDV1B BUSS
XA342	TQ2	C2	16A	(30)	01		PXDV1A SPI0223 34 16A 36 17A	
XA342	TQ2	C3	16B	PXDV30	00	=		PXDV1B BUSS
XA342	TQ2	C3	14B	(33)	01		PXDV1A SPI0223 29 14B 31 15B	
XA342	TQ2	C4	19B	PXDV40	00	=		PXDV1B BUSS
XA342	TQ2	C4	17B	(39)	01		PXDV1A SPI0223 35 17B 37 18B	
XA336	TT3	B1	11A	PXDV5A	00	=		
XA336	TT3	B1	12A	(20)	01		PXCA10 PXDEVO PXDVC0 22 12A 24 13A 26 14A	
XA333	TD4	E2	30B	PXEAOA	00	=		
XA333	TD4	E2	29B	(57)	01		PXR6CS PXRENS PXX050 PXROPA 55 29B 60 28A 62 29A 64 30A	
XA341	TQ2	A4	07B	PXEAO0	00	=		
XA341	TQ2	A4	05B	(15)	01		PXEAOA SPI0223 11 05B 13 06B	



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIG- NATOR	FACTOR	COMMENT
XA336	TT3	B2	09B	PXEBOA	00	=		
XA336	TT3	B2	09A	(79)	01		PXCA10 PXDEVA PXE0B0 14 09A 17 08B 18 10A	
				PXEBOI	00	=		
XA330	TDD	A1	06A	( )	01		PXGNIA 08 06A	
				PXEBOH	00	=		
XA330	TDD	AN	07A	( )	01		PXE81A 10 07A	
XA330	TDD	AP	05B	PXEBOP	00	=		
XA330	TDD	AP	05A	(11)	01		PXR50B 06 05A	
XA330	TDD	AQ	06B	PXEBOQ	00	=		EOB SYNC COUNTER BIT 0
XA330	TDD	AQ	07B	(13)	01		PXEBOA 15 07B	
XA334	TQ2	A1	05A	PXE81A	00	=		
XA334	TQ2	A1	05A	(06)	01		PXE81Q PXCP3B 08 06A 10 07A	
				PXE81I	00	=		
XA329	TDD	A1	06A	( )	01		PXEBOQ 08 06A	
				PXE81N	00	=		
XA329	TDD	AN	07A	( )	01		PXCP1B 10 07A	
XA329	TDD	AP	05B	PXE81P	00	=		
XA329	TDD	AP	05A	(11)	01		PXR50B 06 05A	
XA329	TDD	AQ	06B	PXE81Q	00	=		EOB SYNC COUNTER BIT 1
XA329	TDD	AQ	07B	(13)	01		SPI0203 15 07B	
XA208	TQ2	D1	24A	PXE810	00	=		SYNCED EOB PULSE, CPU-A
XA208	TQ2	D1	25A	(52)	01		PXE81A SPI0033 54 25A 56 26A	
XA340	TD4	F1	37A	PXEBOA	00	=		RECEIVE DATA STROBE BYTE 0
XA340	TD4	F1	37B	(76)	01		PWRIT0 PXENAS PXXB10 PXXA50 75 37B 77 38B 78 38A 79 39B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA340	TD4	E2	30B	PXED1A	00	=		RECEIVE DATA STROBE BYTE 1
XA340	TD4	E2	29B	(57)	01		PWRITO PXENAS PXXB20 PXXA50 55 29B 60 28A 62 29A 64 30A	
XA319	TD4	E1	31B	PXED2A	00	=		RECEIVE DATA STROBE BYTE 2
XA319	TD4	E1	32B	(59)	01		PWRITO PXENAS PXXB30 PXXA50 61 32B 66 31A 68 32A 70 33A	
XA319	TD4	E2	30B	PXED3A	00	=		RECEIVE DATA STROBE BYTE 3
XA319	TD4	E2	29B	(57)	01		PWRITO PXENAS PXXB40 PXXA50 55 29B 60 28A 62 29A 64 30A	
XA340	TD4	F2	35A	PXENAR	00	=		
XA340	TD4	F2	36A	(69)	01		PXENAS PXXB5A PXXO4A PXR52B 71 36A 72 34A 73 36B 74 35B	
XA341	TQ2	F2	34A	PXENAS	00	=		ENABLE STORE E/E
XA341	TQ2	F2	36A	(72)	01		PXENAR PXEA0A 71 36A 73 36B	
XA341	TQ2	B3	10B	PXE0B0	00	=		
XA341	TQ2	B3	08B	(21)	01		PXR092T SPI0223 17 08B 19 09B	
XA342	TQ2	F2	34A	PXGN1A	00	=		
XA342	TQ2	F2	36A	(72)	01		SPI0213 SPI0223 71 36A 73 36B	
XA342	TQ2	F3	35A	PXGN4A	00	=		
XA342	TQ2	F3	34B	(69)	01		SPI0213 SPI0223 65 34B 74 35B	
XA341	TQ2	F4	39A	PXGN5A	00	=		
XA341	TQ2	F4	37A	(80)	01		SPI0213 SPI0223 76 37A 78 38A	
XA341	TQ2	F3	35A	PXHST0	00	=		
XA341	TQ2	F3	34B	(69)	01		PXR091T SPI0223 65 34B 74 35B	
XA332	TT3	A3	07B	PXHS0A	00	=		
XA332	TT3	A3	04B	(15)	01		PXCA10 PXDEVA PXHST0 09 04B 11 05B 13 06B	
XA202	TQ2	A1	05A	PXINHR	00	=		
XA202	TQ2	A1	06A	(06)	01		PXINHS PXIN0A 08 06A 10 07A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM DESIGNATOR	FACTOR	COMMENT
XA203	TT3	A1	04A	PXINHS	00 =		OFF LINE STORE F/F
XA203	TT3	A1	05A	(04)	01	PXINHR PXONLO PXR50B 06 05A 08 06A 10 07A	
XA203	TT3	A3	07B	PXIN0A	00 =		
XA203	TT3	A3	04B	(15)	01	PXR50B PXDVEO PXONLO 09 04B 11 05B 13 06B	
XA334	TQ2	D1	24A	PXIRCO	00 =		
XA334	TQ2	D1	25A	(52)	01	PXR094T SPI0203 54 25A 56 26A	
XA332	TT3	A2	03A	PXIROA	00 =		
XA332	TT3	A2	02B	(07)	01	PXCA10 PXDEVA PXIRCO 01 02B 03 02A 05 03B	
				PXIROI	00 =		
XA330	TDD	BI	03B	( )	01	PXGNIA 05 03B	
				PXIRON	00 =		
XA330	TDD	BN	02B	( )	01	PXIRIA 01 02B	
XA330	TDD	BP	04B	PXIROP	00 =		
XA330	TDD	BP	04A	(09)	01	PXR50B 04 04A	
XA330	TDD	BQ	03A	PXIROQ	00 =		ITR SYNC COUNTER BIT 0
XA330	TDD	BQ	02A	(07)	01	PXIROA 03 02A	
XA328	TQ2	C4	19B	PXIR1A	00 =		
XA328	TQ2	C4	17B	(39)	01	PXIRIQ PXCP3B 35 17B 37 18B	
				PXIR1I	00 =		
XA329	TDD	BI	03B	( )	01	PXIROQ 05 03B	
				PXIR1N	00 =		
XA329	TDD	BN	02B	( )	01	PXCP1B 01 02B	
XA329	TDD	BP	04B	PXIR1P	00 =		
XA329	TDD	BP	04A	(09)	01	PXR50B 04 04A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA329	TDD	BQ	03A	PXIR1Q	00	=		ITR SYNC COUNTER BIT 1
XA329	TDD	BQ	02A	(07)	01		SPI0183 03 02A	
XA206	TQ2	C4	19B	PXIR10	00	=		SYNCED ITR PULSE CPU-A
XA206	TQ2	C4	17B	(39)	01		PXIR1A SPI0033 35 17B 37 18B	
XA342	TQ2	B2	09A	PXPRSA	00	=		
XA342	TQ2	B2	10A	(14)	01		PXPR50X SPI0223 18 10A 20 11A	
XA346	DCF	B1	10B	PXPRSDX	00	=		PORT RESET DR1/RCVR
XA346	DCF	B1	13A	(27)	01		SPI0233 36 13A	
XA346	DCF	B2	11B	PXPR50X	00	=		
XA346	DCF	B2	12B	(29)	01		SPI0213 31 12B	
XA317	DBC	A1	04A	PXRAF0T	00	=		
XA317	DBC	A1	02A	(08)	01		PXR7CS 04 02A	
XA317	DBC	A2	05A	PXRAF1T	00	=		
XA317	DBC	A2	03A	(10)	01		PXR6CS 06 03A	
XA317	DBC	A3	06A	PXRAF2T	00	=		
XA317	DBC	A3	02B	(14)	01		PXR5CS 03 02B	
XA317	DBC	A4	07A	PXRAF3T	00	=		
XA317	DBC	A4	03B	(13)	01		PXR4CR 05 03B	
XA317	DBC	A5	08B	PXRAF4T	00	=		
XA317	DBC	A5		(17)	01		SPA 4T	
XA317	DBC	A6	04B	PXRAF5T	00	=		
XA317	DBC	A6		(07)	01		SPA 5T	
XA317	DBC	A7	05B	PXRAF6T	00	=		
XA317	DBC	A7		(09)	01		SPA 6T	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA317	DBC	A8	06B	PXRAF7T	00	=		
XA317	DBC	A8		(11)	01		SPA 7T	
XA317	DBC	A9	07B	PXRAF8T	00	=		
XA317	DBC	A9		(15)	01		SPA 8T	
XA317	DBC	A0	09A	PXRAF9T	00	=		
XA317	DBC	A0		(18)	01		SPA 9T	
XA334	TQ2	A2	02B	PXRCMR	00	=		
XA334	TQ2	A2	04A	(01)	01		PXRCMS PXRRSA 04 04A 05 03B	
XA335	TQ2	A2	02B	PXRCMS	00	=		I/O INPUT REG COMMAND BIT
XA335	TQ2	A2	04A	(01)	01		PXRCMR PXACMB4 04 04A 05 03B	
XA334	TQ2	A3	04B	PXRENR	00	=		
XA334	TQ2	A3	02A	(09)	01		PXRENS PXRRSA 03 02A 07 03A	
XA335	TQ2	A3	04B	PXRENS	00	=		I/O INPUT REG ENABLE BIT
XA335	TQ2	A3	02A	(09)	01		PXRENR PXAENB4 03 02A 07 03A	
XA334	TQ2	A4	07B	PXRPCR	00	=		
XA334	TQ2	A4	05B	(15)	01		PXRPCS PXRRSA 11 05B 13 06B	
XA335	TQ2	A4	07B	PXRPCS	00	=		I/O INPUT REG PARITY BIT
XA335	TQ2	A4	05B	(15)	01		PXRPCR PXAPCB4 11 05B 13 06B	
XA202	TQ2	C4	19B	PXRPSA	00	=		
XA202	TQ2	C4	17B	(39)	01		PXRPS0 SPI0013 35 17B 37 18B	
XA323	TQ4	A1	05B	PXRPS0	00	=		
XA323	TQ4	A1	05A	(11)	01		PXARSA PXST1A QXST1A QXBRSA 06 05A 08 06A 10 07A 13 06B	
XA335	TQ2	B1	12A	PXRRSA	00	=		I/O INPUT REG RESET
XA335	TQ2	B1	13A	(22)	01		PXRRS0 SPI0203 24 13A 26 14A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
			AND	OR					
XA334	TQ2	B1	12A		PXRRS0	00	=		
XA334	TQ2	B1	13A		(22)	01		PXXA6A PXRS1B 24 13A 26 14A	
XA201	TQ2	C3	16B		PXRSAA	00	=		PXRS2B BUSS
XA201	TQ2	C3	14B		(33)	01		PXRS00 SPI0013 29 14B 31 15B	
XA201	TQ2	A1	05A		PXRS0A	00	=		PXRS0B BUSS
XA201	TQ2	A1	06A		(06)	01		PXRS00 SPI0013 08 06A 10 07A	
					PXRS0B	00	=		
XA201	TQ2	A1	05A		( )	01		PXRS0A PXRS1A PXRS2A PXRS3A 06 05A 01 02B 09 04B 15 07B	
XA203	TT3	B2	09B		PXRS00	00	=		CONTROLLER MASTER RESET
XA203	TT3	B2	09A		(19)	01		PXRP5A PXRP5A DEVINH 14 09A 17 08B 18 10A	
XA201	TQ2	A2	02B		PXRS1A	00	=		PXRS0B BUSS
XA201	TQ2	A2	04A		(01)	01		PXRS00 SPI0013 04 04A 05 03B	
					PXRS1B	00	=		
XA201	TQ2	B1	12A		( )	01		PXRS4A PXRS5A PXRS6A PXRS7A 22 12A 14 09A 21 10B 27 13B	
XA201	TQ2	A3	04B		PXRS2A	00	=		PXRS0B BUSS
XA201	TQ2	A3	02A		(09)	01		PXRS00 SPI0013 03 02A 07 03A	
					PXRS2B	00	=		
XA201	TQ2	C1	18A		( )	01		PXRS8A PXRS9A PXRSAA 38 18A 30 15A 33 16B	
XA201	TQ2	A4	07B		PXRS3A	00	=		PXRS0B BUSS
XA201	TQ2	A4	05B		(15)	01		PXRS00 SPI0013 11 05B 13 06B	
XA201	TQ2	B1	12A		PXRS4A	00	=		PXRS1B BUSS
XA201	TQ2	B1	13A		(22)	01		PXRS00 SPI0013 24 13A 26 14A	
XA201	TQ2	B2	09A		PXRS5A	00	=		PXRS1B BUSS
XA201	TQ2	B2	10A		(14)	01		PXRS00 SPI0013 18 10A 20 11A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA201	TQ2	B3	10B	PXRS6A	00	=	PXRS1B BUSS	
XA201	TQ2	B3	08B	(21)	01	=	PXRS00 SPI0013 17 08B 19 09B	
XA201	TQ2	B4	13B	PXRS7A	00	=	PXRS1B BUSS	
XA201	TQ2	B4	11B	(27)	01	=	PXRS00 SPI0013 23 11B 25 12B	
XA201	TQ2	C1	18A	PXRS8A	00	=	PXRS2B BUSS	
XA201	TQ2	C1	19A	(38)	01	=	PXRS00 SPI0013 40 19A 42 20A	
XA201	TQ2	C2	15A	PXRS9A	00	=	PXRS2B BUSS	
XA201	TQ2	C2	16A	(30)	01	=	PXRS00 SPI0013 34 16A 36 17A	
XA341	TQ2	D1	24A	PXROCR	00	=		
XA341	TQ2	D1	25A	(52)	01	=	PXROCS PXR RSA 54 25A 56 26A	
XA342	TQ2	D1	24A	PXROCS	00	=	I/O INPUT REG DATA BIT 0	
XA342	TQ2	D1	25A	(52)	01	=	PXROCR PXA0CB4 54 25A 56 26A	
XA341	TQ2	C4	19B	PXROPA	00	=		
XA341	TQ2	C4	17B	(39)	01	=	PXROPPR SPI0223 35 17B 37 18B	
XA318	PAR	B1	14A	PXROPPR	00	=		
XA318	PAR	B1	09B	(25)	01	=	PXROCS PXR1CS PXR2CS PXR3CS PXR4CS PXR5CS PXR6CS PXR7CS 17 09B 19 10B 21 11B 23 12B 24 12A 22 11A 20 10A 18 09A	
XA318	PAR	B1	13B	( )	02	+	PXRPCS 27 13B	
XA317	DBC	B1	12A	PXR090T	00	=		
XA317	DBC	B1	10A	(24)	01	=	PXR7CS 20 10A	
XA317	DBC	B2	13A	PXR091T	00	=		
XA317	DBC	B2	11A	(26)	01	=	PXR6CS 22 11A	
XA317	DBC	B3	14A	PXR092T	00	=		
XA317	DBC	B3	09B	(27)	01	=	PXR5CS 19 09B	
XA317	DBC	B4	15A	PXR093T	00	=		
XA317	DBC	B4	10B	(30)	01	=	PXR4CS 21 10B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA317	DBC	B5	16A	PXR094T	00	=		
XA317	DBC	B5		(33)	01		SPA 4T	
XA317	DBC	B6	11B	PXR095T	00	=		
XA317	DBC	B6		(23)	01		SPA 5T	
XA317	DBC	B7	12B	PXR096T	00	=		
XA317	DBC	B7		(25)	01		SPA 6T	
XA317	DBC	B8	13B	PXR097T	00	=		
XA317	DBC	B8		(29)	01		SPA 7T	
XA317	DBC	B9	14B	PXR098T	00	=		
XA317	DBC	B9		(31)	01		SPA 8T	
XA317	DBC	B0	15B	PXR099T	00	=		
XA317	DBC	B0		(34)	01		SPA 9T	
XA341	TQ2	D2	21A	PXR1CR	00	=		
XA341	TQ2	D2	22A	(46)	01		PXR1CS PXR RSA 48 22A 50 23A	
XA342	TQ2	D2	21A	PXR1CS	00	=		I/O INPUT REG DATA BIT 1
XA342	TQ2	D2	22A	(46)	01		PXR1CR PXA1CB4 48 22A 50 23A	
XA341	TQ2	D3	24B	PXR2CR	00	=		
XA341	TQ2	D3	22B	(45)	01		PXR2CS PXR RSA 41 22B 43 23B	
XA342	TQ2	D3	24B	PXR2CS	00	=		I/O INPUT REG DATA BIT 2
XA342	TQ2	D3	22B	(45)	01		PXR2CR PXA2CB4 41 22B 43 23B	
XA341	TQ2	D4	27B	PXR3CR	00	=		
XA341	TQ2	D4	25B	(51)	01		PXR3CS PXR RSA 47 25B 49 26B	
XA342	TQ2	D4	27B	PXR3CS	00	=		I/O INPUT REG DATA BIT 3
XA342	TQ2	D4	25B	(51)	01		PXR3CR PXA3CB4 47 25B 49 26B	



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA341	TQ2	E1	31A	PXR4CR	00	=		
XA341	TQ2	E1	32A	(66)	01		PXR4CS PXR RSA 68 32A 70 33A	
XA342	TQ2	E1	31A	PXR4CS	00	=		I/O INPUT REG DATA BIT 4
XA342	TQ2	E1	32A	(66)	01		PXR4CR PXA4CB4 68 32A 70 33A	
XA341	TQ2	E2	28A	PXR5CR	00	=		
XA341	TQ2	E2	29A	(60)	01		PXR5CS PXR RSA 62 29A 64 30A	
XA342	TQ2	E2	28A	PXR5CS	00	=		I/O INPUT REG DATA BIT 5
XA342	TQ2	E2	29A	(60)	01		PXR5CR PXA5CB4 62 29A 64 30A	
XA341	TQ2	E3	30B	PXR6CR	00	=		
XA341	TQ2	E3	28B	(57)	01		PXR6CS PXR RSA 53 28B 55 29B	
XA342	TQ2	E3	30B	PXR6CS	00	=		I/O INPUT REG DATA BIT 6
XA342	TQ2	E3	28B	(57)	01		PXR6CR PXA6CB4 53 28B 55 29B	
XA341	TQ2	E4	33B	PXR7CR	00	=		
XA341	TQ2	E4	31B	(63)	01		PXR7CS PXR RSA 59 31B 61 32B	
XA342	TQ2	E4	33B	PXR7CS	00	=		I/O INPUT REG DATA BIT 7
XA342	TQ2	E4	31B	(63)	01		PXR7CR PXA7CB4 59 31B 61 32B	
XA342	TQ2	F1	37B	PXSK0A	00	=		
XA342	TQ2	F1	38B	(75)	01		PXXC2Q PXXC3P 77 38B 79 39B	
XA341	TQ2	F1	37B	PXSK00	00	=		STATUS REG CLOCK BITS 0 TO 3
XA341	TQ2	F1	38B	(75)	01		PXSK0A SPI0223 77 38B 79 39B	
XA341	TQ2	C3	16B	PXSST0	00	=		
XA341	TQ2	C3	14B	(33)	01		PXR097T SPI0223 29 14B 31 15B	
XA331	TS8	C1	17B	PXSS0A	00	=		
XA331	TS8	C1	15A	(35)	01		PXDVSS PXXB20 PXXAOP PXXA1Q PXSST0 PXROPA SPI0203 SPI0213 30 15A 31 15B 34 16A 36 17A 37 18B 38 18A 40 19A 42 20A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
				PXSTOI	00	=		
XA330	TDD	CI	13A	( )	01		PXGN1A 24 13A	
				PXSTON	00	=		
XA330	TDD	CN	14A	( )	01		PXST1A 26 14A	
XA330	TDD	CP	11B	PXSTOP	00	=		
XA330	TDD	CP	12A	(23 )	01		SPI0203 22 12A	
XA330	TDD	CQ	12B	PXSTOQ	00	=		STOP SYNC COUNTER BIT 0
XA330	TDD	CQ	13B	(25 )	01		PXST2A 27 13B	
XA342	TQ2	B3	10B	PXST1A	00	=		
XA342	TQ2	B3	08B	(21 )	01		PXST1Q PXCP3B 17 08B 19 09B	
				PXST1I	00	=		
XA329	TDD	CI	13A	( )	01		PXSTOQ 24 13A	
				PXST1N	00	=		
XA329	TDD	CN	14A	( )	01		PXCP1B 26 14A	
XA329	TDD	CP	11B	PXST1P	00	=		
XA329	TDD	CP	12A	(23 )	01		SPI0203 22 12A	
XA329	TDD	CQ	12B	PXST1Q	00	=		STOP SYNC COUNTER BIT 1
XA329	TDD	CQ	13B	(25 )	01		SPI0183 27 13B	
XA342	TQ2	B4	13B	PXST2A	00	=		
XA342	TQ2	B4	11B	(27 )	01		PXST20 SPI0223 23 11B 25 12B	
XA341	TQ2	B4	13B	PXST20	00	=		
XA341	TQ2	B4	11B	(27 )	01		PXHS0A PXSS0A 23 11B 25 12B	
XA317	DBC	C1	18A	PXS031U	00	=		
XA317	DBC	C1	18B	(38 )	01		PXDB0TA PXDB0TB PXDB0TC PXDB0TD PXGN4A 39 18B 41 19B 43 22B 45 23B 50 24A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA317	DBC	C2	19A	PXS032U	00	=		
XA317	DBC	C2	17B	(40)	01		PXSK00 37 17B	
XA317	DBC	C3	20A	PXS033U	00	=		
XA317	DBC	C3	23A	(42)	01		SPI0233 47 23A	
XA317	DBC	C4	21A	PXS034U	00	=		
XA317	DBC	C4	22A	(46)	01		SPI0073 48 22A	
XA317	DBC	C5	17A	PXS035U	00	=		
XA317	DBC	C5	16B	(36)	01		SPI0243 35 16B	
XA317	DBC	D1	26A	PXS471U	00	=		
XA317	DBC	D1	26B	(54)	01		PXDB4TA PXDB4TB PXDB4TC PXDB4TD PXGN4A 53 26B 55 27B 57 29B 59 30B 63 31A	
XA317	DBC	D2	28B	PXS472U	00	=		
XA317	DBC	D2	25B	(56)	01		PXSK00 51 25B	
XA317	DBC	D3	28A	PXS473U	00	=		
XA317	DBC	D3	31B	(60)	01		SPI0233 61 31B	
XA317	DBC	D4	29A	PXS474U	00	=		
XA317	DBC	D4	30A	(62)	01		SPI0243 64 30A	
XA317	DBC	D5	25A	PXS475U	00	=		
XA317	DBC	D5	24B	(52)	01		SPI0173 49 24B	
XA335	TQ2	A1	05A	PXXACA	00	=		
XA335	TQ2	A1	06A	(06)	01		PXXACO SPI0203 08 06A 10 07A	
				PXXACT	00	=		
XA326	TDD	A1	06A	( )	01		SPI0193 08 06A	
				PXXACN	00	=		
XA326	TDD	AN	07A	( )	01		PXXA3P 10 07A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	LOGIC FACTOR	FACTOR	COMMENT
XA326	TDD	AP	05B	PXXACP	00	=		
XA326	TDD	AP	05A	(11)	01		PXXADA 06 05A	
XA326	TDD	AQ	06B	PXXACQ	00	=		
XA326	TDD	AQ	07B	(13)	01		PXRS0B 15 07B	I/O STATE COUNTER CONTROL F/E
XA331	TSB	D1	25B	PXXACO	00	=		
XA331	TSB	D1	23B	(47)	01		PXROCR PXR1CR PXR2CR PXR3CR PXR4CR PXR5CR PXR6CR PXR7CR 43 23B 46 21A 48 22A 49 26B 50 23A 52 24A 54 25A 56 26A	
XA334	TQ2	B4	13B	PXXADA	00	=		
XA334	TQ2	B4	11B	(27)	01		PXXADO SPI0203 23 11B 25 12B	I/O STATE COUNTER START
XA333	TD4	A1	05B	PXXADO	00	=		
XA333	TD4	A1	05A	(11)	01		PXRCMR PXRENR PXRPCR PXXACA 06 05A 08 06A 10 07A 13 06B	
XA329	TDD	DI	10A	PXXAOI	00	=		
XA329	TDD	DI	10A	( )	01		PXXA3P 18 10A	
XA329	TDD	DN	09A	PXXAON	00	=		
XA329	TDD	DN	09A	( )	01		P16MH0 14 09A	
XA329	TDD	DP	10B	PXXAOP	00	=		
XA329	TDD	DP	11A	(21)	01		PXXACP 20 11A	
XA329	TDD	DQ	09B	PXXAOQ	00	=		
XA329	TDD	DQ	08B	(19)	01		SPI0053 17 08B	I/O STATE COUNTER BIT 0
XA330	TDD	DI	10A	PXXA1I	00	=		
XA330	TDD	DI	10A	( )	01		PXXA0Q 18 10A	
XA330	TDD	DN	09A	PXXA1N	00	=		
XA330	TDD	DN	09A	( )	01		P16MH0 14 09A	
XA330	TDD	DP	10B	PXXA1P	00	=		
XA330	TDD	DP	11A	(21)	01		PXXACP 20 11A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERJ	DESIGNATOR	FACTOR	COMMENT
XA330	TDD	DQ	09B	PXXA1Q	00	=		I/O STATE COUNTER BIT 1
XA330	TDD	DQ	08B	(19)	01		SPI0183 17 08B	
				PXXA2I	00	=		
XA329	TDD	EI	19A	( )	01		PXXA1Q 40 19A	
				PXXA2N	00	=		
XA329	TDD	EN	20A	( )	01		PI6MH0 42 20A	
XA329	TDD	EP	17B	PXXA2P	00	=		
XA329	TDD	EP	18A	(35)	01		PXXACP 38 18A	
				PXXA2Q	00	=		I/O STATE COUNTER BIT 2
XA329	TDD	EQ	18B	(37)	01		SPI0203 39 19B	
				PXXA3I	00	=		
XA330	TDD	EI	19A	( )	01		PXXA2Q 40 19A	
				PXXA3N	00	=		
XA330	TDD	EN	20A	( )	01		PI6MH0 42 20A	
XA330	TDD	EP	17B	PXXA3P	00	=		
XA330	TDD	EP	18A	(35)	01		PXXACP 38 18A	
				PXXA3Q	00	=		I/O STATE COUNTER BIT 3
XA330	TDD	EQ	18B	(37)	01		SPI0203 39 19B	
				PXXA4A	00	=		I/O STATE COUNTER STATE 4
XA334	TQ2	B2	09A	(14)	01		PXXA0Q PXXA3Q 18 10A 20 11A	
				PXXA40	00	=		
XA335	TQ2	B3	10B	(21)	01		PXXA4A SPI0223 17 08B 19 09B	
				PXXA5A	00	=		I/O STATE COUNTER STATE 5
XA334	TQ2	B3	08B	(21)	01		PXXA0P PXXA1Q 17 08 19 09B	

CONNECTOR	CIRCUIT GROUP	TEST POINTS AND OR	EQUATION	TERM	DESCR. PREFIX	FACTOR	COMMENT
XA335	TQ2	B4 13B	PXXA50	00 =			
XA335	TQ2	B4 11B	( 27 )	01	PXXA5A	SPI0223 23 11B 25 12B	
XA335	TQ2	B2 09A	PXXA6A	00 =			I/O STATE COUNTER STATE 6
XA335	TQ2	B2 10A	( 14 )	01	PXXA1P	PXXA2Q 18 10A 20 11A	
XA334	TQ2	C1 18A	PXXBCA	00 =			I/O BYTE COUNTER RESET
XA334	TQ2	C1 19A	( 38 )	01	PXXBCO	SPI0203 40 19A 42 20A	
XA336	TT3	A1 04A	PXXBCO	00 =			
XA336	TT3	A1 05A	( 04 )	01	PXRCMR	PXREN R PXRS2B 06 05A 08 06A 10 07A	
XA334	TQ2	C2 15A	PXXBKO	00 =			I/O BYTE COUNTER CLOCK
XA334	TQ2	C2 16A	( 30 )	01	PXXA3Q	SPI0203 34 16A 36 17A	
XA334	TQ2	C3 16B	PXXBOA	00 =			I/O BYTE COUNTER STATE 0
XA334	TQ2	C3 14B	( 33 )	01	PXXBOP	PXXB2P 29 14B 31 15B	
			PXXBOI	00 =			
XA326	TDD	B1 03B	( )	01	PXXB2P	05 03B	
			PXXBON	00 =			
XA326	TDD	BN 02B	( )	01	PXXBKO	01 02B	
XA326	TDD	BP 04B	PXXBOP	00 =			
XA326	TDD	BP 04A	( 09 )	01	PXXBCA	04 04A	
XA326	TDD	BQ 03A	PXXBOQ	00 =			I/O BYTE COUNTER BIT 0
XA326	TDD	BQ 02A	( 07 )	01	SPI0183	03 02A	
XA335	TQ2	C1 18A	PXXB00	00 =			
XA335	TQ2	C1 19A	( 38 )	01	PXXB0A	SPI0223 40 19A 42 20A	
XA335	TQ2	C2 15A	PXXB1A	00 =			I/O BYTE COUNTER STATE 1
XA335	TQ2	C2 16A	( 30 )	01	PXXB0Q	PXXB1P 34 16A 36 17A	

CONNECTOR	TEST POINT GROUP	TEST POINTS AND OR	EQUATION	TERM DESIGNATOR	FACTOR	COMMENT
XA335	TQ2	B4 13B	PXXA50	00 =		
XA335	TQ2	B4 11B	( 27 )	01	PXXA5A SPI0223 23 11B 25 12B	
XA335	TQ2	B2 09A	PXXA6A	00 =		I/O STATE COUNTER STATE 6
XA335	TQ2	B2 10A	( 14 )	01	PXXA1P PXXA2Q 18 10A 20 11A	
XA334	TQ2	C1 18A	PXXBCA	00 =		I/O BYTE COUNTER RESET
XA334	TQ2	C1 19A	( 38 )	01	PXXBCO SPI0203 40 19A 42 20A	
XA336	TT3	A1 04A	PXXBCO	00 =		
XA336	TT3	A1 05A	( 04 )	01	PXRCHR PXREN R PXR52B 06 05A 08 06A 10 07A	
XA334	TQ2	C2 15A	PXXBKO	00 =		I/O BYTE COUNTER CLOCK
XA334	TQ2	C2 16A	( 30 )	01	PXXA3Q SPI0203 34 16A 36 17A	
XA334	TQ2	C3 16B	PXXB0A	00 =		I/O BYTE COUNTER STATE 0
XA334	TQ2	C3 14B	( 33 )	01	PXXB0P PXXB2P 29 14B 31 15B	
			PXXB0I	00 =		
XA326	TDD	BI 03B	( )	01	PXXB2P 05 03B	
			PXXB0N	00 =		
XA326	TDD	BN 02B	( )	01	PXXBKO 01 02B	
XA326	TDD	BP 04B	PXXB0P	00 =		
XA326	TDD	BP 04A	( 09 )	01	PXXBCA 04 04A	
XA326	TDD	BQ 03A	PXXB0Q	00 =		I/O BYTE COUNTER BIT 0
XA326	TDD	BQ 02A	( 07 )	01	SPI0183 03 02A	
XA335	TQ2	C1 18A	PXXB00	00 =		
XA335	TQ2	C1 19A	( 38 )	01	PXXB0A SPI0223 40 19A 42 20A	
XA335	TQ2	C2 15A	PXXB1A	00 =		I/O BYTE COUNTER STATE 1
XA335	TQ2	C2 16A	( 30 )	01	PXXB0Q PXXB1P 34 16A 36 17A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM. DESIG. NATOR	FACTOR	COMMENT
XA334	TQ2	D3	24B	PXXB30	00 =		
XA334	TQ2	D3	22B	(45)	01	PXXB3A SPI0203 41 22B 43 23B	
XA335	TQ2	D1	24A	PXXB4A	00 =		I/O BYTE COUNTER STATE 4
XA335	TQ2	D1	25A	(52)	01	PXXB0P PXXB1Q 54 25A 56 26A	I/O BYTE COUNTER STATE 4
XA334	TQ2	D4	27B	PXXB40	00 =		
XA334	TQ2	D4	25B	(51)	01	PXXB4A SPI0203 47 25B 49 26B	
XA335	TQ2	D2	21A	PXXB5A	00 =		I/O BYTE COUNTER STATE 5
XA335	TQ2	D2	22A	(46)	01	PXXB1P PXXB2Q 48 22A 50 23A	
XA334	TQ2	E1	31A	PXXB50	00 =		
XA334	TQ2	E1	32A	(66)	01	PXXB5A SPI0203 68 32A 70 33A	
XA335	TQ2	D3	24B	PXXCIA	00 =		
XA335	TQ2	D3	22B	(45)	01	PXXCIO SPI0223 41 22B 43 23B	
XA325	TDD	AI	06A	PXXCII ( )	00 = 01	PXGN4A 08 06A	
XA325	TDD	AN	07A	PXXCIN ( )	00 = 01	PXXC4P 10 07A	
XA325	TDD	AP	05B	PXXCIP (11)	00 = 01	PXRS0B 06 05A	
XA325	TDD	AQ	06B	PXXCIQ (13)	00 = 01	PXXCIA 15 07B	DEV/DEF INDICATOR CONTROL F/E
XA333	TD4	A2	04B	PXXCIO	00 =		INDICATOR INPUTS
XA333	TD4	A2	02B	(09)	01	PDV0FA PDV0NA PXSS0A PX0D3A 01 02B 04 04A 05 03B 07 03A	
XA333	TD4	B1	11B	PXXCRO	00 =		
XA333	TD4	B1	12A	(23)	01	PXXCIP PXXDDP PXXDIP PXXDSP 22 12A 24 13A 25 12B 26 14A	



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA335	TQ2	D4	27B	PXXCSA	00	=		INPUT STROBE COUNT 5 TO 7
XA335	TQ2	D4	25B	(51)	01		PXXC2Q PXXC4Q 47 25B 49 26B	
XA334	TQ2	E2	28A	PXXCS0	00	=		
XA334	TQ2	E2	29A	(60)	01		PXXCSA SPI0203 62 29A 64 30A	
XA329	TDD	GI	25A	PXXC0I	00	=		
XA329	TDD	GI		( )	01		PXXC4P 54 25A	
XA329	TDD	GN	26A	PXXCON	00	=		
XA329	TDD	GN		( )	01		PI6M10 56 26A	
XA329	TDD	GP	25B	PXXC0P	00	=		
XA329	TDD	GP	24A	(47)	01		PXXCR0 52 24A	
XA329	TDD	GQ	26B	PXXC0Q	00	=		INPUT STROBE COUNTER BIT 0
XA329	TDD	GQ	27B	(49)	01		SPI0203 51 27B	
XA330	TDD	GI	25A	PXXC1I	00	=		
XA330	TDD	GI		( )	01		PXXC0Q 54 25A	
XA330	TDD	GN	26A	PXXC1N	00	=		
XA330	TDD	GN		( )	01		PI6M10 56 26A	
XA330	TDD	GP	25B	PXXC1P	00	=		
XA330	TDD	GP	24A	(47)	01		PXXCR0 52 24A	
XA330	TDD	GQ	26B	PXXC1Q	00	=		INPUT STROBE COUNTER BIT 1
XA330	TDD	GQ	27B	(49)	01		SPI0203 51 27B	
XA329	TDD	HI	22A	PXXC2I	00	=		
XA329	TDD	HI		( )	01		PXXC1Q 48 22A	
XA329	TDD	HN	21A	PXXC2N	00	=		
XA329	TDD	HN		( )	01		PI6M10 46 21A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERN	LOGIC FACTOR	FACTOR	COMMENT
XA329	TDD	HP	24B	PXXC2P	00	=		
XA329	TDD	HP	23A	(45)	01		PXXCRO 50 23A	
XA329	TDD	HQ	23B	PXXC2Q	00	=		INPUT STROBE COUNTER BIT 2
XA329	TDD	HQ	22B	(43)	01		SPI0183 41 22B	
XA326	TDD	CI	13A	PXXC3I	00	=		
				( )	01		PXXC2Q 24 13A	
XA326	TDD	CN	14A	PXXC3N	00	=		
				( )	01		P16M10 26 14A	
XA326	TDD	CP	11B	PXXC3P	00	=		
XA326	TDD	CP	12A	(23)	01		PXXCRO 22 12A	
XA326	TDD	CQ	12B	PXXC3Q	00	=		INPUT STROBE COUNTER BIT 3
XA326	TDD	CQ	13B	(25)	01		SPI0193 27 13B	
XA330	TDD	HI	22A	PXXC4I	00	=		
				( )	01		PXXC3Q 48 22A	
XA330	TDD	HN	21A	PXXC4N	00	=		
				( )	01		P16M10 46 21A	
XA330	TDD	HP	24B	PXXC4P	00	=		
XA330	TDD	HP	23A	(45)	01		PXXCRO 50 23A	
XA330	TDD	HQ	23B	PXXC4Q	00	=		INPUT STROBE COUNTER BIT 4
XA330	TDD	HQ	22B	(43)	01		SPI0183 41 22B	
XA335	TQ2	E1	31A	PXXDDA	00	=		
XA335	TQ2	E1	32A	(66)	01		PXXDDO SPI0223 68 32A 70 33A	
XA329	TDD	JI	32A	PXXDDI	00	=		
				( )	01		PXGN4A 68 32A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
			AND	OR					
					PXXDDN	00 =			
XA329	TDD	JN	33A		( )	01	PXXDIP 70 33A		
XA329	TDD	JP	31B			00 =			
XA329	TDD	JP	31A		(59 )	01	PXRS0B 66 31A		
XA329	TDD	JQ	32B			00 =			INPUT DATA CONTROL F/F
XA329	TDD	JQ	33B		(61 )	01	PXXDDA 63 33B		
XA334	TQ2	E3	30B			00 =			
XA334	TQ2	E3	28B		(57 )	01	PRIDEA PLTEOA 53 28B 55 29B		
XA335	TQ2	E2	28A			00 =			
XA335	TQ2	E2	29A		(60 )	01	PXXDIS PXEA00 62 29A 64 30A		
XA329	TDD	KI	29A			00 =			
					( )	01	PXGN4A 62 29A		
XA329	TDD	KN	28A			00 =			
					( )	01	PXXDIP 60 28A		
XA329	TDD	KP	30B			00 =			
XA329	TDD	KP	30A		(57 )	01	PXRS0B 64 30A		
XA329	TDD	KQ	29B			00 =			INPUT INTERRUPT CONTROL F/F
XA329	TDD	KQ	28B		(55 )	01	PXXDIA 53 28B		
XA332	TT3	B1	11A			00 =			
XA332	TT3	B1	12A		(20 )	01	PXXDIS PXDIRA PXINHR 22 12A 24 13A 26 14A		
XA333	TD4	B2	10B			00 =			
XA333	TD4	B2	09A		(21 )	01	PXXDIR QP08NA QP02NA PP10NA 14 09A 18 10A 19 09B 20 11A		
XA335	TQ2	E3	30B			00 =			
XA335	TQ2	E3	28B		(57 )	01	PXXDRO SPIO223 53 28B 55 29B		

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA336	TT3	B3	13B	PXXDRO	00	=		
XA336	TT3	B3	10B	(27)	01		PXXDDP 21 10B PXXDIP 23 11B PXXDSP 25 12B	
XA208	TQ2	D2	21A	PXXDSA	00	=		
XA208	TQ2	D2	22A	(46)	01		PXXDDQ 4B 22A PXXCSO 50 23A	
XA330	TDD	MI	36A	PXXDSI	00	=		
XA330	TDD	MI	36A	( )	01		PXGN4A 71 36A	CPU-A
XA330	TDD	MN	34A	PXXDSN	00	=		
XA330	TDD	MN	34A	( )	01		PXXDIP 72 34A	
XA330	TDD	MP	35A	PXXDSP	00	=		
XA330	TDD	MP	36B	(69)	01		PXRS0B 73 36B	
XA330	TDD	MQ	35B	PXXDSQ	00	=		
XA330	TDD	MQ	34B	(74)	01		PXIR1A 65 34B	
XA209	TQ2	D3	24B	PXXOSO	00	=		
XA209	TQ2	D3	22B	(45)	01		SPI0222 41 22B PXXOSA 43 23B	SUM. START 1 OR 2
XA330	TDD	KI	29A	PXXDOI	00	=		
XA330	TDD	KI	29A	( )	01		PXXDIP 62 29A	
XA330	TDD	KN	28A	PXXDON	00	=		
XA330	TDD	KN	28A	( )	01		PXXC3P 60 28A	
XA330	TDD	KP	30B	PXXDOP	00	=		
XA330	TDD	KP	30A	(57)	01		PXXCRO 64 30A	
XA330	TDD	KQ	29B	PXXDOQ	00	=		
XA330	TDD	KQ	28B	(55)	01		SPI0203 53 28B	INPUT BYTE COUNTER BIT 0
XA325	TDD	BI	03B	PXXDII	00	=		
XA325	TDD	BI	03B	( )	01		PXXDOQ 05 03B	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

LOGIC UNIT ASSEMBLY NO. 149015 REV. C INDEX PXXD1N

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA325	TDD	BN	02B	PXXD1N ( )	00 01	=	PXXC3P 01 02B	
XA325	TDD	BP	04B	PXXD1P (09 )	00 01	=	PXXCRO 04 04A	
XA325	TDD	BQ	03A	PXXD1Q (07 )	00 01	=	PXXDIA 03 02A	INPUT BYTE COUNTER BIT 1
XA335	TQ2	E4	33B	PXXRCA (63 )	00 01	=	PXXRCO PXXREP 59 31B 61 32B	
XA325	TDD	CI	13A	PXXRCI ( )	00 01	=	PXGN4A 24 13A	
XA325	TDD	CN	14A	PXXRCN ( )	00 01	=	PXXRZP 26 14A	
XA325	TDD	CP	11B	PXXRCP (23 )	00 01	=	PXRSOB 22 12A	
XA325	TDD	CQ	12B	PXXRCQ (25 )	00 01	=	PXXRCA 27 13B	REQUEST CONTROL E/F
XA331	TS8	E1	31B	PXXRCO (59 )	00 01	=	PLPT1A QP08NA QP02NA QP06QA PP48QA PP10NA SPIO213 SPIO203 55 29B 60 28A 61 32B 62 29A 64 30A 66 31A 68 32A 70 33A	REQUEST INPUTS
XA326	TDD	LI	38B	PXXREI ( )	00 01	=	PXGN4A 77 38B	
XA326	TDD	LN	39B	PXXREN ( )	00 01	=	PXEAOA 79 39B	
XA326	TDD	LP	37A	PXXREP (76 )	00 01	=	PXRSOB 75 37B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA326	TDD	LQ	38A	PXXREQ	00	=		REQUEST ENABLE F/F
XA326	TDD	LQ	39A	( 78 )	01		PXXROP 80 39A	
				PXXR01	00	=		
XA330	TDD	LI	38B	( )	01		PXXR2P 77 38B	
				PXXRON	00	=		
XA330	TDD	LN	39B	( )	01		P16MH0 79 39B	
XA330	TDD	LP	37A	PXXROP	00	=		
XA330	TDD	LP	37B	( 76 )	01		PXXRCQ 75 37B	
XA330	TDD	LQ	38A	PXXROQ	00	=		REQUEST STROBE COUNTER BIT 0
XA330	TDD	LQ	39A	( 78 )	01		SPI0203 80 39A	
				PXXR11	00	=		
XA329	TDD	MI	36A	( )	01		PXXROQ 71 36A	
				PXXR1N	00	=		
XA329	TDD	MN	34A	( )	01		P16MH0 72 34A	
XA329	TDD	MP	35A	PXXR1P	00	=		
XA329	TDD	MP	36B	( 69 )	01		PXXRCQ 73 36B	
XA329	TDD	MQ	35B	PXXR1Q	00	=		REQUEST STROBE COUNTER BIT 1
XA329	TDD	MQ	34B	( 74 )	01		SPI0183 65 34B	
				PXXR2I	00	=		
XA326	TDD	DI	10A	( )	01		PXXR1Q 18 10A	
				PXXR2N	00	=		
XA326	TDD	DN	09A	( )	01		P16MH0 14 09A	
XA326	TDD	DP	10B	PXXR2P	00	=		
XA326	TDD	DP	11A	( 21 )	01		PXXRCQ 20 11A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESCR. FACTOR	FACTOR	COMMENT
XA326	TDD	DQ	09B	PXXR2Q	00	=		REQUEST STROBE COUNTER BIT 2
XA326	TDD	DQ	08B	(19)	01		SPI0183 17 08B	
XA328	TQ2	C2	15A	PXX04A	00	=		
XA328	TQ2	C2	16A	(30)	01		PXXB00 PXXA40 34 16A 36 17A	
XA333	TD4	C1	17B	PXX05A	00	=		
XA333	TD4	C1	18B	(35)	01		PXXB0P PXXB2P PXXA0P PXXA1Q 37 18B 38 18A 40 19A 42 20A	
XA334	TQ2	F1	37B	PXX050	00	=		I/O BYTE COUNT=0, STATE=5
XA334	TQ2	F1	38B	(75)	01		PXX05A SPI0203 77 38B 79 39B	
XA336	TT3	C1	17A	PX0DEA	00	=		OFR RECEIVE DATA PARITY ERROR
XA336	TT3	C1	18A	(36)	01		PX0FRS PXR0PPR PXXA50 38 18A 40 19A 42 20A	
XA342	TQ2	F4	39A	PX0DRA	00	=		OFR RESET STROBE
XA342	TQ2	F4	37A	(80)	01		PX0FRS PXXB10 76 37A 78 38A	
XA336	TT3	C2	15B	PX0D0A	00	=		OFR RECEIVE DATA STROBE BYTE 0
XA336	TT3	C2	14B	(31)	01		PX0FRS PXXB20 PXXA50 29 14B 30 15A 34 16A	
XA336	TT3	C3	19B	PX0D1A	00	=		OFR RECEIVE DATA STROBE BYTE 1
XA336	TT3	C3	16B	(39)	01		PX0FRS PXXB30 PXXA50 33 16B 35 17B 37 18B	
XA336	TT3	D1	23A	PX0D2A	00	=		OFR RECEIVE DATA STROBE BYTE 2
XA336	TT3	D1	24A	(50)	01		PX0FRS PXXB40 PXXA50 52 24A 54 25A 56 26A	
XA336	TT3	D2	23B	PX0D3A	00	=		OFR RECEIVE DATA STROBE BYTE 3
XA336	TT3	D2	22B	(43)	01		PX0FRS PXXB50 PXXA50 41 22B 46 21A 48 22A	
XA336	TT3	F1	36B	PX0FRR	00	=		
XA336	TT3	F1	37B	(73)	01		PX0FRS PXXB0A PXR52B 75 37B 77 38B 79 39B	
XA335	TQ2	F1	37B	PX0FRS	00	=		OFR COMMAND STORE E/F
XA335	TQ2	F1	38B	(75)	01		PX0FRR PX0ROA 77 38B 79 39B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERMS	DESIGNATOR	FACTOR	COMMENT
XA334	TQ2	F2	34A	PXOFRO	00	=		
XA334	TQ2	F2	36A	( 72 )	01		PXR098T SPI0203 71 36A 73 36B	
XA202	TQ2	C3	16B	PXONLO	00	=		
XA202	TQ2	C3	14B	( 33 )	01		PXASLA SPI0013 29 14B 31 15B	
XA333	TD4	C2	16B	PXOR0A	00	=		
XA333	TD4	C2	15A	( 33 )	01		PXCA10 PXDEVA PXOFRO POFRNO 30 15A 31 15B 34 16A 36 17A	
				PX1MAI	00	=		
XA320	TDD	AI	06A	( )	01		PX1MBP 08 06A	
				PX1MAN	00	=		
XA320	TDD	AN	07A	( )	01		P04MH0 10 07A	
XA320	TDD	AP	05B	PX1MAP	00	=		
XA320	TDD	AP	05A	( 11 )	01		SPI0173 06 05A	
XA320	TDD	AQ	06B	PX1MAQ	00	=		
XA320	TDD	AQ	07B	( 13 )	01		SPI0023 15 07B	SYNC CLOCK BIT 0
				PX1MBI	00	=		
XA321	TDD	AI	06A	( )	01		PX1MAQ 08 06A	
				PX1MBN	00	=		
XA321	TDD	AN	07A	( )	01		P04MH0 10 07A	
XA321	TDD	AP	05B	PX1MBP	00	=		
XA321	TDD	AP	05A	( 11 )	01		SPI0173 06 05A	
XA321	TDD	AQ	06B	PX1MBQ	00	=		
XA321	TDD	AQ	07B	( 13 )	01		SPI0183 15 07B	SYNC CLOCK BIT 1
XA324	TS8	C1	17B	PX111A	00	=		
XA324	TS8	C1	15A	( 35 )	01		PXXA0Q PXXA3Q PXDEVS PXXB20 PS1110 PXROPA SPI0253 SPI0263 30 15A 31 15B 34 16A 36 17A 37 18B 38 18A 40 19A 42 20A	



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA335	TQ2	F4	39A	PX1110	00	=		
XA335	TQ2	F4	37A	(80)	01		PX111A SPI0213 76 37A 78 38A	
XA206	TQ2	D1	24A	PZE010	00	=		ENABLE 0-1.
XA206	TQ2	D1	25A	(52)	01		PONSTA QONSTA 54 25A 56 26A	
XA208	TQ2	A4	07B	POENBA	00	=		
XA208	TQ2	A4	05B	(15)	01		POENBO SPI0033 11 05B 13 06B	
XA209	TQ2	A3	04B	POENBO	00	=		SUM, STATE 9 OR XFR TO STATE
XA209	TQ2	A3	02A	(09)	01		PST11A PSTDE9T 03 02A 07 03A	2
XA208	TQ2	C3	16B	POFRNA	00	=		ENABLE OFR FROM CPU-A
XA208	TQ2	C3	14B	(33)	01		PONL20 PS1110 29 14B 31 15B	
XA209	TQ2	C2	15A	POFRNO	00	=		
XA209	TQ2	C2	16A	(30)	01		POFRNA SPI0033 34 16A 36 17A	
XA206	TQ2	E3	30B	POFRSO	00	=		SUM, OFR STROBES
XA206	TQ2	E3	28B	(57)	01		PXODRA QXODRA 53 28B 55 29B	
XA319	TQ4	A1	05B	POFRTA	00	=		OFR FR. CPU-A
XA319	TQ4	A1	05A	(11)	01		PONL20 PXOFRS SPI0253 PST010 06 05A 08 06A 10 07A 13 06B	
XA205	TT3	F1	36B	POFINA	00	=		INTERRUPT CPU-B, RECEIVER
XA205	TT3	F1	37B	(73)	01		PONLNQ QXXDIS QXXR1Q 75 37B 77 38B 79 39B	
XA340	TQ4	D2	24B	POLPEA	00	=		DEVICE ERROR, CPU-A RECEIVER
XA340	TQ4	D2	23B	(45)	01		QDER90 QSTRBO PONLNQ PST390 43 23B 46 21A 48 22A 50 23A	
XA207	TQ2	C1	18A	PONCKA	00	=		
XA207	TQ2	C1	19A	(38)	01		PONCKO SPI0033 40 19A 42 20A	
XA206	TQ2	C3	16B	PONCKO	00	=		SUM, CONTROL CLOCKS, CPU-A
XA206	TQ2	C3	14B	(33)	01		PCOICA P11ZEA 29 14B 31 15B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	LOGIC FACTOR	FACTOR	COMMENT
XA207	TQ2	D1	24A	PONCLA	00	=		
XA207	TQ2	D1	25A	(52)	01		PONCLO SPI0033 54 25A 56 26A	
XA206	TQ2	D3	24B	PONCLO	00	=		RACE CONTROL, CLEAR CPU-A
XA206	TQ2	D3	22B	(45)	01		PXRS2B QONSTA 41 22B 43 23B	SENDER CONTROL
XA320	TDD	BI	03B	PONLNI	00	=		
				( )	01		PST11A 05 03B	
XA320	TDD	BN	02B	PONLNN	00	=		
				( )	01		PONCKA 01 02B	
XA320	TDD	BP	04B	PONLNP	00	=		
XA320	TDD	BP	04A	(09)	01		PONCLA 04 04A	
XA320	TDD	BQ	03A	PONLNQ	00	=		
XA320	TDD	BQ	02A	(07)	01		SPI0053 03 02A	
XA202	TQ2	D4	27B	PONL10	00	=		CPU-A SENDER CONTROL, UNLOAD
XA202	TQ2	D4	25B	(51)	01		PONLNP SPI0033 47 25B 49 26B	
XA202	TQ2	E3	30B	PONL20	00	=		
XA202	TQ2	E3	28B	(57)	01		PONLNP SPI0033 53 28B 55 29B	
XA202	TQ2	E4	33B	PONL30	00	=		
XA202	TQ2	E4	31B	(63)	01		PONLNP SPI0033 59 31B 61 32B	
XA207	TQ2	C2	15A	PONSTA	00	=		CPU-A SYNC PULSE
XA207	TQ2	C2	16A	(30)	01		PXIR10 POWLNQ 34 16A 36 17A	
XA207	TQ2	C3	16B	P0001A	00	=		CLOCK, STATE 0-1
XA207	TQ2	C3	14B	(33)	01		PST000 PZE010 29 14B 31 15B	
XA207	TQ2	D3	24B	P0102A	00	=		CLOCK, STATE 1-2
XA207	TQ2	D3	22B	(45)	01		PSR120 PST010 41 22B 43 23B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA207	TQ2	E3	30B	PO111A	00	=		CLOCK, STATE 11-1
XA207	TQ2	E3	28B	(57)	01		PST010 POFRS0 53 28B 55 29B	
XA207	TQ2	E1	31A	PO203A	00	=		CLOCK, STATE 2-3
XA207	TQ2	E1	32A	(66)	01		PST020 PQOFNO 68 32A 70 33A	
XA207	TQ2	A1	05A	PO304A	00	=		CLOCK, 3-4
XA207	TQ2	A1	06A	(06)	01		PST030 PEC040 08 06A 10 07A	
XA202	TQ2	D3	24B	PO4MH0	00	=		
XA202	TQ2	D3	22B	(45)	01		T04MHK SPI0033 41 22B 43 23B	
XA207	TQ2	A2	02B	PO405A	00	=		CLOCK, STATE 4-5
XA207	TQ2	A2	04A	(01)	01		PST040 PEC050 04 04A 05 03B	
XA207	TQ2	B4	13B	PO411A	00	=		CLOCK, STATE 4-11
XA207	TQ2	B4	11B	(27)	01		PEC110 PST040 23 11B 25 12B	
XA207	TQ2	E4	33B	PO506A	00	=		CLOCK, STATE 5-6
XA207	TQ2	E4	31B	(63)	01		PLSTRO PST050 59 31B 61 32B	
XA207	TQ2	B1	12A	PO607A	00	=		CLOCK, STATE 6-7
XA207	TQ2	B1	13A	(22)	01		PST060 PEC070 24 13A 26 14A	
XA207	TQ2	A3	04B	PO708A	00	=		CLOCK, STATE 7-8
XA207	TQ2	A3	02A	(09)	01		PST070 PDT0T0 03 02A 07 03A	
XA207	TQ2	A4	07B	PO805A	00	=		CLOCK, STATE 8-5
XA207	TQ2	A4	05B	(15)	01		PEC050 PST080 11 05B 13 06B	
XA207	TQ2	E2	28A	PO809A	00	=		CLOCK, STATE 8-9
XA207	TQ2	E2	29A	(60)	01		PQOFNO PST080 62 29A 64 30A	
XA207	TQ2	B2	09A	PO910A	00	=		CLOCK, STATE 9-10
XA207	TQ2	B2	10A	(14)	01		PST090 PEC100 18 10A 20 11A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA209	TQ2	B2	09A	P1CSMA	00	=		
XA209	TQ2	B2	10A	(14)	01		P1CSMO SPI0033 18 10A 20 11A	
XA331	TS8	F1	37A	P1CSMO	00	=		PARTIAL SUM STATE COUNTER
XA331	TS8	F1	36A	(76)	01		P0001A P0102A P0203A P0304A P0405A P0506A P0607A P0708A 71 36A 72 34A 73 36B 74 35B 75 37B 77 38B 78 38A 79 39B	CLOCKS
XA335	TQ2	F2	34A	P1PND0	00	=		INTERRUPT PENDING, CPU-A
XA335	TQ2	F2	36A	(72)	01		PXXDIR PXXDIP 71 36A 73 36B	
XA207	TQ2	B3	10B	P1011A	00	=		CLOCK, STATE 10-11
XA207	TQ2	B3	08B	(21)	01		PST100 PEC110 17 08B 19 09B	
XA205	TT3	D2	23B	P11ZEA	00	=		RELEASE DEV STROBE, CPU-A
XA205	TT3	D2	22B	(43)	01		PREL90 PSTRB0 PONLNQ 41 22B 46 21A 48 22A	
XA209	TQ2	E1	31A	P1100A	00	=		FOR, COUNTER = 0
XA209	TQ2	E1	32A	(66)	01		PLST00 PSTCT5U 68 32A 70 33A	
XA207	TQ2	D4	27B	P1102A	00	=		CLOCK, STATE 11-2
XA207	TQ2	D4	25B	(51)	01		PSR120 PSTCT5U 47 25B 49 26B	
XA201	TQ2	C4	19B	P16MHA	00	=		
XA201	TQ2	C4	17B	(39)	01		T16MHA SPI0013 35 17B 37 18B	
XA202	TQ2	B1	12A	P16MHO	00	=		
XA202	TQ2	B1	13A	(22)	01		P16MHA SPI0013 24 13A 26 14A	
XA202	TQ2	B2	09A	P16MIO	00	=		
XA202	TQ2	B2	10A	(14)	01		P16MHA SPI0013 18 10A 20 11A	
XA209	TQ2	B3	10B	P2CSMA	00	=		
XA209	TQ2	B3	08B	(21)	01		P2CSMO SPI0033 17 08B 19 09B	
XA308	TS8	F1	37A	P2CSMO	00	=		PARTIAL SUM STATE COUNTER
XA308	TS8	F1	36A	(76)	01		P0809A P0910A P1011A P0111A P0411A SPI0253 P1102A P0805A 71 36A 72 34A 73 36B 74 35B 75 37B 77 38B 78 38A 79 39B	CLOCKS

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA330	TDD	J1	32A	P2ODLI ( )	00 01	=	SPI0183 68 32A	
XA330	TDD	JN	33A	P2ODLN ( )	00 01	=	PDLQ6P 70 33A	
XA330	TDD	JP	31B	P2ODLP (59 )	00 01	=	PQDLCA 66 31A	
XA330	TDD	JQ	32B	P2ODLQ (61 )	00 01	=		INPUT STATUS CONTROL F/F
XA330	TDD	JQ	33B				SPI0053 63 33B	
XA208	TQ2	C1	18A	P3ENBA (38 )	00 01	=	P3ENB0 SPI0033 40 19A 42 20A	
XA209	TQ2	A4	07B	P3ENB0 (15 )	00 01	=	PST11A PEL05A 11 05B 13 06B	SUM. ENABLE XFER TO STATE 2 OR 5
XA205	TT3	D3	27B	QC01CA (51 )	00 01	=	PXCP1B QXIR0Q PST000 45 24B 47 25B 49 26B	ITR SYNC. CPU-B. STATE 0
XA205	TT3	B3	13B	QDARQA (27 )	00 01	=	QONL10 PXXDIR PXXR1Q 21 10B 23 11B 25 12B	REQUEST TO RECEIVER. CPU-B
XA307	TQ2	A1	05A	QDER90 (06 )	00 01	=	QXR095T SPI0043 08 06A 10 07A	
XA306	TQ2	A2	02B	QDLCKA (01 )	00 01	=	Q2ODLP PXIMBQ 04 04A 05 03B	1 MHZ REQ. DELAY TIMER CLOCK. CPU-B
XA306	TQ2	A3	04B	QDLENA (09 )	00 01	=	Q2ODLQ QXXREP 03 02A 07 03A	REQ. DELAY ENDED. ENABLE RECEIVED. CPU-B
XA307	TQ2	A2	02B	QDLENO (01 )	00 01	=	QDLENA SPI0043 04 04A 05 03B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	ORIGINATOR	FACTOR	COMMENT
				QDLQ1I	00	=		
XA301	TDD	CI	13A	( )	01		QDLQ5P 24 13A	
				QDLQ1N	00	=		
XA301	TDD	CN	14A	( )	01		QDLCKA 26 14A	
XA301	TDD	CP	11B	QDLQ1P	00	=		
XA301	TDD	CP	12A	(23 )	01		QDLCA 22 12A	
XA301	TDD	CQ	12B	QDLQ1Q	00	=		
XA301	TDD	CQ	13B	(25 )	01		SPI0043 27 13B	
				QDLQ2I	00	=		
XA302	TDD	BI	03B	( )	01		QDLQ1Q 05 03B	
				QDLQ2N	00	=		
XA302	TDD	BN	02B	( )	01		QDLCKA 01 02B	
XA302	TDD	BP	04B	QDLQ2P	00	=		
XA302	TDD	BP	04A	(09 )	01		QDLCA 04 04A	
XA302	TDD	BQ	03A	QDLQ2Q	00	=		
XA302	TDD	BQ	02A	(07 )	01		SPI0023 03 02A	
				QDLQ3I	00	=		
XA301	TDD	DI	10A	( )	01		QDLQ2Q 18 10A	
				QDLQ3N	00	=		
XA301	TDD	DN	09A	( )	01		QDLCKA 14 09A	
XA301	TDD	DP	10B	QDLQ3P	00	=		
XA301	TDD	DP	11A	(21 )	01		QDLCA 20 11A	
XA301	TDD	DQ	09B	QDLQ3Q	00	=		
XA301	TDD	DQ	08B	(19 )	01		SPI0023 17 08B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	LOGIC FACTOR	FACTOR	COMMENT
				QDLQ4I	00 =			
XA302	TDD	CI	13A	( )	01		QDLQ30 24 13A	
				QDLQ4N	00 =			
XA302	TDD	CN	14A	( )	01		QDLCKA 26 14A	
XA302	TDD	CP	11B	QDLQ4P	00 =			
XA302	TDD	CP	12A	(23 )	01		QDLCA 22 12A	
XA302	TDD	CQ	12B	QDLQ4Q	00 =			
XA302	TDD	CQ	13B	(25 )	01		SPI0043 27 13B	
				QDLQ5I	00 =			
XA303	TDD	AI	06A	( )	01		QDLQ4Q 08 06A	
				QDLQ5N	00 =			
XA303	TDD	AN	07A	( )	01		QDLCKA 10 07A	
XA303	TDD	AP	05B	QDLQ5P	00 =			
XA303	TDD	AP	05A	(11 )	01		QDLCA 06 05A	
XA303	TDD	AQ	06B	QDLQ5Q	00 =			
XA303	TDD	AQ	07B	(13 )	01		SPI0043 15 07B	
XA307	TQ2	A3	04B	QDLQ6A	00 =			
XA307	TQ2	A3	02A	(09 )	01		QDLQ6Q SPI0043 03 02A 07 03A	
				QDLQ6I	00 =			
XA302	TDD	DI	10A	( )	01		QDLQ6A 18 10A	
				QDLQ6N	00 =			
XA302	TDD	DN	09A	( )	01		QDLQ5P 14 09A	
XA302	TDD	DP	10B	QDLQ6P	00 =			
XA302	TDD	DP	11A	(21 )	01		QDLCA 20 11A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA302	TDD	DQ	C9B	QDLQ6Q	00	=		
XA302	TDD	DQ	08B	(19)	01		SPI0023 17 08B	
XA206	TQ2	B4	13B	QDTER0	00	=		DATA ACCEPT OR REJECT, CPU-B
XA206	TQ2	B4	11B	(27)	01		QXR096T QXR095T 23 11B 25 12B	
XA205	TT3	B1	11A	QDTRQA	00	=		DATA REQ. TO SENDER, CPU-B
XA205	TT3	B1	12A	(20)	01		QONL10 QXXDIR QXXR1Q 22 12A 24 13A 26 14A	
XA206	TQ2	C2	15A	QDVAE0	00	=		SUM. DEV ACCEPT OR DEV ERROR, CPU-B
XA206	TQ2	C2	16A	(30)	01		QXR095T QXR094T 34 16A 36 17A	
XA201	TQ2	E2	28A	QDVCLA	00	=		
XA201	TQ2	E2	29A	(60)	01		QDVCL0 SPI0013 62 29A 64 30A	
XA202	TQ2	E2	28A	QDVCL0	00	=		CPU-B SENDER OR MASTER CLEAR
XA202	TQ2	E2	29A	(60)	01		QONLNP PXR51B 62 29A 64 30A	
XA307	TQ2	B1	12A	QDV0FA	00	=		CPU-A RECEIVING, SEND INDICAT
XA307	TQ2	B1	13A	(22)	01		PRQ0F0 PONL10 24 13A 26 14A	OR.
XA307	TQ2	B2	09A	QDVONA	00	=		CPU-B SENDING, SEND INDICATOR
XA307	TQ2	B2	10A	(14)	01		QONL10 PRQ0N0 18 10A 20 11A	
XA207	TQ2	F1	37B	QEBCKA	00	=		CLOCK, STATE 11-0
XA207	TQ2	F1	38B	(75)	01		QEBCK0 SPI0092 77 38B 79 39B	
XA206	TQ2	F1	37B	QEBCK0	00	=		SUM. RELEASE DEV STROBES
XA206	TQ2	F1	38B	(75)	01		QSTRBA QXEB1A 77 38B 79 39B	
XA315	DBC	E1	34A	QEBCT1U	00	=		FOR COUNTER, CPU-B, BIT 1
XA315	DBC	E1	33B	(70)	01		QXR3CR QXR2CR QXR1CR QXROCR QXDEV 71 33B 73 34B 75 35B 77 36B 80 38B	
XA315	DBC	E2	35A	QEBCT2U	00	=		
XA315	DBC	E2	32A	(72)	01		QEBCKA 69 32A	



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA315	DBC	E3	36A	QEBCT3U	00	=		
XA315	DBC	E3	37B	(74)	01		SPI0073 79 37B	
XA315	DBC	E4	37A	QEBCT4U	00	=		
XA315	DBC	E4	38A	(76)	01		SPI0233 78 38A	
XA315	DBC	E5	33A	QEBCT5U	00	=		
XA315	DBC	E5	32B	(68)	01		QONL20 66 32B	
XA209	TQ2	E2	28A	QEBSRA	00	=		
XA209	TQ2	E2	29A	(60)	01		QEBSRO SPI0043 62 29A 64 30A	
XA208	TQ2	F1	37B	QEBSRO	00	=		STATE 2 OR CPU-B NOT SENDER
XA208	TQ2	F1	38B	(75)	01		PSTDE2T QONL20 77 38B 79 39B	
XA205	TT3	C2	15B	QENDDA	00	=		DEV STROBE, ACCEPT OR REJECT, CPU-A RECEIVER
XA205	TT3	C2	14B	(31)	01		PSTRBO QONL10 PDVAEO 29 14B 30 15A 34 16A	
XA203	TT3	C3	19B	QENFCA	00	=		INTERRUPT SENDER, CPU-B
XA203	TT3	C3	16B	(39)	01		QONL10 QXXDIS QXXRIQ 33 16B 35 17B 37 18B	
				QE080I	00	=		
XA302	TDD	MI	36A	( )	01		QEBCT5U 71 36A	CPU-B
				QE080N	00	=		
XA302	TDD	MN	34A	( )	01		QXEBT0 72 34A	
XA302	TDD	MP	35A	QE080P	00	=		
XA302	TDD	MP	36B	(69)	01		QEBSRA 73 36B	
XA302	TDD	MQ	35B	QE080Q	00	=		
XA302	TDD	MQ	34B	(74)	01		SPI0023 65 34B	
				QLPTSI	00	=		
XA301	TDD	EI	19A	( )	01		SPI0043 40 19A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA301	TDD	EN	20A	QLPTSN ( )	00	=		
					01	=	QLPT4Q 42 20A	
XA301	TDD	EP	17B	QLPTSP (35 )	00	=		
XA301	TDD	EP	18A		01	=	PST110 38 18A	
XA301	TDD	EQ	18B	QLPTSQ (37 )	00	=		
XA301	TDD	EQ	19B		01	=	SPI0023 39 19B	
XA332	TT3	C1	17A	QLPT1A (36 )	00	=		
XA332	TT3	C1	18A		01	=	QLTS2Q QDLENO PXCP3B 38 18A 40 19A 42 20A	SYNCED LOOP TEST PULSE, CPU-B
				QLPT4I ( )	00	=		
XA302	TDD	EI	19A		01	=	PXGN5A 40 19A	
				QLPT4N ( )	00	=		
XA302	TDD	EN	20A		01	=	QXEA0A 42 20A	
XA302	TDD	EP	17B	QLPT4P (35 )	00	=		
XA302	TDD	EP	18A		01	=	PXRS1B 38 18A	
XA302	TDD	EQ	18B	QLPT4Q (37 )	00	=		
XA302	TDD	EQ	19B		01	=	QLPT1A 39 19B	
XA319	T04	D2	24B	QLSD0A (45 )	00	=		
XA319	T04	D2	23B		01	=	P0NL10 QXXD00 QXXD0P QXXD1P 43 23B 46 21A 48 22A 50 23A	LAST DATA BYTE TO RECEIVER, CPU-B
XA306	TQ2	B3	10B	QLTE0A (21 )	00	=		
XA306	TQ2	B3	08B		01	=	QXEA00 QLPT4Q 17 08B 19 09B	ENABLE DATA TO SENDER (LOOP TEST CPU-B)
				QLTS1I ( )	00	=		
XA303	TDD	BI	03B		01	=	QXGN4A 05 03B	
				QLTS1N ( )	00	=		
XA303	TDD	BN	02B		01	=	QLPT1A 01 02B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERMINAL	DESIGNATOR	FACTOR	COMMENT
XA303	TDD	BP	04B	QLTS1P	00	=		
XA303	TDD	BP	04A	(09)	01		PXRS1B 04 04A	
XA303	TDD	BQ	03A	QLTS1Q	00	=		
XA303	TDD	BQ	02A	(07)	01		QX0D3A 03 02A	
XA304	TDD	AI	05A	QLTS2I	00	=		
XA304	TDD	AI		( )	01		QLTS1Q 08 06A	
XA304	TDD	AN	07A	QLTS2N	00	=		
XA304	TDD	AN		( )	01		PXCPIB 10 07A	
XA304	TDD	AP	05B	QLTS2P	00	=		
XA304	TDD	AP	05A	(11)	01		PXRS1B 06 05A	
XA304	TDD	AQ	06B	QLTS2Q	00	=		
XA304	TDD	AQ	07B	(13)	01		SPI0043 15 07B	
XA306	TQ2	B4	13B	QNLCA	00	=		VALID COMMANDS, CPU-B SENDER
XA306	TQ2	B4	11B	(27)	01		QXXC10 QOVLNQ 23 11B 25 12B	
XA332	TT3	F1	36B	QPERRR	00	=		
XA332	TT3	F1	37B	(73)	01		QPERRS PXRS2B PPER1A 75 37B 77 38B 79 39B	
XA333	TD4	F1	37A	QPERRS	00	=		PARITY ERROR BIT, CPU-B
XA333	TD4	F1	37B	(76)	01		POLPEA QXDPEA QXODEA QPERRR 75 37B 77 38B 78 38A 79 39B	
XA332	TT3	C2	15B	QP02NA	00	=		INTERRUPT CPU-A, CPU-B SENDER, STATE 2
XA332	TT3	C2	14B	(31)	01		PDLNO QONL20 PST020 29 14B 30 15A 34 16A	
XA332	TT3	C3	19B	QP06QA	00	=		DATA REQ. TO CPU-A, CPU-B SENDER
XA332	TT3	C3	16B	(39)	01		PDLNO QONL20 PST060 33 16B 35 17B 37 18B	
XA305	TD4	C1	17B	QP08NA	00	=		INTERRUPT CPU-A, CPU-B SENDER, STATE 8
XA305	TD4	C1	18B	(35)	01		PDLNO PST080 QE0B0Q QONL20 37 18B 38 18A 40 19A 42 20A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERN	DESIGNATOR	FACTOR	COMMENT
XA307	TQ2	B4	13B	QQDLCA	00	=		START REQ. DELAY TIMER, CPU-B
XA307	TQ2	B4	11B	(27)	01		QXXR0Q SPI0063 23 11B 25 12B	
XA332	TT3	D2	23B	QQ10NA	00	=		INTERRUPT CPU-B, SENDER, STATE
XA332	TT3	D2	22B	(43)	01		QONL20 QDLENO QRJ100 41 22B 46 21A 48 22A	10
XA305	TD4	C2	16B	QQ48QA	00	=		DATA REQUEST TO CPU-B, SENDER
XA305	TD4	C2	15A	(33)	01		QDLENO PST480 QE0BOP QONLNQ 30 15A 31 15B 34 16A 36 17A	
XA309	TQ2	A2	02B	QREL90	00	=		
XA309	TQ2	A2	04A	(01)	01		QXRAF5T SPI0063 04 04A 05 03B	
XA332	TT3	B3	13B	QRJCTA	00	=		RECEIVER REJECT, CPU-B SENDER
XA332	TT3	B3	10B	(27)	01		QONL20 PPERRS PST040 21 10B 23 11B 25 12B	
XA310	TQ2	A1	05A	QRJ100	00	=		DATA REJECT OR STATE 10, CPU-A
XA310	TQ2	A1	06A	(06)	01		QRJCTA PST10A 08 06A 10 07A	RECEIVING
XA310	TQ2	A2	02B	QRT0EA	00	=		ENABLE DATA TO RECEIVER, CPU-A
XA310	TQ2	A2	04A	(01)	01		QXEAOO PST070 04 04A 05 03B	
XA205	TT3	E3	33B	QRT12A	00	=		START DEV. STROBE, CPU-B
XA205	TT3	E3	30B	(63)	01		QONLNQ QXDV1B QSTRTO 57 30B 59 31B 61 32B	
XA324	TS8	B1	11B	QSTRBA	00	=		
XA324	TS8	B1	09A	(23)	01		QXXAOQ QXXA3Q QXXB20 QXDEVS QXROPA SPI0253 SPI0263 SPI0153 14 09A 18 10A 19 09B 20 11A 22 12A 24 13A 25 12B 26 14A	
XA208	TQ2	E4	33B	QSTRB0	00	=		
XA208	TQ2	E4	31B	(63)	01		QSTRBA SPI0033 59 31B 61 32B	
XA206	TQ2	F3	35A	QSTRTO	00	=		START 1 OR START 2, CPU-B
XA206	TQ2	F3	34B	(69)	01		QXRAF6T QXRAF7T 65 34B 74 35B	
XA303	TDD	CI	13A	QSTR1I	00	=		
XA303	TDD	CI		( )	01		QSTR80 24 13A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
				QSTR1N	00 =			
XA303	TDD	CN	14A	( )	01		QX1110 26 14A	
XA303	TDD	CP	11B	QSTR1P	00 =			
XA303	TDD	CP	12A	(23 )	01		PDVCLA 22 12A	
XA303	TDD	CQ	12B	QSTR1Q	00 =			
XA303	TDD	CQ	13B	(25 )	01		SPI0043 27 13B	
				QSTR2I	00 =			
XA304	TDD	CI	13A	( )	01		QSTR90 24 13A	
				QSTR2N	00 =			
XA304	TDD	CN	14A	( )	01		QX1110 26 14A	
XA304	TDD	CP	11B	QSTR2P	00 =			
XA304	TDD	CP	12A	(23 )	01		PDVCLA 22 12A	
XA304	TDD	CQ	12B	QSTR2Q	00 =			
XA304	TDD	CQ	13B	(25 )	01		SPI0043 27 13B	
XA306	TQ2	C4	19B	QSTR80	00 =			
XA306	TQ2	C4	17B	(39 )	01		QXRAF6T SPI0043 35 17B 37 18B	
XA307	TQ2	C4	19B	QSTR90	00 =			
XA307	TQ2	C4	17B	(39 )	01		QXRAF7T SPI0063 35 17B 37 18B	
XA208	TQ2	B4	13B	QST05A	00 =			NO INTERRUPT,CPU-B
XA208	TQ2	B4	11B	(27 )	01		Q0NL20 PXXDIR 23 11B 25 12B	
XA205	TT3	A2	03A	QTH04A	00 =			CPU-B SENDER,CPU-A DEV STROBE
XA205	TT3	A2	02B	(07 )	01		Q0NL10 PSTRB0 PDTERO 01 02B 03 02A 05 03B	
XA307	TQ2	B3	10B	QWRITA	00 =			CPU-B SENDING, STATE 5
XA307	TQ2	B3	08B	(21 )	01		Q0NLN0 PST050 17 08B 19 09B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESCR. PREFIX	FACTOR	COMMENT
			AND	OR					
XA310	TQ2	F4	39A		QWRITO	00	=		
XA310	TQ2	F4	37A		( 80 )	01		QWRITA SPI0073 76 37A 78 38A	
					QXACMB4	00	=		
XA343	YLD	D1	24A		( )	01		QXBCMD 52 24A	
					QXAENB4	00	=		
XA343	YLD	D2	21A		( )	01		QXBEND 46 21A	
					QXAPCB4	00	=		
XA343	YLD	D3	24B		( )	01		QXBPCD 45 24B	
					QXAOCB4	00	=		
XA343	YLD	E1	31A		( )	01		QXB0CD 66 31A	
					QXA1CB4	00	=		
XA343	YLD	E2	28A		( )	01		QXB1CD 60 28A	
					QXA2CB4	00	=		
XA343	YLD	E3	30B		( )	01		QXB2CD 57 30B	
					QXA3CB4	00	=		
XA343	YLD	E4	33B		( )	01		QXB3CD 63 33B	
					QXA4CB4	00	=		
XA343	YLD	F1	37B		( )	01		QXB4CD 75 37B	
					QXA5CB4	00	=		
XA343	YLD	F2	34A		( )	01		QXB5CD 72 34A	
					QXA6CB4	00	=		
XA343	YLD	F3	35A		( )	01		QXB6CD 69 35A	
					QXA7CB4	00	=		
XA343	YLD	F4	39A		( )	01		QXB7CD 80 39A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESIGNATION	FACTOR	COMMENT
			AND	OR					
XA343	TLD	D1	24A		QXBCMD	00	=		
XA343	TLD	D1	25A		(52)	01		QXBIF0 QXBCMOX 54 25A 56 26A	QXACMB4 BUSS
XA346	DCF	A1	02B		QXBCMDX	00	=		
XA346	DCF	A1	05A		(07)	01		QXB0EA 06 05A	TACMBB BUSS
XA346	DCF	A2	03B		QXBCMOX	00	=		
XA346	DCF	A2	04B		(09)	01		QXGNIA 11 04B	
XA343	TLD	D2	21A		QXBEND	00	=		
XA343	TLD	D2	22A		(46)	01		QXBIF0 QXBENOX 48 22A 50 23A	QXAENB4 BUSS
XA346	DCF	A3	07B		QXBENDX	00	=		
XA346	DCF	A3	05A		(17)	01		QXB0EA 06 05A	TAENBB BUSS
XA346	DCF	A4	06B		QXBENOX	00	=		
XA346	DCF	A4	05B		(15)	01		QXGNIA 13 05B	
XA309	TQ2	A3	04B		QXBIEA	00	=		
XA309	TQ2	A3	02A		(09)	01		QXBSLOX QXXDRA 03 02A 07 03A	PORT B INPUT ENABLE
XA310	TQ2	A3	04B		QXBIEO	00	=		
XA310	TQ2	A3	02A		(09)	01		QXBIEA SPI0063 03 02A 07 03A	
XA310	TQ2	A4	07B		QXBIF0	00	=		
XA310	TQ2	A4	05B		(15)	01		QXBIEA SPI0063 11 05B 13 06B	
XA346	DCF	A5	08B		QXBINDX	00	=		
XA346	DCF	A5	05A		(14)	01		QXB0EA 06 05A	TAINBB BUSS
XA346	DCF	A6	07A		QXBINOX	00	=		
XA346	DCF	A6	06A		(10)	01		QXDBIO 08 06A	
XA343	TLD	D3	24B		QXBPCD	00	=		
XA343	TLD	D3	22B		(45)	01		QXBIF0 QXBPCOX 41 22B 43 23B	QXAPCB4 BUSS

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TEMP	DESIGNATION	FACTOR	COMMENT
XA346	DCF	A7	02A	QXBPCDX	00	=		
XA346	DCF	A7	05A	(01)	01		QXBOEA 06 05A	TAQPBB BUSS
XA346	DCF	A8	03A	QXBPCOX	00	=		
XA346	DCF	A8	04A	(03)	01		QXDSBPR 04 04A	
XA305	TD4	D1	25B	QXBRQA	00	=		
XA305	TD4	D1	26B	(47)	01		QXBSLOX QXINHR DEVINH QXXROQ 49 26B 52 24A 54 25A 56 26A	
XA345	DCF	B3	15B	QXBRQDX	00	=		REQUEST TO CPU-B
XA345	DCF	B3	13A	(37)	01		PXGN1A 36 13A	
XA309	TQ2	A4	07B	QXBRQ0	00	=		
XA309	TQ2	A4	05B	(15)	01		QXBRQA SPI0063 11 05B 13 06B	
XA345	DCF	B4	14B	QXBRQOX	00	=		
XA345	DCF	B4	13B	(35)	01		QXBRQ0 33 13B	
XA203	TT3	C1	17A	QXB RSA	00	=		PORT B RESET
XA203	TT3	C1	18A	(36)	01		QXBCM0X QXBEN0X QXBSLOX 38 18A 40 19A 42 20A	
XA309	TQ2	B1	12A	QXB SLA	00	=		
XA309	TQ2	B1	13A	(22)	01		QXBSLOX SPI0063 24 13A 26 14A	
XA346	DCF	B5	16A	QXB SLDX	00	=		
XA346	DCF	B5	13A	(41)	01		SPI0233 36 13A	
XA346	DCF	B6	15A	QXB SLOX	00	=		
XA346	DCF	B6	14A	(40)	01		SPI0103 38 14A	
XA345	DCF	B7	10A	QXB STD4	00	=		PORT B STATUS DR1/RCLR
XA345	DCF	B7	13A	(23)	01		PXGN1A 36 13A	
XA345	DCF	B8	11A	QXB STOX	00	=		
XA345	DCF	B8	12A	(30)	01		QXBSLOX 34 12A	



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CONNECTOR	UNIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIG-NATOR	FACTOR	COMMENT
XA343	TLD	E1	31A	QXB0CD	00	=		QXA0CB4 BUSS
XA343	TLD	E1	32A	(66)	01		QXB1E0 QXB0COX 68 32A 70 33A	
XA346	DCF	C1	25B	QXB0CDX	00	=		TA00BB BUSS
XA346	DCF	C1	29A	(46)	01		QXB0EA 52 29A	
XA346	DCF	C2	26B	QXB0COX	00	=		
XA346	DCF	C2	27B	(47)	01		QXS031U 49 27B	
XA305	TD4	D2	24B	QXB0EA	00	=		PORT B OUTPUT ENABLE
XA305	TD4	D2	23B	(45)	01		QXBSLOX QXINHR DEVINH QXXCSO 43 23B 46 21A 48 22A 50 23A	
XA343	TLD	E2	28A	QXB1CD	00	=		QXA1CB4 BUSS
XA343	TLD	E2	29A	(60)	01		QXB1E0 QXB1COX 62 29A 64 30A	
XA346	DCF	C3	30B	QXB1CDX	00	=		TA01BB BUSS
XA346	DCF	C3	29A	(55)	01		QXB0EA 52 29A	
XA346	DCF	C4	29B	QXB1COX	00	=		
XA346	DCF	C4	28B	(56)	01		QXS032U 51 28B	
XA343	TLD	E3	30B	QXB2CD	00	=		QXA2CB4 BUSS
XA343	TLD	E3	28B	(57)	01		QXB1E0 QXB2COX 53 28B 55 29B	
XA346	DCF	C5	31B	QXB2CDX	00	=		TA02BB BUSS
XA346	DCF	C5	29A	(60)	01		QXB0EA 52 29A	
XA346	DCF	C6	31A	QXB2COX	00	=		
XA346	DCF	C6	30A	(57)	01		QXS033U 54 30A	
XA343	TLD	E4	33B	QXB3CD	00	=		QXA3CB4 BUSS
XA343	TLD	E4	31B	(63)	01		QXB1E0 QXB3COX 59 31B 61 32B	
XA346	DCF	C7	25A	QXB3CDX	00	=		TA03BB BUSS
XA346	DCF	C7	29A	(43)	01		QXB0EA 52 29A	



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATION	FACTOR	COMMENT
XA346	DCF	D8	34A	QX87C0X	00	II		
XA346	DCF	D8	35A	(18)	01		QXS474U 70 35A	
XA305	TD4	E1	31B	QXCA0A	00	=		
XA305	TD4	E1	32B	(59)	01		QXR6CS QXRCMS QXX050 QXROPA 61 32B 66 31A 68 32A 70 33A	
XA308	TS8	A1	05B	QXCA1A	00	=		
XA308	TS8	A1	02B	(11)	01		QXCMA5 QXXB10 QXXA0P QXXA1Q QXRPCS QXROCS QXR1CR QXR2CR 01 02B 04 04A 05 03B 06 05A 07 03A 08 06A 10 07A 13 06B	
XA310	TQ2	B1	12A	QXCA10	00	=		
XA310	TQ2	B1	13A	(22)	01		QXCA1A SPI0063 24 13A 26 14A	
XA305	TD4	B1	11B	QXCMAR	00	=		
XA305	TD4	B1	12A	(23)	01		QXCMA5 QXXB2A QXX04A PXR52B 22 12A 24 13A 25 12B 26 14A	
XA306	TQ2	B1	12A	QXCMA5	00	=		ADDRESS STORE F/F
XA306	TQ2	B1	13A	(22)	01		QXCMAR QXCA0A 24 13A 26 14A	
XA332	TT3	E2	29B	QXDBIA	00	=		
XA332	TT3	E2	28B	(55)	01		QXXDSQ QXXDOP QXXDIQ 53 28B 60 28A 62 29A	
XA336	TT3	F3	39A	QXDBI0	00	=		
XA336	TT3	F3	35A	(80)	01		QXXCIP QXXDIP QXDBIA 69 35A 76 37A 78 38A	
XA314	MUX	D1	27B	QXDB0TA	00	=		
XA314	MUX	D1	25B	(55)	01		PONL10 QXDB01X QXXCIQ QXXDIP 51 25B 53 26B 52 25A 49 24B	
XA314	MUX	D2	31B	QXDB0TB	00	=		
XA314	MUX	D2	29B	(61)	01		QONL10 QXDB11X 57 29B 59 30B	
XA314	MUX	D3	28A	QXDB0TC	00	=		
XA314	MUX	D3	26A	(60)	01		PPERRS QXDB21X 54 26A 56 28B	
XA314	MUX	D4	31A	QXDB0TD	00	=		
XA314	MUX	D4	29A	(63)	01		QPERRS QXDB31X 62 29A 64 30A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA312	MUX	A1	02A	QXDB01X	00	=		
XA312	MUX	A1	02B	(04)	01		PRO0BQ PR24BQ PR08BQ PR16BQ QXGNIA PONL10 PXXREQ PEBCCA 03 02B 05 03B 07 04B 09 05B 11 06B 15 07B 18 09A 17 08B	
XA312	MUX	A2	03A	QXDB02X	00	=		
XA312	MUX	A2	06A	(06)	01		QXXD1Q QXXD0Q QXXDSQ QXGN5A 14 06A 10 05A 08 04A 13 07A	
XA312	MUX	B1	10A	QXDB11X	00	=		
XA312	MUX	B1	09B	(20)	01		PR01BQ PR25BQ PR09BQ PR17BQ QXGNIA QONL10 QXXREQ PEBCCA 19 09B 21 10B 23 11B 25 12B 29 13B 31 14B 34 15B 33 16A	
XA312	MUX	B2	11A	QXDB12X	00	=		
XA312	MUX	B2	14A	(22)	01		QXXD1Q QXXD0Q QXXDSQ QXGN5A 27 14A 26 13A 24 12A 30 15A	
XA312	MUX	C1	17A	QXDB21X	00	=		
XA312	MUX	C1	16B	(36)	01		PR02BQ PR26BQ PR10BQ PR18BQ QXGNIA PPERRS PIPNDO PEBCCA 35 16B 37 17B 39 18B 41 19B 43 22B 45 23B 47 23A 50 24A	
XA312	MUX	C2	18A	QXDB22X	00	=		
XA312	MUX	C2	21A	(38)	01		QXXD1Q QXXD0Q QXXDSQ QXGN5A 46 21A 42 20A 40 19A 48 22A	
XA313	MUX	A1	02A	QXDB31X	00	=		
XA313	MUX	A1	02B	(04)	01		PR03BQ PR27BQ PR11BQ PR19BQ QXGNIA QPERRS QIPNDO PEBCAA 03 02B 05 03B 07 04B 09 05B 11 06B 15 07B 18 09A 17 08B	
XA313	MUX	A2	03A	QXDB32X	00	=		
XA313	MUX	A2	06A	(06)	01		QXXD1Q QXXD0Q QXXDSQ QXGN5A 14 06A 10 05A 08 04A 13 07A	
XA312	MUX	D1	27B	QXDB4TA	00	=		
XA312	MUX	D1	25B	(55)	01		PART10 QXDB41X QXXC1Q QXXDIP 51 25B 53 26B 52 25A 49 24B	
XA312	MUX	D2	31B	QXDB4TB	00	=		
XA312	MUX	D2	29B	(61)	01		PART20 QXDB51X 57 29B 59 30B	
XA312	MUX	D3	28A	QXDB4TC	00	=		
XA312	MUX	D3	26A	(60)	01		PE0MS0 QXDB61X 54 26A 56 28B	
XA312	MUX	D4	31A	QXDB4TD	00	=		
XA312	MUX	D4	29A	(63)	01		PTSM00 QXDB71X 62 29A 64 30A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA313	MUX	B1	10A	QXD341X	00	=		
XA313	MUX	B1	09B	(20)	01		PR04BQ PR28BQ PR12BQ PR20BQ QXGN1A PART10 P20DLO PSTCT4U 19 09B 21 10B 23 11B 25 12B 29 13B 31 14B 34 15B 33 16A	
XA313	MUX	B2	11A	QXDB42X	00	=		
XA313	MUX	B2	14A	(22)	01		QXXD1Q QXXD0Q QXXD5Q QXGN5A 27 14A 26 13A 24 12A 30 15A	
XA313	MUX	C1	17A	QXDB51X	00	=		
XA313	MUX	C1	16B	(36)	01		PR05BQ PR29BQ PR13BQ PR21BQ QXGN1A PART20 Q20DLO PSTCT3U 35 16B 37 17B 39 18B 41 19B 43 22B 45 23B 47 23A 50 24A	
XA313	MUX	C2	18A	QXDB52X	00	=		
XA313	MUX	C2	21A	(38)	01		QXXD1Q QXXD0Q QXXD5Q QXGN5A 46 21A 42 20A 40 19A 48 22A	
XA314	MUX	A1	02A	QXDB61X	00	=		
XA314	MUX	A1	02B	(04)	01		PR06BQ PR30BQ PR14BQ PR22BQ QXGN4A PEOMSO QXGN1A PSTCT2U 03 02B 05 03B 07 04B 09 05B 11 06B 15 07B 18 09A 17 08B	
XA314	MUX	A2	03A	QXDB62X	00	=		
XA314	MUX	A2	06A	(06)	01		QXXD1Q QXXD0Q QXXD5Q QXGN5A 14 06A 10 05A 08 04A 13 07A	
XA314	MUX	B1	10A	QXDB71X	00	=		
XA314	MUX	B1	09B	(20)	01		PR07BQ PR31BQ PR15BQ PR23BQ QXGN4A PTSMDO QXGN1A PSTCT1U 19 09B 21 10B 23 11B 25 12B 29 13B 31 14B 34 15B 33 16A	
XA314	MUX	B2	11A	QXDB72X	00	=		
XA314	MUX	B2	14A	(22)	01		QXXD1Q QXXD0Q QXXD5Q QXGN5A 27 14A 26 13A 24 12A 30 15A	
XA309	TQ2	B2	09A	QXDEVA	00	=		*
XA309	TQ2	B2	10A	(14)	01		QXR3CS SPI0063 18 10A 20 11A	
XA305	TQ4	B2	10B	QXDEVR	00	=		
XA305	TQ4	B2	09A	(21)	01		QXDEVS QXXB0A QXXB3A PXR52B 14 09A 18 10A 19 09B 20 11A	
XA306	TQ2	B2	09A	QXDEVS	00	=		DEVICE COMMAND STORE E/F
XA306	TQ2	B2	10A	(14)	01		QXDEVR QXDVOA 18 10A 20 11A	
XA310	TQ2	B2	09A	QXDEVO	00	=		
XA310	TQ2	B2	10A	(14)	01		QXDEVA SPI0063 18 10A 20 11A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
			AND	OR					
XA336	TT3	D3	27B		QXDIRA	00	=		
XA336	TT3	D3	24B		(51)	01		QXXDIQ QXXC2Q QXXC4Q 45 24B 47 25B 49 26B	INTERRUPT COMPLETED TO CPU-B
XA305	TD4	E2	30B		QXDPEA	00	=		
XA305	TD4	E2	29B		(57)	01		QWRITO QXENAS QXROPPR QXXA50 55 29B 60 28A 62 29A 64 30A	RECEIVE DATA PARITY ERROR
XA318	PAR	C1	20A		QXDSBPR	00	=		
XA318	PAR	C1	15B		(42)	01		QXS031U QXS032U QXS033U QXS034U QXS471U QXS472U QXS473U QXS474U 31 15B 33 16B 35 17B 37 18B 39 18A 36 17A 34 16A 30 15A	
XA318	PAR	C1	19B		( )	02	+	QXXCIQ 39 19B	
XA309	TQ2	B3	10B		QXDVCO	00	=		
XA309	TQ2	B3	08B		(21)	01		QXR090T SPI0063 17 08B 19 09B	
XA305	TD4	A1	05B		QXDVSR	00	=		
XA305	TD4	A1	05A		(11)	01		QXDVSS QXXBOA QXXB3A PXR52B 06 05A 08 06A 10 07A 13 06B	
XA306	TQ2	A4	07B		QXDVSS	00	=		
XA306	TQ2	A4	05B		(15)	01		QXDVSR QXDV5A 11 05B 13 06B	DEV COMMAND STOP STORE E/E
XA305	TD4	F1	37A		QXDVOA	00	=		
XA305	TD4	F1	37B		(76)	01		QXCA10 QXDEV0 QXDVCO PBUSYR 75 37B 77 38B 78 38A 79 39B	
XA308	TS8	B1	11B		QXDV1A	00	=		
XA308	TS8	B1	09A		(23)	01		QXDEV5 QXXB20 QXXA0P QXXA1Q QXROPA SPI0023 SPI0053 SPI0063 14 09A 18 10A 19 09B 20 11A 22 12A 24 13A 25 12B 26 14A	
					QXDV1B	00	=		
XA306	TQ2	F1	37B		( )	01		QXDV10 QXDV20 QXDV30 QXDV40 75 37B 72 34A 69 35A 80 39A	
XA306	TQ2	F1	37B		QXDV10	00	=		
XA306	TQ2	F1	38B		(75)	01		QXDV1A SPI0043 77 38B 79 39B	QXDV1B BUSS
XA306	TQ2	F2	34A		QXDV20	00	=		
XA306	TQ2	F2	36A		(72)	01		QXDV1A SPI0043 71 36A 73 36B	QXDV1B BUSS
XA306	TQ2	F3	35A		QXDV30	00	=		
XA306	TQ2	F3	34B		(69)	01		QXDV1A SPI0043 65 34B 74 35B	QXDV1B BUSS

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA306	TQ2	F4	39A	QXDV40	00	=		
XA306	TQ2	F4	37A	(80)	01		QXDVI8 SPI0043 76 37A 78 38A	BUSS
XA203	TT3	E3	33B	QXDV5A	00	=		
XA203	TT3	E3	30B	(63)	01		QXCA10 QXDEVO QXDVCO 57 30B 59 31B 61 32B	
XA305	TD4	F2	35A	QXEA0A	00	=		
XA305	TD4	F2	36A	(69)	01		QXR6CS QXRENS QXX050 QXROPA 71 36A 72 34A 73 36B 74 35B	
XA307	TQ2	F1	37B	QXEA00	00	=		
XA307	TQ2	F1	38B	(75)	01		QXEA0A SPI0063 77 38B 79 39B	
XA203	TT3	F1	36B	QXEBOA	00	=		
XA203	TT3	F1	37B	(73)	01		QXCA10 QXDEVA QXEBOB 75 37B 77 38B 79 39B	
				QXEBOI	00	=		
XA303	TDD	DI	10A	( )	01		QXCNIA 18 10A	
				QXEBOH	00	=		
XA303	TDD	DN	09A	( )	01		QXEBA 14 09A	
XA303	TDD	DP	10B	QXEBOH	00	=		
XA303	TDD	DP	11A	(21)	01		PXRSIB 20 11A	
XA303	TDD	DQ	09B	QXEBOQ	00	=		
XA303	TDD	DQ	08B	(19)	01		QXEBOA 17 08B	EOB SYNC COUNTER BIT 0
XA307	TQ2	F2	34A	QXEBA	00	=		
XA307	TQ2	F2	36A	(72)	01		QXEBAQ PXCP3B 71 36A 73 36B	
				QXEBAI	00	=		
XA304	TDD	DI	10A	( )	01		QXEBOQ 18 10A	
				QXEBAH	00	=		
XA304	TDD	DN	09A	( )	01		PXCP1B 14 09A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERJ	DESIG. NATOR	FACTOR	COMMENT
XA304	TDD	DP	10B	QXEB1P	00	=		
XA304	TDD	DP	11A	(21)	01		PXRS1B 20 11A	
XA304	TDD	DQ	09B	QXEB1Q	00	=		FOR SYNC COUNTER BIT 1
XA304	TDD	DQ	08B	(19)	01		SPI0023 17 08B	
XA208	TQ2	E2	28A	QXEB10	00	=		SYNCED FOR PULSE, CPU-B
XA208	TQ2	E2	29A	(60)	01		QXEB1A SPI0033 62 29A 64 30A	
XA311	TD4	A1	05B	QXED0A	00	=		RECEIVE DATA STROBE BYTE 0
XA311	TD4	A1	05A	(11)	01		QWRITO QXENAS QXXB10 QXXA50 06 05A 08 06A 10 07A 13 06B	
XA311	TD4	A2	04B	QXED1A	00	=		RECEIVE DATA STROBE BYTE 1
XA311	TD4	A2	02B	(09)	01		QWRITO QXENAS QXXB20 QXXA50 01 02B 04 04A 05 03B 07 03A	
XA311	TD4	B1	11B	QXED2A	00	=		RECEIVE DATA STROBE BYTE 2
XA311	TD4	B1	12A	(23)	01		QWRITO QXENAS QXXB30 QXXA50 22 12A 24 13A 25 12B 26 14A	
XA311	TD4	B2	10B	QXED3A	00	=		RECEIVE DATA STROBE BYTE 3
XA311	TD4	B2	09A	(21)	01		QWRITO QXENAS QXXB40 QXXA50 14 09A 18 10A 19 09B 20 11A	
XA305	TD4	A2	04B	QXENAR	00	=		
XA305	TD4	A2	02B	(09)	01		QXENAS QXXB5A QXX04A PXRS2B 01 02B 04 04A 05 03B 07 03A	
XA306	TQ2	A1	05A	QXENAS	00	=		ENABLE STORE F/E
XA306	TQ2	A1	06A	(06)	01		QXENAR QXEA0A 08 06A 10 07A	
XA310	TQ2	B3	10B	QXE0S0	00	=		
XA310	TQ2	B3	08B	(21)	01		QXR092T SPI0063 17 08B 19 09B	
XA327	TQ2	E1	31A	QXGN1A	00	=		
XA327	TQ2	E1	32A	(66)	01		SPI0183 SPI0193 68 32A 70 33A	
XA327	TQ2	E2	28A	QXGN4A	00	=		
XA327	TQ2	E2	29A	(60)	01		SPI0193 SPI0183 62 29A 64 30A	



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA327	TQ2	E3	30B	QXGN5A	00 =			
XA327	TQ2	E3	28B	(57)	01		SPT0193 SPI0183 53 28B 55 29B	
XA309	TQ2	B4	13B	QXHST0	00 =			
XA309	TQ2	B4	11B	(27)	01		QXR091T SPI0063 23 11B 25 12B	
XA203	TT3	F2	35B	QXH50A	00 =			
XA203	TT3	F2	34B	(74)	01		QXCA10 QXDEVA QXHST0 65 34B 71 36A 72 34A	
XA202	TQ2	A2	02B	QXINHR	00 =			
XA202	TQ2	A2	04A	(01)	01		QXINHS QXIN0A 04 04A 05 03B	
XA203	TT3	A2	03A	QXINHS	00 =			OFF LINE STORE F/E
XA203	TT3	A2	02B	(07)	01		QXINHR QXONLO PXR50B 01 02B 03 02A 05 03B	
XA203	TT3	B1	11A	QXIN0A	00 =			
XA203	TT3	B1	12A	(20)	01		PXR50B PXDVE0 QXONLO 22 12A 24 13A 26 14A	
XA310	TQ2	B4	13B	QXIRCO	00 =			
XA310	TQ2	B4	11B	(27)	01		QXR094T SPI0063 23 11B 25 12B	
XA203	TT3	F3	39A	QXIROA	00 =			
XA203	TT3	F3	35A	(80)	01		QXCA10 QXDEVA QXIRCO 69 35A 76 37A 78 38A	
XA301	TDD	FI	16A	QXIROI	00 =			
XA301	TDD	FN	15A	( )	01		QXGN1A 34 16A	
XA301	TDD	FP	16B	QXIRON	00 =			
XA301	TDD	FP	17A	( )	01		QXIR1A 30 15A	
XA301	TDD	FQ	15B	QXIROP	00 =			
XA301	TDD	FQ	14B	(33)	01		PXR51B 36 17A	
XA301	TDD	FQ	15B	QXIROQ	00 =			ITR SYNC COUNTER BIT 0
XA301	TDD	FQ	14B	(31)	01		QXIROA 29 14B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	LOGIC FACTOR	FACTOR	COMMENT
			AND	OR					
XA307	TQ2	F3	35A		QXIR1A	00	=		
XA307	TQ2	F3	34B		(69)	01	=	QXIR1Q PXCP3B 65 34B 74 35B	
					QXIR1I	00	=		
XA302	YDD	FI	16A		( )	01	=	QXIROQ 34 16A	
					QXIR1N	00	=		
XA302	YDD	FN	15A		( )	01	=	PXCP1B 30 15A	
XA302	TDD	FP	16B		QXIR1P	00	=		
XA302	TDD	FP	17A		(33)	01	=	PXRS1B 36 17A	
XA302	TDD	FQ	15B		QXIR1Q	00	=		
XA302	YDD	FQ	14B		(31)	01	=	SPI0023 29 14B	ITR SYNC COUNTER BIT 1
XA206	TQ2	D2	21A		QXIR10	00	=		
XA206	TQ2	D2	22A		(46)	01	=	QXIR1A SPI0033 48 22A 50 23A	SYNCED ITR PULSE, CPU-B
XA315	DBC	A1	04A		QXRAF0T	00	=		
XA315	DBC	A1	02A		(08)	01	=	QXR7CS 04 02A	
XA315	DBC	A2	05A		QXRAF1T	00	=		
XA315	DBC	A2	03A		(10)	01	=	QXR6CS 06 03A	
XA315	DBC	A3	06A		QXRAF2T	00	=		
XA315	DBC	A3	02B		(14)	01	=	QXR5CS 03 02B	
XA315	DBC	A4	07A		QXRAF3T	00	=		
XA315	DBC	A4	03B		(13)	01	=	QXR4CR 05 03B	
XA315	DBC	A5	08B		QXRAF4T	00	=		
XA315	DBC	A5			(17)	01	=	SPA 4T	
XA315	DBC	A6	04B		QXRAF5T	00	=		
XA315	DBC	A6			(07)	01	=	SPA 5T	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	=	FACTOR	COMMENT
XA315	DBC	A7	05B	QXRAF6T	00	=			
XA315	DBC	A7		(09)	01	=	SPA 6T		
XA315	DBC	A8	06B	QXRAF7T	00	=			
XA315	DBC	A8		(11)	01	=	SPA 7T		
XA315	DBC	A9	07B	QXRAF8T	00	=			
XA315	DBC	A9		(15)	01	=	SPA 8T		
XA315	DBC	A0	09A	QXRAF9T	00	=			
XA315	DBC	A0		(18)	01	=	SPA 9T		
XA306	TQ2	C1	18A	QXRCLR	00	=			
XA306	TQ2	C1	19A	(38)	01	=	QXRCLS QXRRSA 40 19A 42 20A		
XA307	TQ2	C1	18A	QXRCLS	00	=		I/O INPUT REG COMMAND BIT	
XA307	TQ2	C1	19A	(38)	01	=	QXRCLR QXACMB4 40 19A 42 20A		
XA306	TQ2	C2	15A	QXRENR	00	=			
XA306	TQ2	C2	16A	(30)	01	=	QXRENS QXRRSA 34 16A 36 17A		
XA307	TQ2	C2	15A	QXRENS	00	=		I/O INPUT REG ENABLE BIT	
XA307	TQ2	C2	16A	(30)	01	=	QXRENR QXAENB4 34 16A 36 17A		
XA306	TQ2	C3	16B	QXRPCR	00	=			
XA306	TQ2	C3	14B	(33)	01	=	QXRPCS QXRRSA 29 14B 31 15B		
XA307	TQ2	C3	16B	QXRPCS	00	=		I/O INPUT REG PARIITY BIT	
XA307	TQ2	C3	14B	(33)	01	=	QXRPCR QXAPCB4 29 14B 31 15B		
XA309	TQ2	A1	05A	QXRRSA	00	=		I/O INPUT REG RESET	
XA309	TQ2	A1	06A	(06)	01	=	QXRRS0 SPI0063 08 06A 10 07A		
XA307	TQ2	F4	39A	QXRRS0	00	=			
XA307	TQ2	F4	37A	(80)	01	=	QXXA6A PXR52B 76 37A 78 38A		

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA306	TQ2	D1	24A	QXR0CR	00	=		
XA306	TQ2	D1	25A	(52)	01	=	QXR0CS QXRRSA 54 25A 56 26A	
XA307	TQ2	D1	24A	QXR0CS	00	=		I/O INPUT REG DATA BIT 0
XA307	TQ2	D1	25A	(52)	01	=	QXR0CR QXA0CB4 54 25A 56 26A	
XA327	TQ2	F1	37B	QXR0PA	00	=		
XA327	TQ2	F1	38B	(75)	01	=	QXR0PPR SPI0193 77 38B 79 39B	
XA318	PAR	D1	26A	QXR0PPR	00	=		
XA318	PAR	D1	23B	(56)	01	=	QXR0CS QXR1CS QXR2CS QXR3CS QXR4CS QXR5CS QXR6CS QXR7CS 43 23B 45 24B 47 25B 49 26B 52 24A 50 23A 48 22A 46 21A	
XA318	PAR	D1	27B	( )	02	+	QXRPCS 51 27B	
XA315	DBC	B1	12A	QXR090T	00	=		
XA315	DBC	B1	10A	(24)	01	=	QXR7CS 20 10A	
XA315	DBC	B2	13A	QXR091T	00	=		
XA315	DBC	B2	11A	(26)	01	=	QXR6CS 22 11A	
XA315	DBC	B3	14A	QXR092T	00	=		
XA315	DBC	B3	09B	(27)	01	=	QXR5CS 19 09B	
XA315	DBC	B4	15A	QXR093T	00	=		
XA315	DBC	B4	10B	(30)	01	=	QXR4CS 21 10B	
XA315	DBC	B5	16A	QXR094T	00	=		
XA315	DBC	B5		(33)	01	=	SPA 4T	
XA315	DBC	B6	11B	QXR095T	00	=		
XA315	DBC	B6		(23)	01	=	SPA 5T	
XA315	DBC	B7	12B	QXR096T	00	=		
XA315	DBC	B7		(25)	01	=	SPA 6T	
XA315	DBC	B8	13B	QXR097T	00	=		
XA315	DBC	B8		(29)	01	=	SPA 7T	

CONNECTOR	OUTPUT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA315	DBC	B9	14B	QXR098T	00	=		
XA315	DBC	B9		(31)	01		SPA 8T	
XA315	DBC	B0	15B	QXR099T	00	=		
XA315	DBC	B0		(34)	01		SPA 9T	
XA306	TQ2	D2	21A	QXR1CR	00	=		
XA306	TQ2	D2	22A	(46)	01		QXR1CS QXRRSA 48 22A 50 23A	
XA307	TQ2	D2	21A	QXR1CS	00	=		I/O INPUT REG DATA BIT 1
XA307	TQ2	D2	22A	(46)	01		QXR1CR QXA1CB4 48 22A 50 23A	
XA306	TQ2	D3	24B	QXR2CR	00	=		
XA306	TQ2	D3	22B	(45)	01		QXR2CS QXRRSA 41 22B 43 23B	
XA307	TQ2	D3	24B	QXR2CS	00	=		I/O INPUT REG DATA BIT 2
XA307	TQ2	D3	22B	(45)	01		QXR2CR QXA2CB4 41 22B 43 23B	
XA306	TQ2	D4	27B	QXR3CR	00	=		
XA306	TQ2	D4	25B	(51)	01		QXR3CS QXRRSA 47 25B 49 26B	
XA307	TQ2	D4	27B	QXR3CS	00	=		I/O INPUT REG DATA BIT 3
XA307	TQ2	D4	25B	(51)	01		QXR3CR QXA3CB4 47 25B 49 26B	
XA306	TQ2	E1	31A	QXR4CR	00	=		
XA306	TQ2	E1	32A	(66)	01		QXR4CS QXRRSA 68 32A 70 33A	
XA307	TQ2	E1	31A	QXR4CS	00	=		I/O INPUT REG DATA BIT 4
XA307	TQ2	E1	32A	(66)	01		QXR4CR QXA4CB4 68 32A 70 33A	
XA306	TQ2	E2	28A	QXR5CR	00	=		
XA306	TQ2	E2	29A	(60)	01		QXR5CS QXRRSA 62 29A 64 30A	
XA307	TQ2	E2	28A	QXR5CS	00	=		I/O INPUT REG DATA BIT 5
XA307	TQ2	E2	29A	(60)	01		QXR5CR QXA5CB4 62 29A 64 30A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA306	TQ2	E3	30B	QXR6CR	00	=		
XA306	TQ2	E3	28B	(57)	01	=	QXR6CS QXRRSA 53 28B 55 29B	
XA307	TQ2	E3	30B	QXR6CS	00	=		I/O INPUT REG DATA BIT 6
XA307	TQ2	E3	28B	(57)	01	=	QXR6CR QXA6CB4 53 28B 55 29B	
XA306	TQ2	E4	33B	QXR7CR	00	=		
XA306	TQ2	E4	31B	(63)	01	=	QXR7CS QXRRSA 59 31B 61 32B	
XA307	TQ2	E4	33B	QXR7CS	00	=		I/O INPUT REG DATA BIT 7
XA307	TQ2	E4	31B	(63)	01	=	QXR7CR QXA7CB4 59 31B 61 32B	
XA310	TQ2	D1	24A	QXSK0A	00	=		
XA310	TQ2	D1	25A	(52)	01	=	QXXC2Q QXXC3P 54 25A 56 26A	
XA309	TQ2	D1	24A	QXSK00	00	=		STATUS REG CLOCK BITS 0 TO 3
XA309	TQ2	D1	25A	(52)	01	=	QXSK0A SPI0063 54 25A 56 26A	
XA327	TQ2	F2	34A	QXSST0	00	=		
XA327	TQ2	F2	36A	(72)	01	=	QXR097T SPI0193 71 36A 73 36B	
XA308	TS8	C1	17B	QXSS0A	00	=		
XA308	TS8	C1	15A	(35)	01	=	QXDVSS QXXB20 QXXA0P QXXA1Q QXSST0 QXROPA SPI0063 SPI0023 30 15A 31 15B 34 16A 36 17A 37 18B 38 18A 40 19A 42 20A	
				QXST0I	00	=		
XA303	TDD	EI	19A	( )	01	=	QXGN1A 40 19A	
				QXSTON	00	=		
XA303	TDD	EN	20A	( )	01	=	QXST1A 42 20A	
XA303	TDD	EP	17B	QXSTOP	00	=		
XA303	TDD	EP	18A	(35)	01	=	SPI0043 38 18A	
XA303	TDD	EQ	18B	QXSTOQ	00	=		STOP SYNC COUNTER BIT 0
XA303	TDD	EQ	19B	(37)	01	=	QXST2A 39 19B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM.	DESIG. PREFIX	FACTOR	COMMENT
XA327	TQ2	F3	35A	QXST1A	00 =			
XA327	TQ2	F3	34B	(69)	01		QXST1Q PXCP3B 65 34B 74 35B	
				QXST1I	00 =			
XA304	TDD	E1	19A	( )	01		QXST00 40 19A	
				QXST1N	00 =			
XA304	TDD	EN	20A	( )	01		PXCP1B 42 20A	
XA304	TDD	EP	17B	QXST1P	00 =			
XA304	TDD	EP	18A	(35)	01		SPI0043 38 18A	
XA304	TDD	EQ	18B	QXST1Q	00 =			STOP SYNC COUNTER BIT 1
XA304	TDD	EQ	19B	(37)	01		SPI0023 39 19B	
XA310	TQ2	D2	21A	QXST2A	00 =			
XA310	TQ2	D2	22A	(46)	01		QXST20 SPI0063 48 22A 50 23A	
XA309	TQ2	D2	21A	QXST20	00 =			
XA309	TQ2	D2	22A	(46)	01		QXH50A QX550A 48 22A 50 23A	
XA316	DBC	C1	18A	QXS031U	00 =			
XA316	DBC	C1	18B	(38)	01		QXDB0TA QXDB0TB QXDB0TC QXDB0TD QXGN4A 39 18B 41 19B 43 22B 45 23B 50 24A	
XA316	DBC	C2	19A	QXS032U	00 =			
XA316	DBC	C2	17B	(40)	01		QXSK00 37 17B	
XA316	DBC	C3	20A	QXS033U	00 =			
XA316	DBC	C3	23A	(42)	01		SPI0233 47 23A	
XA316	DBC	C4	21A	QXS034U	00 =			
XA316	DBC	C4	22A	(46)	01		SPI0243 48 22A	
XA316	DBC	C5	17A	QXS035U	00 =			
XA316	DBC	C5	16B	(36)	01		SPI0073 35 16B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA316	DBC	D1	26A	QXS471U	00	=		
XA316	DBC	D1	25B	(54)	01		QXDB4TA QXDB4TB QXDB4TC QXDB4TD QXGN4A 53 26B 55 27B 57 29B 59 30B 63 31A	
XA316	DBC	D2	28B	QXS472U	00	=		
XA316	DBC	D2	25B	(56)	01		QXSK00 51 25B	
XA316	DBC	D3	28A	QXS473U	00	=		
XA316	DBC	D3	31B	(60)	01		SPI0073 61 31B	
XA316	DBC	D4	29A	QXS474U	00	=		
XA316	DBC	D4	30A	(62)	01		SPI0243 64 30A	
XA316	DBC	D5	25A	QXS475U	00	=		
XA316	DBC	D5	24B	(52)	01		SPI0233 49 24B	
XA327	TQ2	F4	39A	QXXACA	00	=		
XA327	TQ2	F4	37A	(80)	01		QXXACO SPI0193 76 37A 78 38A	
				QXXACI	00	=		
XA303	TDD	FI	16A	( )	01		SPI0023 34 16A	
				QXXACN	00	=		
XA303	TDD	FN	15A	( )	01		QXXA3P 30 15A	
XA303	TDD	FP	16B	QXXACP	00	=		
XA303	TDD	FP	17A	(33)	01		QXXADA 36 17A	
XA303	TDD	FQ	15B	QXXACQ	00	=		
XA303	TDD	FQ	14B	(31)	01		PXRS1B 29 14B	I/O STATE COUNTER CONTROL E/E
XA308	TS8	D1	25B	QXXACO	00	=		
XA308	TS8	D1	23B	(47)	01		QXROCR QXR1CR QXR2CR QXR3CR QXR4CR QXR5CR QXR6CR QXR7CR 43 23B 46 21A 48 22A 49 26B 50 23A 52 24A 54 25A 56 26A	
XA309	TQ2	D3	24B	QXXADA	00	=		
XA309	TQ2	D3	22B	(45)	01		QXXADO SPI0063 41 22B 43 23B	I/O STATE COUNTER START



CONNECTOR	CURCUT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM =	DESIG- NATOR	FACTOR	COMMENT
XA311	TD4	C1	17B	QXXADO	00	=		
XA311	TD4	C1	18B	(35)	01		QXRCLR QXREN QXRPCR QXXACA 37 18B 38 18A 40 19A 42 20A	
				QXXA0I	00	=		
XA301	TDD	GI	25A	( )	01		QXXA3P 54 25A	
				QXXA0N	00	=		
XA301	TDD	GN	26A	( )	01		Q16MH0 56 26A	
XA301	TDD	GP	25B	QXXA0P	00	=		
XA301	TDD	GP	24A	(47)	01		QXXACP 52 24A	
XA301	TDD	GQ	26B	QXXA0Q	00	=		I/O STATE COUNTER BIT 0
XA301	TDD	GQ	27B	(49)	01		SPT0043 51 27B	
				QXXA1I	00	=		
XA302	TDD	GI	25A	( )	01		QXXA0Q 54 25A	
				QXXA1N	00	=		
XA302	TDD	GN	26A	( )	01		Q16MH0 56 26A	
XA302	TDD	GP	25B	QXXA1P	00	=		
XA302	TDD	GP	24A	(47)	01		QXXACP 52 24A	
XA302	TDD	GQ	26B	QXXA1Q	00	=		I/O STATE COUNTER BIT 1
XA302	TDD	GQ	27B	(49)	01		SPT0043 51 27B	
				QXXA2I	00	=		
XA301	TDD	HI	22A	( )	01		QXXA1Q 48 22A	
				QXXA2N	00	=		
XA301	TDD	HN	21A	( )	01		Q16MH0 46 21A	
XA301	TDD	HP	24B	QXXA2P	00	=		
XA301	TDD	HP	23A	(45)	01		QXXACP 50 23A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA301	TDD	HQ	23B	QXXA2Q	00 =			I/O STATE COUNTER BIT 2
XA301	TDD	HQ	22B	(43)	01	SPI0023 41 22B		
				QXXA3I	00 =			
XA302	TDD	HI	22A	( )	01	QXXA2Q 48 22A		
				QXXA3N	00 =			
XA302	TDD	HN	21A	( )	01	Q16MHO 46 21A		
XA302	TDD	HP	24B	QXXA3P	00 =			
XA302	TDD	HP	23A	(45)	01	QXXACP 50 23A		
XA302	TDD	HQ	23B	QXXA3Q	00 =			I/O STATE COUNTER BIT 2
XA302	TDD	HQ	22B	(43)	01	SPI0023 41 22B		
XA309	TQ2	E1	31A	QXXA4A	00 =			I/O STATE COUNTER STATE 4
XA309	TQ2	E1	32A	(66)	01	QXXA0Q QXXA3Q 68 32A 70 33A		
XA310	TQ2	E1	31A	QXXA40	00 =			
XA310	TQ2	E1	32A	(66)	01	QXXA4A SPI0063 68 32A 70 33A		
XA309	TQ2	E2	28A	QXXA5A	00 =			I/O STATE COUNTER STATE 5
XA309	TQ2	E2	29A	(60)	01	QXXA0P QXXA1Q 62 29A 64 30A		
XA310	TQ2	E2	28A	QXXA50	00 =			
XA310	TQ2	E2	29A	(60)	01	QXXA5A SPI0063 62 29A 64 30A		
XA309	TQ2	D4	27B	QXXA6A	00 =			I/O STATE COUNTER STATE 6
XA309	TQ2	D4	25B	(51)	01	QXXA1P QXXA2Q 47 25B 49 26B		
XA310	TQ2	D3	24B	QXXBCA	00 =			I/O BYTE COUNTER RESET
XA310	TQ2	D3	22B	(45)	01	QXXBC0 SPI0063 41 22B 43 23B		
XA332	TT3	E3	33B	QXXBC0	00 =			
XA332	TT3	E3	30B	(63)	01	QXRMR QXREN PRS2B 57 30B 59 31B 61 32B		

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA310	TQ2	D4	27B	QXXBK0	00	=		I/O BYTE COUNTER CLOCK
XA310	TQ2	D4	25B	(51)	01		QXXA3Q SPI0063 47 25B 49 26B	
XA309	TQ2	E3	30B	QXXB0A	00	=		I/O BYTE COUNTER STATE 0
XA309	TQ2	E3	28B	(57)	01		QXXB0P QXXB2P 53 28B 55 29B	
				QXXB0I	00	=		
XA302	TDD	J1	32A	( )	01		QXXB2P 68 32A	
				QXXB0N	00	=		
XA302	TDD	JN	33A	( )	01		QXXBK0 70 33A	
XA302	TDD	JP	31B	QXXB0P	00	=		
XA302	TDD	JP	31A	(59)	01		QXXB0A 66 31A	
XA302	TDD	JQ	32B	QXXB0Q	00	=		I/O BYTE COUNTER BIT 0
XA302	TDD	JQ	33B	(61)	01		SPI0043 63 33B	
XA310	TQ2	E3	30B	QXXB00	00	=		
XA310	TQ2	E3	28B	(57)	01		QXXB0A SPI0063 53 28B 55 29B	
XA309	TQ2	E4	33B	QXXB1A	00	=		I/O BYTE COUNTER STATE 1
XA309	TQ2	E4	31B	(63)	01		QXXB0Q QXXB1P 59 31B 61 32B	
				QXXB1I	00	=		
XA303	TDD	G1	25A	( )	01		QXXB0Q 54 25A	
				QXXB1N	00	=		
XA303	TDD	GN	26A	( )	01		QXXBK0 56 26A	
XA303	TDD	GP	25B	QXXB1P	00	=		
XA303	TDD	GP	24A	(47)	01		QXXB0A 52 24A	
XA303	TDD	GQ	26B	QXXB1Q	00	=		I/O BYTE COUNTER BIT 1
XA303	TDD	GQ	27B	(49)	01		SPI0043 51 27B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA310	TQ2	E4	33B	QXXB10	00	=		
XA310	TQ2	E4	31B	(63)	01		QXXB1A SPI0063 59 31B 61 32B	
XA309	TQ2	C1	18A	QXXB2A	00	=		I/O BYTE COUNTER STATE 2
XA309	TQ2	C1	19A	(38)	01		QXXB1Q QXXB2P 40 19A 42 20A	
XA304	TDD	FI	16A	QXXB2I	00	=		
				( )	01		QXXB1Q 34 16A	
XA304	TDD	FN	15A	QXXB2N	00	=		
				( )	01		QXXBK0 30 15A	
XA304	TDD	FP	16B	QXXB2P	00	=		
XA304	TDD	FP	17A	(33)	01		QXXBCA 36 17A	
XA304	TDD	FQ	15B	QXXB2Q	00	=		I/O BYTE COUNTER BIT 2
XA304	TDD	FQ	14B	(31)	01		SPI0053 29 14B	
XA310	TQ2	C1	18A	QXXB20	00	=		
XA310	TQ2	C1	19A	(38)	01		QXXB2A SPI0063 40 19A 42 20A	
XA309	TQ2	C2	15A	QXXB3A	00	=		I/O BYTE COUNTER STATE 3
XA309	TQ2	C2	16A	(30)	01		QXXB0Q QXXB2Q 34 16A 36 17A	
XA310	TQ2	C2	15A	QXXB30	00	=		
XA310	TQ2	C2	16A	(30)	01		QXXB3A SPI0063 34 16A 36 17A	
XA309	TQ2	C3	16B	QXXB4A	00	=		I/O BYTE COUNTER STATE 4
XA309	TQ2	C3	14B	(33)	01		QXXB0P QXXB1Q 29 14B 31 15B	I/O BYTE COUNTER STATE 4
XA310	TQ2	C3	16B	QXXB40	00	=		
XA310	TQ2	C3	14B	(33)	01		QXXB4A SPI0063 29 14B 31 15B	
XA309	TQ2	C4	19B	QXXB5A	00	=		I/O BYTE COUNTER STATE 5
XA309	TQ2	C4	17B	(39)	01		QXXB1P QXXB2Q 35 17B 37 18B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA310	TQ2	C4	19B	QXXB50	00	=		
XA310	TQ2	C4	17B	(139)	01		QXXB5A SPI0063 35 17B 37 18B	
XA328	TQ2	C3	16B	QXXCIA	00	=		
XA328	TQ2	C3	14B	(133)	01		QXXC10 SPI0193 29 14B 31 15B	
XA304	TDD	GI	25A	QXXCII	00	=		
				( )	01		QXGN4A 54 25A	
XA304	TDD	GN	26A	QXXCIN	00	=		
				( )	01		QXXC4P 56 26A	
XA304	TDD	GP	25B	QXXCIP	00	=		
XA304	TDD	GP	24A	(147)	01		PXR51B 52 24A	
XA304	TDD	GQ	26B	QXXCIQ	00	=		DEV/OPR INDICATOR CONTROL F/F
XA304	TDD	GQ	27B	(149)	01		QXXCIA 51 27B	
XA311	TD4	C2	16B	QXXC10	00	=		INDICATOR INPUTS
XA311	TD4	C2	15A	(133)	01		QDV0FA QDV0NA QXSS0A QX0D3A 30 15A 31 15B 34 16A 36 17A	
XA311	TD4	D1	25B	QXXCRO	00	=		
XA311	TD4	D1	26B	(147)	01		QXXC1P QXXDDP QXXDIP QXXDSP 49 26B 52 24A 54 25A 56 26A	
XA309	TQ2	F1	37B	QXXCSA	00	=		INPUT STROBE COUNT 5 TO 7
XA309	TQ2	F1	38B	(175)	01		QXXC2Q QXXC4Q 77 38B 79 39B	
XA310	TQ2	F1	37B	QXXCS0	00	=		
XA310	TQ2	F1	38B	(175)	01		QXXCSA SPI0063 77 38B 79 39B	
XA303	TDD	H1	22A	QXXCOI	00	=		
				( )	01		QXXC4P 48 22A	
XA303	TDD	HN	21A	QXXCON	00	=		
				( )	01		Q16M10 46 21A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM #	DESIG. FACTOR	FACTOR	COMMENT
XA303	TDD	HP	24B	QXXCOP	00	=		
XA303	TDD	HP	23A	(45)	01		QXXCRO 50 23A	
XA303	TDD	HQ	23B	QXXCOQ	00	=		
XA303	TDD	HQ	22B	(43)	01		SPI0023 41 22B	INPUT STROBE COUNTER BIT 0
XA304	TDD	HI	22A	QXXC1I	00	=		
				( )	01		QXXCOQ 48 22A	
XA304	TDD	HN	21A	QXXC1N	00	=		
				( )	01		Q16M10 46 21A	
XA304	TDD	HP	24B	QXXC1P	00	=		
XA304	TDD	HP	23A	(45)	01		QXXCRO 50 23A	
XA304	TDD	HQ	23B	QXXC1Q	00	=		
XA304	TDD	HQ	22B	(43)	01		SPI0043 41 22B	INPUT STROBE COUNTER BIT 1
XA303	TDD	JI	32A	QXXC2I	00	=		
				( )	01		QXXC1Q 68 32A	
XA303	TDD	JN	33A	QXXC2N	00	=		
				( )	01		Q16M10 70 33A	
XA303	TDD	JP	31B	QXXC2P	00	=		
XA303	TDD	JP	31A	(59)	01		QXXCRO 66 31A	
XA303	TDD	JQ	32B	QXXC2Q	00	=		
XA303	TDD	JQ	33B	(61)	01		SPI0043 63 33B	INPUT STROBE COUNTER BIT 2
XA304	TDD	JI	32A	QXXC3I	00	=		
				( )	01		QXXC2Q 68 32A	
XA304	TDD	JN	33A	QXXC3N	00	=		
				( )	01		Q16M10 70 33A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
			AND	OR					
XA304	TDD	JP	31B		QXXC3P	00	=		
XA304	TDD	JP	31A	(59)		01		QXXCR0 66 31A	
XA304	TDD	JQ	32B		QXXC3Q	00	=		INPUT STROBE COUNTER BIT 3
XA304	TDD	JQ	33B	(61)		01		SPI0043 63 33B	
XA301	TDD	JJ	32A		QXXC4I	00	=		
XA301	TDD	JJ	32A	( )		01		QXXC3Q 68 32A	
XA301	TDD	JN	33A		QXXC4N	00	=		
XA301	TDD	JN	33A	( )		01		Q16M10 70 33A	
XA301	TDD	JP	31B		QXXC4P	00	=		
XA301	TDD	JP	31A	(59)		01		QXXCR0 66 31A	
XA301	TDD	JQ	32B		QXXC4Q	00	=		INPUT STROBE COUNTER BIT 4
XA301	TDD	JQ	33B	(61)		01		SPI0043 63 33B	
XA310	TQ2	F2	34A		QXXDDA	00	=		
XA310	TQ2	F2	36A	(72)		01		QXXDD0 SPI0073 71 36A 73 36B	
XA304	TDD	KI	29A		QXXDDI	00	=		
XA304	TDD	KI	29A	( )		01		QXGN4A 62 29A	
XA304	TDD	KN	28A		QXXDDN	00	=		
XA304	TDD	KN	28A	( )		01		QXXD1P 60 28A	
XA304	TDD	KP	30B		QXXDDP	00	=		
XA304	TDD	KP	30A	(57)		01		PXRS1B 64 30A	
XA304	TDD	KQ	29B		QXXDDQ	00	=		INPUT DATA CONTROL E/F
XA304	TDD	KQ	28B	(55)		01		QXXCDA 53 28B	
XA309	TQ2	F2	34A		QXXDD0	00	=		
XA309	TQ2	F2	36A	(72)		01		QRIDEA QLTE0A 71 36A 73 36B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERN	DESIGNATOR	FACTOR	COMMENT
XA309	TQ2	F3	35A	QXXDIA	00	=		
XA309	TQ2	F3	34B	(69)	01		QXXDIS QXEA00 65 34B 74 35B	
XA301	TDD	LI	38B	QXXDII	00	=		
XA301	TDD	LI	38B	( )	01		QXGN4A 77 38B	
XA301	TDD	LN	39B	QXXDIN	00	=		
XA301	TDD	LN	39B	( )	01		QXXDIP 79 39B	
XA301	TDD	LP	37A	QXXDIP	00	=		
XA301	TDD	LP	37B	(76)	01		PXRS1B 75 37B	
XA301	TDD	LQ	38A	QXXDIQ	00	=		
XA301	TDD	LQ	39A	(78)	01		QXXDIA 80 39A	INPUT INTERRUPT CONTROL F/F
XA332	TT3	F2	35B	QXXDIR	00	=		
XA332	TT3	F2	34B	(74)	01		QXXDIS QXDIRA QXINHR 65 34B 71 36A 72 34A	
XA333	TD4	F2	35A	QXXDIS	00	=		
XA333	TD4	F2	36A	(69)	01		QXXDIR PQ08NA PQ02NA QQ10NA 71 36A 72 34A 73 36B 74 35B	
XA207	TQ2	F3	35A	QXXDRA	00	=		
XA207	TQ2	F3	34B	(69)	01		QXXDRO SPI0033 65 34B 74 35B	
XA332	TT3	F3	39A	QXXDRO	00	=		
XA332	TT3	F3	35A	(80)	01		QXXDDP QXXDIP QXXDSP 69 35A 76 37A 78 38A	
XA208	TQ2	D3	24B	QXXDSA	00	=		
XA208	TQ2	D3	22B	(45)	01		QXXDDQ QXXCS0 41 22B 43 23B	
XA302	TDD	KI	29A	QXXDSI	00	=		
XA302	TDD	KI	29A	( )	01		QXGN4A 62 29A	
XA302	TDD	KN	28A	QXXDSN	00	=		
XA302	TDD	KN	28A	( )	01		QXXD1P 60 28A	



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA302	TDD	KP	30B	QXXDSP	00	=		
XA302	TDD	KP	30A	(57)	01		PXR51B 64 30A	
XA302	TDD	KQ	29B	QXXDSQ	00	=		INPUT STATUS CONTROL FZF
XA302	TDD	KQ	28B	(55)	01		QXIRIA 53 28B	
XA209	TQ2	D2	21A	QXXDSO	00	=		EOB_COUNTER = 0
XA209	TQ2	D2	22A	(46)	01		QXXDSA SPI0102 48 22A 50 23A	
				QXXDOI	00	=		
XA301	TDD	KI	29A	( )	01		QXXDIP 62 29A	
				QXXDON	00	=		
XA301	TDD	KN	28A	( )	01		QXXC3P 60 28A	
XA301	TDD	KP	30B	QXXDOP	00	=		
XA301	TDD	KP	30A	(57)	01		QXXCRO 64 30A	
XA301	TDD	KQ	29B	QXXDOQ	00	=		INPUT BYTE COUNTER BIT 0
XA301	TDD	KQ	28B	(55)	01		SPI0023 53 28B	
				QXXD1I	00	=		
XA303	TDD	KI	29A	( )	01		QXXDOQ 62 29A	
				QXXD1N	00	=		
XA303	TDD	KN	28A	( )	01		QXXC3P 60 28A	
XA303	TDD	KP	30B	QXXD1P	00	=		
XA303	TDD	KP	30A	(57)	01		QXXCRO 64 30A	
XA303	TDD	KQ	29B	QXXD1Q	00	=		INPUT BYTE COUNTER BIT 1
XA303	TDD	KQ	28B	(55)	01		QXXD1A 53 28B	
XA310	TQ2	F3	35A	QXXRCA	00	=		
XA310	TQ2	F3	34B	(69)	01		QXXRCO QXXREP 65 34B 74 35B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESIGN RATION	FACTOR	COMMENT
			AND	OR					
XA302	TDD	LI	38B		QXXRCI ( )	00 01	=	QXGN4A 77 38B	
XA302	TDD	LN	39B		QXXRCN ( )	00 01	=	QXXR2P 79 39B	
XA302	TDD	LP	37A		QXXRCP (76 )	00 01	=	PXRS1B 75 37B	
XA302	TDD	LQ	38A		QXXRCQ (78 )	00 01	=	QXXRCA 80 39A	REQUEST CONTROL E/F
XA308	TSB	E1	31B		QXXRCO (59 )	00 01	=	QLPT1A PQ08NA PQ02NA PQ060A Q0480A Q010NA SPT0023 SPT0063 55 29B 60 28A 61 32B 62 29A 64 30A 66 31A 68 32A 70 33A	REQUEST INPUTS
XA303	TDD	LI	38B		QXXREI ( )	00 01	=	QXGN4A 77 38B	
XA303	TDD	LN	39B		QXXREN ( )	00 01	=	QXEAOA 79 39B	
XA303	TDD	LP	37A		QXXREP (76 )	00 01	=	PXRS1B 75 37B	
XA303	TDD	LQ	38A		QXXREQ (78 )	00 01	=	QXXROP 80 39A	REQUEST ENABLE E/F
XA304	TDD	LI	38B		QXXROI ( )	00 01	=	QXXR2P 77 38B	
XA304	TDD	LN	39B		QXXRON ( )	00 01	=	Q16MH0 79 39B	
XA304	TDD	LP	37A		QXXROP (76 )	00 01	=	QXXRCQ 75 37B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA304	TDD	LQ	38A	QXXR0Q	00	=		REQUEST STROBE COUNTER BIT 0
XA304	TDD	LQ	39A	(78)	01		SPI0043 80 39A	
				QXXR1I	00	=		
XA303	TDD	MI	36A	( )	01		QXXR0Q 71 36A	
				QXXR1N	00	=		
XA303	TDD	MN	34A	( )	01		Q16MH0 72 34A	
XA303	TDD	MP	35A	QXXR1P	00	=		
XA303	TDD	MP	36B	(69)	01		QXXRCQ 73 36B	
XA303	TDD	MQ	35B	QXXR1Q	00	=		REQUEST STROBE COUNTER BIT 1
XA303	TDD	MQ	34B	(74)	01		SPI0043 65 34B	
				QXXR2I	00	=		
XA301	TDD	MI	36A	( )	01		QXXR1Q 71 36A	
				QXXR2N	00	=		
XA301	TDD	MN	34A	( )	01		Q16MH0 72 34A	
XA301	TDD	MP	35A	QXXR2P	00	=		
XA301	TDD	MP	36B	(69)	01		QXXRCQ 73 36B	
XA301	TDD	MQ	35B	QXXR2Q	00	=		REQUEST STROBE COUNTER BIT 2
XA301	TDD	MQ	34B	(74)	01		SPI0043 65 34B	
XA309	TQ2	F4	39A	QXX04A	00	=		
XA309	TQ2	F4	37A	(80)	01		QXXB00 QXXA40 76 37A 78 38A	
XA311	TD4	D2	24B	QXX05A	00	=		
XA311	TD4	D2	23B	(45)	01		QXXB0P QXXB2P QXXA0P QXXA1Q 43 23B 46 21A 48 22A 50 23A	
XA207	TQ2	F4	39A	QXX050	00	=		I/O BYTE COUNT=0, STATE=5
XA207	TQ2	F4	37A	(80)	01		QXX05A SPI0033 76 37A 78 38A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIG-NATOR	FACTOR	COMMENT
XA203	TT3	D1	23A	QX0DEA	00	=		DEF RECEIVE DATA PARITY ERROR
XA203	TT3	D1	24A	(50)	01		QX0FRS QXROPPR QXXA50 52 24A 54 25A 56 26A	
XA208	TQ2	F4	39A	QX0DRA	00	=		DEF RESET STROBE
XA208	TQ2	F4	37A	(80)	01		QX0FRS QXXB10 76 37A 78 38A	
XA203	TT3	D2	23B	QX0D0A	00	=		DEF RECEIVE DATA STROBE BYTE 0
XA203	TT3	D2	22B	(43)	01		QX0FRS QXXB20 QXXA50 41 22B 46 21A 48 22A	
XA203	TT3	D3	27B	QX0D1A	00	=		DEF RECEIVE DATA STROBE BYTE 1
XA203	TT3	D3	24B	(51)	01		QX0FRS QXXB30 QXXA50 45 24B 47 25B 49 26B	
XA203	TT3	E1	30A	QX0D2A	00	=		DEF RECEIVE DATA STROBE BYTE 2
XA203	TT3	E1	31A	(64)	01		QX0FRS QXXB40 QXXA50 66 31A 68 32A 70 33A	
XA203	TT3	E2	29B	QX0D3A	00	=		DEF RECEIVE DATA STROBE BYTE 3
XA203	TT3	E2	28B	(55)	01		QX0FRS QXXB50 QXXA50 53 28B 60 28A 62 29A	
XA205	TT3	F3	39A	QX0FRR	00	=		
XA205	TT3	F3	35A	(80)	01		QX0FRS QXXB0A PXR52B 69 35A 76 37A 78 38A	
XA206	TQ2	F4	39A	QX0FRS	00	=		DEF COMMAND STORE E/F
XA206	TQ2	F4	37A	(80)	01		QX0FRR QX0R0A 76 37A 78 38A	
XA208	TQ2	F3	35A	QX0FRO	00	=		
XA208	TQ2	F3	34B	(69)	01		QXR098T SPI0033 65 34B 74 35B	
XA202	TQ2	C1	18A	QX0NLO	00	=		
XA202	TQ2	C1	19A	(38)	01		QXB5LA SPI0013 40 19A 42 20A	
XA311	TD4	E1	31B	QX0R0A	00	=		
XA311	TD4	E1	32B	(59)	01		QXCA10 QXDEVA QX0FRO Q0FRNO 61 32B 66 31A 68 32A 70 33A	
XA324	TS8	D1	25B	QX111A	00	=		
XA324	TS8	D1	23B	(47)	01		QXXA0Q QXXA3Q QXDEVS QXXB20 PS1110 QXROPA SPI0253 SPI0263 43 23B 46 21A 48 22A 49 26B 50 23A 52 24A 54 25A 56 26A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESIGNATION	FACTOR	COMMENT
			AND	OR					
XA307	TQ2	A4	07B		QX1110	00 =			
XA307	TQ2	A4	05B		(15)	01	QX111A SPI0043 11 05B 13 06B		
XA208	TQ2	C4	19B		QOFRNA	00 =			ENABLE OFR FROM CPU-B
XA208	TQ2	C4	17B		(39)	01	PS1110 QONL20 35 17B 37 18B		
XA209	TQ2	C3	16B		QOFRNO	00 =			
XA209	TQ2	C3	14B		(33)	01	QOFRNA SPI0033 29 14B 31 15B		
XA319	TD4	A2	04B		QOFRTA	00 =			OFR FR. CPU-B
XA319	TD4	A2	02B		(09)	01	QONL20 QXOFRS SPI0253 PST010 01 02B 04 04A 05 03B 07 03A		
XA205	TT3	F2	35B		QOFINA	00 =			INTERRUPT CPU-A. RECEIVER
XA205	TT3	F2	34B		(74)	01	QONLNQ PXXDYS PXXR1Q 65 34B 71 36A 72 34A		
XA311	TD4	E2	30B		QOLPEA	00 =			DEVICE ERROR, CPU-B SENDER
XA311	TD4	E2	29B		(57)	01	PDER90 PSTR80 QONLNQ PST390 55 29B 60 28A 62 29A 64 30A		
XA207	TQ2	D2	21A		QONCKA	00 =			
XA207	TQ2	D2	22A		(46)	01	QONCK0 SPI0033 48 22A 50 23A		
XA206	TQ2	D4	27B		QONCK0	00 =			SUM. CONTROL CLOCKS. CPU-B
XA206	TQ2	D4	25B		(51)	01	QC01CA Q112EA 47 25B 49 26B		
XA321	TDD	BI	03B		QONLNI	00 =			
XA321	TDD	BI	03B		( )	01	PST11A 05 03B		
XA321	TDD	BN	02B		QONLNN	00 =			
XA321	TDD	BN	02B		( )	01	QONCKA 01 02B		
XA321	TDD	BP	04B		QONLNP	00 =			
XA321	TDD	BP	04A		(09)	01	PXRS2B 04 04A		
XA321	TDD	BQ	03A		QONLNQ	00 =			
XA321	TDD	BQ	02A		(07)	01	SPI0053 03 02A		

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA201	TQ2	E3	30B	QONL10	00	=		
XA201	TQ2	E3	28B	(57)	01		QONLNP SPI0013 53 28B 55 29B	CPU-B, SENDER CONTROL, UNLOAD
XA201	TQ2	E4	33B	QONL20	00	=		
XA201	TQ2	E4	31B	(63)	01		QONLNP SPI0013 59 31B 61 32B	
XA207	TQ2	C4	19B	QONSTA	00	=		
XA207	TQ2	C4	17B	(39)	01		QXIR10 QONLNQ 35 17B 37 18B	CPU-B SYNC PULSE
XA207	TQ2	F2	34A	Q1PNDO	00	=		
XA207	TQ2	F2	36A	(72)	01		QXXDIR QXXDIP 71 36A 73 36B	INTERRUPT PENDING, CPU-B
XA205	TT3	E1	30A	Q11ZEA	00	=		
XA205	TT3	E1	31A	(64)	01		QREL90 QSTRB0 QONLNQ 66 31A 68 32A 70 33A	RELEASE DEV STROBE, CPU-B
XA202	TQ2	B3	10B	Q16MH0	00	=		
XA202	TQ2	B3	08B	(21)	01		P16MHA SPI0013 17 08B 19 09B	
XA202	TQ2	B4	13B	Q16MI0	00	=		
XA202	TQ2	B4	11B	(27)	01		P16MHA SPI0013 23 11B 25 12B	
XA304	TDD	MI	36A	Q20DLI	00	=		
XA304	TDD	MI		( )	01		SPI0023 71 36A	CPU-B
XA304	TDD	MN	34A	Q20DLN	00	=		
XA304	TDD	MN		( )	01		QDLQ6P 72 34A	
XA304	TDD	MP	35A	Q20DLP	00	=		
XA304	TDD	MP	36B	(69)	01		QDLCA 73 36B	
XA304	TDD	MQ	35B	Q20DLQ	00	=		
XA304	TDD	MQ	34B	(74)	01		SPI0053 65 34B	
XA143	DCF	C1	25B	TACMAB	00	=		
XA143	DCF	C1		( )	01		KXACMDX 46 25B	
XA243	DCF	C1	25B	( )	02	+	JXACMDX 46 25B	
XA345	DCF	A1	02B	( )	03	+	PXACMDX 07 02B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA446	DCF	C5	31B	( )	04	+	TXACMDX 60 31B	
XA142	DCF	C1	25B	TACMBB ( )	00	=	KXBCMDX	
XA242	DCF	C1	25B	( )	02	+	46 25B JXBCMDX	
XA346	DCF	A1	02B	( )	03	+	46 25B QXBCMDX	
XA445	DCF	C5	31B	( )	04	+	07 02B TXBCMDX 60 31B	
XA511	TQ2	B2	09A	TADSAA	00	=		
XA511	TQ2	B2	10A	(14 )	01		TADSAOX SPI020 18 10A 20 11A	
XA545	DCF	C3	30B	TADSADX4	00	=		FILE PROTECT 2 RECEIVER
XA545	DCF	C3	29A	(55 )	01		SPI029 52 29A	
XA545	DCF	C4	29B	TADSAOX	00	=		
XA545	DCF	C4	28B	(56 )	01		SPI030 51 28B	
XA545	DCF	D3	37B	TADSBDX4	00	=		REWINDING 2 RECEIVER
XA545	DCF	D3	36A	(78 )	01		SPI029 72 36A	
XA545	DCF	D4	36B	TADSBOX	00	=		
XA545	DCF	D4	35B	(75 )	01		SPI030 73 35B	
XA545	DCF	A3	07B	TADSCDX4	00	=		BOT 2 RECEIVER
XA545	DCF	A3	05A	(17 )	01		SPI029 06 05A	
XA545	DCF	A4	06B	TADSCOX	00	=		
XA545	DCF	A4	05B	(15 )	01		SPI030 13 05B	
XA545	DCF	B3	15B	TADSDDX4	00	=		EOT 2 RECEIVER
XA545	DCF	B3	13A	(37 )	01		SPI029 36 13A	
XA545	DCF	B4	14B	TADSDDX	00	=		
XA545	DCF	B4	13B	(35 )	01		SPI030 33 13B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	REG. FACTOR	FACTOR	COMMENT
			AND	OR					
XA421	MUX	C1	17A		TADSW1X	00			
XA421	MUX	C1	16B		(36)	01		TADSAA TADS00 TADS10 SPI008 TADSA0X SPI007 SPI006 SPI003 35 16B 37 17B 39 18B 41 19B 43 22B 45 23B 47 23A 50 24A	BOT MULTIPLEXER
XA421	MUX	C2	18A		TADSW2X	00	=		
XA421	MUX	C2	21A		(38)	01		TADSBOX TADSCOX TADSDOX TXGN6A 46 21A 42 20A 40 19A 48 22A	
XA422	MUX	C1	17A		TADSX1X	00	=		
XA422	MUX	C1	16B		(36)	01		TEOT10X TADSDOX UEOT30X SPI003 UEOT40X SPI006 SPI007 35 16B 37 17B 39 18B 41 19B 43 22B 45 23B 47 23A	EQT MULTIPLEXER
XA422	MUX	C2	18A		TADSX2X	00	=		
XA422	MUX	C2	21A		(38)	01		TTS2BS TTS3BS TTS4BS TXGN6A 46 21A 42 20A 40 19A 48 22A	
XA517	TQ2	D1	24A		TAOS00	00	=		
XA517	TQ2	D1	25A		(52)	01		TADSAA SPI021 54 25A 56 26A	
XA543	TLD	E1	31A		TADS1D4	00	=		
XA543	TLD	E1	32A		(66)	01		TTS1BR SPI029 68 32A 70 33A	MIT 1 ADDRESS SELECT
XA517	TQ2	D2	21A		TADS10	00	=		
XA517	TQ2	D2	22A		(46)	01		TADSAA SPI021 48 22A 50 23A	
XA543	TLD	E2	28A		TADS2D4	00	=		
XA543	TLD	E2	29A		(60)	01		TTS2BR SPI029 62 29A 64 30A	
XA543	TLD	E3	30B		TADS3D4	00	=		
XA543	TLD	E3	28B		(57)	01		TTS3BR SPI029 53 28B 55 29B	
XA543	TLD	E4	33B		TADS4D4	00	=		
XA543	TLD	E4	31B		(63)	01		TTS4BR SPI029 59 31B 61 32B	MIT 4 ADDRESS SELECT
					TAENAB	00	=		
XA143	DCF	C3	30B		( )	01		KXAENDX 55 30B	
XA243	DCF	C3	30B		( )	02	+	JXAENDX 55 30B	
XA345	DCF	A3	07B		( )	03	+	PXAENDX 17 07B	
XA446	DCF	C7	25A		( )	04	+	TXAENDX 43 25A	



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
				TAENBB	00	=		
XA142	DCF	C3	30B	( )	01		KXBENDX 55 30B	
XA242	DCF	C3	30B	( )	02	+	JXBENDX 55 30B	
XA346	DCF	A3	07B	( )	03	+	QXBENDX 17 07B	
XA445	DCF	C7	25A	( )	04	+	TXBENDX 43 25A	
				TAINAB	00	=		
XA143	DCF	C5	31B	( )	01		KXAINDX 60 31B	
XA243	DCF	C5	31B	( )	02	+	JXAINDX 60 31B	
XA345	DCF	A5	08B	( )	03	+	PXAINDX 14 08B	
XA446	DCF	C3	30B	( )	04	+	TXAINDX 55 30B	
				TAINBB	00	=		
XA142	DCF	C5	31B	( )	01		KXBINDX 60 31B	
XA242	DCF	C5	31B	( )	02	+	JXBINDX 60 31B	
XA346	DCF	A5	08B	( )	03	+	QXBINDX 14 08B	
XA445	DCF	C3	30B	( )	04	+	TXBINDX 55 30B	
				TAOPAB	00	=		
XA143	DCF	C7	25A	( )	01		KXAPCDX 43 25A	
XA243	DCF	C7	25A	( )	02	+	JXAPCDX 43 25A	
XA345	DCF	A7	02A	( )	03	+	PXAPCDX 01 02A	
XA446	DCF	C1	25B	( )	04	+	TXAPCDX 46 25B	
				TAOPBB	00	=		
XA142	DCF	C7	25A	( )	01		KXBPCDX 43 25A	
XA242	DCF	C7	25A	( )	02	+	JXBPCDX 43 25A	
XA346	DCF	A7	02A	( )	03	+	QXBPCDX 01 02A	
XA445	DCF	C1	25B	( )	04	+	TXBPCDX 46 25B	

CONNECTOR	CIRCUIT GROUP	GROUP	TEST POINTS AND OR	EQUATION	TERMS	DESIG- NATOR	FACTOR	COMMENT
				TA00AB	00 =			
XA143	DCF	A1	02B	( )	01		KXA0CDX 07 02B	
XA243	DCF	A1	02B	( )	02 +		JXA0CDX 07 02B	
XA345	DCF	C1	25B	( )	03 +		PXA0CDX 46 25B	
XA446	DCF	A1	02B	( )	04 +		TXA0CDX 07 02B	
				TA00BB	00 =			
XA142	DCF	A1	02B	( )	01		KXB0CDX 07 02B	
XA242	DCF	A1	02B	( )	02 +		JXB0CDX 07 02B	
XA346	DCF	C1	25B	( )	03 +		QXB0CDX 46 25B	
XA445	DCF	A1	02B	( )	04 +		TXB0CDX 07 02B	
				TA01AB	00 =			
XA143	DCF	A3	07B	( )	01		KXA1CDX 17 07B	
XA243	DCF	A3	07B	( )	02 +		JXA1CDX 17 07B	
XA345	DCF	C3	30B	( )	03 +		PXA1CDX 55 30B	
XA446	DCF	A3	07B	( )	04 +		TXA1CDX 17 07B	
				TA01BB	00 =			
XA142	DCF	A3	07B	( )	01		KXB1CDX 17 07B	
XA242	DCF	A3	07B	( )	02 +		JXB1CDX 17 07B	
XA346	DCF	C3	30B	( )	03 +		QXB1CDX 55 30B	
XA445	DCF	A3	07B	( )	04 +		TXB1CDX 17 07B	
				TA02AB	00 =			
XA143	DCF	A5	08B	( )	01		KXA2CDX 14 08B	
XA243	DCF	A5	08B	( )	02 +		JXA2CDX 14 08B	
XA345	DCF	C5	31B	( )	03 +		PXA2CDX 60 31B	
XA446	DCF	A5	08B	( )	04 +		TXA2CDX 14 08B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
			AND	OR					
					TA02BB	00	=		
XA142	DCF	A5	08B	( )	( )	01	=	KXB2CDX 14 08B	
XA242	DCF	A5	08B	( )	( )	02	+	JXB2CDX 14 08B	
XA346	DCF	C5	31B	( )	( )	03	+	QXB2CDX 60 31B	
XA445	DCF	A5	08B	( )	( )	04	+	TXB2CDX 14 08B	
					TA03AB	00	=		
XA143	DCF	A7	02A	( )	( )	01	=	KXA3CDX 01 02A	
XA243	DCF	A7	02A	( )	( )	02	+	JXA3CDX 01 02A	
XA345	DCF	C7	25A	( )	( )	03	+	PXA3CDX 43 25A	
XA446	DCF	A7	02A	( )	( )	04	+	TXA3CDX 01 02A	
					TA03BB	00	=		
XA142	DCF	A7	02A	( )	( )	01	=	KXB3CDX 01 02A	
XA242	DCF	A7	02A	( )	( )	02	+	JXB3CDX 01 02A	
XA346	DCF	C7	25A	( )	( )	03	+	QXB3CDX 43 25A	
XA445	DCF	A7	02A	( )	( )	04	+	TXB3CDX 01 02A	
					TA04AB	00	=		
XA143	DCF	B1	10B	( )	( )	01	=	KXA4CDX 27 10B	
XA243	DCF	B1	10B	( )	( )	02	+	JXA4CDX 27 10B	
XA345	DCF	D1	32B	( )	( )	03	+	PXA4CDX 65 32B	
XA446	DCF	B1	10B	( )	( )	04	+	TXA4CDX 27 10B	
					TA04BB	00	=		
XA142	DCF	B1	10B	( )	( )	01	=	KXB4CDX 27 10B	
XA242	DCF	B1	10B	( )	( )	02	+	JXB4CDX 27 10B	
XA346	DCF	D1	32B	( )	( )	03	+	QXB4CDX 65 32B	
XA445	DCF	B1	10B	( )	( )	04	+	TXB4CDX 27 10B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
				TA05AB	00	=		
XA143	DCF	B3	15B	( )	01		KXA5CDX 37 15B	
XA243	DCF	B3	15B	( )	02	+	JXA5CDX 37 15B	
XA345	DCF	D3	37B	( )	03	+	PXA5CDX 78 37B	
XA446	DCF	B3	15B	( )	04	+	TXA5CDX 37 15B	
				TA05BB	00	=		
XA142	DCF	B3	15B	( )	01		KXB5CDX 37 15B	
XA242	DCF	B3	15B	( )	02	+	JXB5CDX 37 15B	
XA346	DCF	D3	37B	( )	03	+	QXB5CDX 78 37B	
XA445	DCF	B3	15B	( )	04	+	TXB5CDX 37 15B	
				TA06AB	00	=		
XA143	DCF	B5	16A	( )	01		KXA6CDX 41 16A	
XA243	DCF	B5	16A	( )	02	+	JXA6CDX 41 16A	
XA345	DCF	D5	38B	( )	03	+	PXA6CDX 80 38B	
XA446	DCF	B5	16A	( )	04	+	TXA6CDX 41 16A	
				TA06BB	00	=		
XA142	DCF	B5	16A	( )	01		KXB6CDX 41 16A	
XA242	DCF	B5	16A	( )	02	+	JXB6CDX 41 16A	
XA346	DCF	D5	38B	( )	03	+	QXB6CDX 80 38B	
XA445	DCF	B5	16A	( )	04	+	TXB6CDX 41 16A	
				TA07AB	00	=		
XA143	DCF	B7	10A	( )	01		KXA7CDX 23 10A	
XA243	DCF	B7	10A	( )	02	+	JXA7CDX 23 10A	
XA345	DCF	D7	33A	( )	03	+	PXA7CDX 61 33A	
XA446	DCF	B7	10A	( )	04	+	TXA7CDX 23 10A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIG- NATOR	FACTOR	COMMENT
				TA07BB	00	=		
XA142	DCF	B7	10A	( )	01		KXB7CDX 23 10A	
XA242	DCF	B7	10A	( )	02	+	JXB7CDX 23 10A	
XA346	DCF	D7	33A	( )	03	+	OXB7CDX 61 33A	
XA445	DCF	B7	10A	( )	04	+	TXB7CDX 23 10A	
XA519	TQ2	C1	18A	TBCP00	00	=		BUFFER REG CLOCK BITS 0 TO 3
XA519	TQ2	C1	19A	(38 )	01		TRWCOA TLPT1A 40 19A 42 20A	
XA519	TQ2	C2	15A	TBCP10	00	=		
XA519	TQ2	C2	16A	(30 )	01		TRWCOA TLPT1A 34 16A 36 17A	
XA519	TQ2	C3	16B	TBCP20	00	=		
XA519	TQ2	C3	14B	(33 )	01		TRWCOA TLPT1A 29 14B 31 15B	
XA519	TQ2	C4	19B	TBCP30	00	=		
XA519	TQ2	C4	17B	(39 )	01		TRWCOA TLPT1A 35 17B 37 18B	
XA519	TQ2	D1	24A	TBCP40	00	=		
XA519	TQ2	D1	25A	(52 )	01		TRWCOA TLPT1A 54 25A 56 26A	
XA519	TQ2	D2	21A	TBCP50	00	=		
XA519	TQ2	D2	22A	(46 )	01		TRWCOA TLPT1A 48 22A 50 23A	
XA519	TQ2	D3	24B	TBCP60	00	=		
XA519	TQ2	D3	22B	(45 )	01		TRWCOA TLPT1A 41 22B 43 23B	
XA519	TQ2	D4	27B	TBCP70	00	=		BUFFER REG CLOCK BITS 28 TO 31
XA519	TQ2	D4	25B	(51 )	01		TRWCOA TLPT1A 47 25B 49 26B	
XA502	TS8	B1	11B	TBSY0A	00	=		NEW COMMAND BUSY INHIBIT
XA502	TS8	B1	09A	(23 )	01		TSNC1S TRDY10X TADSW2X TBSY2A TFPE2A TCILKO TSCK1B SPI018 14 09A 18 10A 19 09B 20 11A 22 12A 24 13A 25 12B 26 14A	
XA505	TQ2	A2	02B	TBSY2A	00	=		
XA505	TQ2	A2	04A	(01 )	01		YDIRSP TBOTOS 04 04A 05 03B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	RELATION	FACTOR	COMMENT
XA523	TQ2	C2	15A	TBUSYA	00	=		
XA523	TQ2	C2	16A	(30)	01		TBUSYO SPI022 34 16A 36 17A	
XA410	TD4	C1	17B	TBUSYR	00	=		
XA410	TD4	C1	18B	(35)	01		TBUSYS TINT1A TINT5A TXRS1B 37 18B 38 18A 40 19A 42 20A	
XA412	TQ2	C4	19B	TBUSYS	00	=		TAPE MOTION BUSY F/F
XA412	TQ2	C4	17B	(39)	01		TBUSYR TBSY0A 35 17B 37 18B	
XA522	TQ2	C2	15A	TBUSYO	00	=		
XA522	TQ2	C2	16A	(30)	01		TBUSYR TCSDOR 34 16A 36 17A	
XA523	TQ2	D3	24B	TBOTCA	00	=		
XA523	TQ2	D3	22B	(45)	01		TBOT10X SPI022 41 22B 43 23B	
XA523	TQ2	C1	18A	TBOT0A	00	=		
XA523	TQ2	C1	19A	(38)	01		TBOT10X TSCL3B 40 19A 42 20A	
XA523	TQ2	C3	16B	TBOT0R	00	=		
XA523	TQ2	C3	14B	(33)	01		TBOT0S TBOT1A 29 14B 31 15B	
XA522	TQ2	C3	16B	TBOT0S	00	=		BEGIN OF TAPE (BOT) F/F
XA522	TQ2	C3	14B	(33)	01		TBOT0R TBOT0A 29 14B 31 15B	
XA525	TD4	C1	17B	TBOT1A	00	=		
XA525	TD4	C1	18B	(35)	01		TBOTCA TSTPOR TBOT1R TSCL3B 37 18B 38 18A 40 19A 42 20A	
XA545	DCF	A1	02B	TBOT1DX4	00	=		BOT 1 RECEIVER
XA545	DCF	A1	05A	(07)	01		SPI029 06 05A	
XA524	TT3	D1	23A	TBOT1R	00	=		
XA524	TT3	D1	24A	(50)	01		TBOT1S TBOT3A TBOT0S 52 24A 54 25A 56 26A	
XA522	TQ2	D1	24A	TBOT1S	00	=		BOT COUNTER BIT 1
XA522	TQ2	D1	25A	(52)	01		TBOT1R TBOT2A 54 25A 56 26A	

CONNECTOR	CONC. TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TRN	DESIGNATION	FACTOR	COMMENT
XA545	DCF	A2	03B	TBOT10X	00 =			
XA545	DCF	A2	04B	(09)	01	SPI028 11 04B		
XA524	TT3	C1	17A	TBOT2A	00 =			
XA524	TT3	C1	18A	(36)	01	TBOT0S TBOT2R TSCL18 38 18A 40 19A 42 20A		
XA523	TQ2	C4	19B	TBOT2R	00 =			
XA523	TQ2	C4	17B	(39)	01	TBOT2S TBOT0S 35 17B 37 18B		
XA522	TQ2	C4	19B	TBOT2S	00 =			BOT COUNTER BIT 2
XA522	TQ2	C4	17B	(39)	01	TBOT2R TBOT4A 35 17B 37 18B		
XA523	TQ2	D2	21A	TBOT3A	00 =			BUFFER REG BIT 8
XA523	TQ2	D2	22A	(46)	01	TBOT2S TSCL18 48 22A 50 23A		
XA523	TQ2	D1	24A	TBOT4A	00 =			
XA523	TQ2	D1	25A	(52)	01	TBOT1S TSCL3B 54 25A 56 26A		
				TB00B1	00 =			
XA533	TDD	E1	19A	( )	01	T000BQ 40 19A		
				TB00BN	00 =			
XA533	TDD	EN	20A	( )	01	TBCP00 42 20A		
XA533	TDD	EP	17B	TB00BP	00 =			
XA533	TDD	EP	18A	(35)	01	SPI025 38 18A		
XA533	TDD	EQ	18B	TB00BQ	00 =			BUFFER REG BIT 0
XA533	TDD	EQ	19B	(37)	01	SPI024 39 19B		
				TB01B1	00 =			
XA533	TDD	F1	16A	( )	01	T501B0 34 16A		
				TB01BN	00 =			
XA533	TDD	FN	15A	( )	01	TBCP00 30 15A		

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA533	TDD	FP	16B	TB01BP	00	=		
XA533	TDD	FP	17A	(33)	01		SPI023 36 17A	
XA533	TDD	FQ	15B	TB01BQ	00	=		
XA533	TDD	FQ	14B	(31)	01		SPI008 29 14B	
XA533	TDD	GI	25A	TB02BI	00	=		
				( )	01		TD02BQ 54 25A	
XA533	TDD	GN	26A	TB02BN	00	=		
				( )	01		TBCP00 56 26A	
XA533	TDD	GP	25B	TB02BP	00	=		
XA533	TDD	GP	24A	(47)	01		SPI025 52 24A	
XA533	TDD	GQ	26B	TB02BQ	00	=		
XA533	TDD	GQ	27B	(49)	01		SPI024 51 27B	
XA533	TDD	HI	22A	TB03BI	00	=		
				( )	01		TD03BQ 48 22A	
XA533	TDD	HN	21A	TB03BN	00	=		
				( )	01		TBCP00 46 21A	
XA533	TDD	HP	24B	TB03BP	00	=		
XA533	TDD	HP	23A	(45)	01		SPI023 50 23A	
XA533	TDD	HQ	23B	TB03BQ	00	=		
XA533	TDD	HQ	22B	(43)	01		SPI008 41 22B	
XA533	TDD	JI	32A	TB04BI	00	=		
				( )	01		TD04BQ 68 32A	
XA533	TDD	JN	33A	TB04BN	00	=		
				( )	01		TBCP10 70 33A	



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA533	TDD	JP	31B	TB04BP	00	=		
XA533	TDD	JP	31A	(59)	01		SPI025 66 31A	
XA533	TDD	JQ	32B	TB04BQ	00	=		
XA533	TDD	JQ	33B	(61)	01		SPI024 63 33B	
XA533	TDD	KI	29A	TB05BI	00	=		
				( )	01		TD05BQ 62 29A	
XA533	TDD	KN	28A	TB05BN	00	=		
				( )	01		TBCP10 60 28A	
XA533	TDD	KP	30B	TB05BP	00	=		
XA533	TDD	KP	30A	(57)	01		SPI023 64 30A	
XA533	TDD	KQ	29B	TB05BQ	00	=		
XA533	TDD	KQ	28B	(55)	01		SPI008 53 28B	
XA533	TDD	LI	38B	TB06BI	00	=		
				( )	01		TD06BQ 77 38B	
XA533	TDD	LN	39B	TB06BN	00	=		
				( )	01		TBCP10 79 39B	
XA533	TDD	LP	37A	TB06BP	00	=		
XA533	TDD	LP	37B	(76)	01		SPI025 75 37B	
XA533	TDD	LQ	38A	TB06BQ	00	=		
XA533	TDD	LQ	39A	(78)	01		SPI024 80 39A	
XA533	TDD	MI	36A	TB07BI	00	=		
				( )	01		TD07BQ 71 36A	
XA533	TDD	MN	34A	TB07BN	00	=		
				( )	01		TBCP10 72 34A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TER. V	DESIGNATOR	FACTOR	COMMENT
			AND	OR					
XA533	TDD	MP	35A		TB07BP	00 =			
XA533	TDD	MP	36B		(69)	01	SPI023 73 36B		
XA533	TDD	MQ	35B		TB07BQ	00 =			BUFFER REG BIT 7
XA533	TDD	MQ	34B		(74)	01	SPI008 65 34B		
XA534	TDD	AI	06A		TB08BI	00 =			
					( )	01	TD08BQ 08 06A		
XA534	TDD	AN	07A		TB08BN	00 =			
					( )	01	TBCP20 10 07A		
XA534	TDD	AP	05B		TB08BP	00 =			
XA534	TDD	AP	05A		(11)	01	SPI025 06 05A		
XA534	TDD	AQ	06B		TB08BQ	00 =			
XA534	TDD	AQ	07B		(13)	01	SPI024 15 07B		
XA534	TDD	BI	03B		TB09BI	00 =			
					( )	01	TD09BQ 05 03B		
XA534	TDD	BN	02B		TB09BN	00 =			
					( )	01	TBCP20 01 02B		
XA534	TDD	BP	04B		TB09BP	00 =			
XA534	TDD	BP	04A		(09)	01	SPI023 04 04A		
XA534	TDD	BQ	03A		TB09BQ	00 =			
XA534	TDD	BQ	02A		(07)	01	SPI008 03 02A		
XA534	TDD	CI	13A		TB10BI	00 =			
					( )	01	TD10BQ 24 13A		
XA534	TDD	CN	14A		TB10BN	00 =			
					( )	01	TBCP20 26 14A		

CONNECTOR	UNIT	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA534	TDD	CP	11B	TB10BP	00	=		
XA534	TDD	CP	12A	(23)	01		SPI025 22 12A	
XA534	TDD	CQ	12B	TB10BQ	00	=		
XA534	TDD	CQ	13B	(25)	01		SPI024 27 13B	
XA534	TDD	DI	10A	TB11BI	00	=		
				( )	01		TD11BQ 18 10A	
XA534	TDD	UN	09A	TB11BN	00	=		
				( )	01		TBCP20 14 09A	
XA534	TDD	DP	10B	TB11BP	00	=		
XA534	TDD	DP	11A	(21)	01		SPI023 20 11A	
XA534	TDD	DQ	09B	TB11BQ	00	=		
XA534	TDD	DQ	08B	(19)	01		SPI008 17 08B	
XA534	TDD	EI	19A	TB12BI	00	=		
				( )	01		TD12BQ 40 19A	
XA534	TDD	EN	20A	TB12BN	00	=		
				( )	01		TBCP30 42 20A	
XA534	TDD	EP	17B	TB12BP	00	=		
XA534	TDD	EP	18A	(35)	01		SPI025 38 18A	
XA534	TDD	EQ	18B	TB12BQ	00	=		
XA534	TDD	EQ	19B	(37)	01		SPI024 39 19B	
XA534	TDD	FI	16A	TB13BI	00	=		
				( )	01		TD13BQ 34 16A	
XA534	TDD	FN	15A	TB13BN	00	=		
				( )	01		TBCP30 30 15A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA534	TDD	FP	16B	TB13BP	00	=		
XA534	TDD	FP	17A	(33)	01		SPI023 36 17A	
XA534	TDD	FQ	15B	TB13BQ	00	=		
XA534	TDD	FQ	14B	(31)	01		SPT008 29 14B	
XA534	TDD	GI	25A	TB14BI	00	=		
				( )	01		TD14BQ 54 25A	
XA534	TDD	GN	26A	TB14BN	00	=		
				( )	01		TBCP30 56 26A	
XA534	TDD	GP	25B	TB14BP	00	=		
XA534	TDD	GP	24A	(47)	01		SPI025 52 24A	
XA534	TDD	GQ	26B	TB14BQ	00	=		
XA534	TDD	GQ	27B	(49)	01		SPI024 51 27B	
XA534	TDD	HI	22A	TB15BI	00	=		
				( )	01		TD15BQ 48 22A	
XA534	TDD	HN	21A	TB15BN	00	=		
				( )	01		TBCP30 46 21A	
XA534	TDD	HP	24B	TB15BP	00	=		
XA534	TDD	HP	23A	(45)	01		SPI023 50 23A	
XA534	TDD	HQ	23B	TB15BQ	00	=		
XA534	TDD	HQ	22B	(43)	01		SPI008 41 22B	BUFFER REG BIT 15
XA534	TDD	JI	32A	TB16BI	00	=		
				( )	01		TD16BQ 68 32A	
XA534	TDD	JN	33A	TB16BN	00	=		
				( )	01		TBCP40 70 33A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERMS	DESIGNATOR	FACTOR	COMMENT
XA534	TDD	JP	31B	TB16BP	00	=		
XA534	TDD	JP	31A	(59)	01		SPI025 66 31A	
XA534	TDD	JQ	32B	TB16BQ	00	=		BUFFER REG BIT 16
XA534	TDD	JQ	33B	(61)	01		SPI024 63 33B	
XA534	TDD	KI	29A	TB17BI	00	=		
				( )	01		TD17BQ 62 29A	
XA534	TDD	KN	28A	TB17BN	00	=		
				( )	01		TBCP40 60 28A	
XA534	TDD	KP	30B	TB17BP	00	=		
XA534	TDD	KP	30A	(57)	01		SPI023 64 30A	
XA534	TDD	KQ	29B	TB17BQ	00	=		
XA534	TDD	KQ	28B	(55)	01		SPI008 53 28B	
XA534	TDD	LI	38B	TB18BI	00	=		
				( )	01		TD18BQ 77 38B	
XA534	TDD	LN	39B	TB18BN	00	=		
				( )	01		TBCP40 79 39B	
XA534	TDD	LP	37A	TB18BP	00	=		
XA534	TDD	LP	37B	(76)	01		SPI025 75 37B	
XA534	TDD	LQ	38A	TB18BQ	00	=		
XA534	TDD	LQ	39A	(78)	01		SPI024 80 39A	
XA534	TDD	MI	36A	TB19BI	00	=		
				( )	01		TD19BQ 71 36A	
XA534	TDD	MN	34A	TB19BN	00	=		
				( )	01		TBCP40 72 34A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIG-NATOR	FACTOR	COMMENT
XA534	TDD	MP	35A	TB19BP	00	=		
XA534	TDD	MP	36B	(69)	01		SPI023 73 36B	
XA534	TDD	MQ	35B	TB19BQ	00	=		
XA534	TDD	MQ	34B	(74)	01		SPI008 65 34B	
				TB20BI	00	=		
XA535	TDD	AI	06A	( )	01		TD20BQ 08 06A	
				TB20BN	00	=		
XA535	TDD	AN	07A	( )	01		TBCP50 10 07A	
XA535	TDD	AP	05B	TB20BP	00	=		
XA535	TDD	AP	05A	(11)	01		SPI025 06 05A	
XA535	TDD	AQ	06B	TB20BQ	00	=		
XA535	TDD	AQ	07B	(13)	01		SPI024 15 07B	
				TB21BI	00	=		
XA535	TDD	BI	03B	( )	01		TD21BQ 05 03B	
				TB21BN	00	=		
XA535	TDD	BN	02B	( )	01		TBCP50 01 02B	
XA535	TDD	BP	04B	TB21BP	00	=		
XA535	TDD	BP	04A	(09)	01		SPI023 04 04A	
XA535	TDD	BQ	03A	TB21BQ	00	=		
XA535	TDD	BQ	02A	(07)	01		SPI008 03 02A	
				TB22BI	00	=		
XA535	TDD	CI	13A	( )	01		TD22BQ 24 13A	
				TB22BN	00	=		
XA535	TDD	CN	14A	( )	01		TBCP50 26 14A	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TB22BP

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

FILE IDENT

T39CIFC5

DATE 09-03-82

PAGE

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
			AND	OR					
XA535	TDD	CP	11B		TB22BP	00	=		
XA535	TDD	CP	12A		(23)	01		SPI025 22 12A	
XA535	TDD	CQ	12B		TB22BQ	00	=		
XA535	TDD	CQ	13B		(25)	01		SPI024 27 13B	
					TB23BI	00	=		
XA535	TDD	DI	10A		( )	01		T023BQ 18 10A	
					TB23BN	00	=		
XA535	TDD	DN	09A		( )	01		TBCP50 14 09A	
XA535	TDD	DP	10B		TB23BP	00	=		
XA535	TDD	DP	11A		(21)	01		SPI023 20 11A	
XA535	TDD	DQ	09B		TB23BQ	00	=		BUFFER REG BIT 23
XA535	TDD	DQ	08B		(19)	01		SPI008 17 08B	
					TB24BI	00	=		
XA535	TDD	EI	19A		( )	01		T024BQ 40 19A	
					TB24BN	00	=		
XA535	TDD	EN	20A		( )	01		TBCP60 42 20A	
XA535	TDD	EP	17B		TB24BP	00	=		
XA535	TDD	EP	18A		(35)	01		SPI025 38 18A	
XA535	TDD	EQ	18B		TB24BQ	00	=		BUFFER REG BIT 24
XA535	TDD	EQ	19B		(37)	01		SPI024 39 19B	
					TB25BI	00	=		
XA535	TDD	FI	16A		( )	01		T025BQ 34 16A	
					TB25BN	00	=		
XA535	TDD	FN	15A		( )	01		TBCP60 30 15A	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TB25BP

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

FILE IDENT

T39CIFC5

DATE 09-03-82

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATION	FACTOR	COMMENT
XA535	TDD	FP	16B	TB25BP	00	=		
XA535	TDD	FP	17A	(33)	01		SPI023 36 17A	
XA535	TDD	FQ	15B	TB25BQ	00	=		
XA535	TDD	FQ	14B	(31)	01		SPI008 29 14B	
XA535	TDD	GI	25A	TB26BI	00	=		
				( )	01		TD26BQ 54 25A	
XA535	TDD	GN	26A	TB26BN	00	=		
				( )	01		TBCP60 56 26A	
XA535	TDD	GP	25B	TB26BP	00	=		
XA535	TDD	GP	24A	(47)	01		SPI025 52 24A	
XA535	TDD	GQ	26B	TB26BQ	00	=		
XA535	TDD	GQ	27B	(49)	01		SPI024 51 27B	
XA535	TDD	HI	22A	TB27BI	00	=		
				( )	01		TD27BQ 48 22A	
XA535	TDD	HN	21A	TB27BN	00	=		
				( )	01		TBCP60 46 21A	
XA535	TDD	HP	24B	TB27BP	00	=		
XA535	TDD	HP	23A	(45)	01		SPI023 50 23A	
XA535	TDD	HQ	23B	TB27BQ	00	=		
XA535	TDD	HQ	22B	(43)	01		SPI008 41 22B	
XA535	TDD	JI	32A	TB28BI	00	=		
				( )	01		TD28BQ 68 32A	
XA535	TDD	JN	33A	TB28BN	00	=		
				( )	01		TBCP70 70 33A	



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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TB28BP

LOGIC

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UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DISC. DATE	FACTOR	COMMENT
XA535	TDD	JP	31B	TB288P	00	=		
XA535	TDD	JP	31A	(59)	01		SPI025 66 31A	
XA535	TDD	JQ	32B	TB288Q	00	=		
XA535	TDD	JQ	33B	(61)	01		SPI024 63 33B	
XA535	TDD	KI	29A	TB298I	00	=		
				( )	01		TD298Q 62 29A	
XA535	TDD	KN	28A	TB298N	00	=		
				( )	01		TBCP70 60 28A	
XA535	TDD	KP	30B	TB298P	00	=		
XA535	TDD	KP	30A	(57)	01		SPI023 64 30A	
XA535	TDD	KQ	29B	TB298Q	00	=		
XA535	TDD	KQ	28B	(55)	01		SPI008 53 28B	
XA535	TDD	LI	38B	TB308I	00	=		
				( )	01		TD308Q 77 38B	
XA535	TDD	LN	39B	TB308N	00	=		
				( )	01		TBCP70 79 39B	
XA535	TDD	LP	37A	TB308P	00	=		
XA535	TDD	LP	37B	(76)	01		SPI025 75 37B	
XA535	TDD	LQ	38A	TB308Q	00	=		
XA535	TDD	LQ	39A	(78)	01		SPI024 80 39A	
XA535	TDD	MI	36A	TB318I	00	=		
				( )	01		TD318Q 71 36A	
XA535	TDD	MN	34A	TB318N	00	=		
				( )	01		TBCP70 72 34A	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TB31BP

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

FILE IDENT T39C1FC5 DATE 09-03-82 PAGE 248

CONNECTOR	GROUP TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIG- NATOR	FACTOR	COMMENT
XA535	TDD	MP	35A	TB31BP	00	=		
XA535	TDD	MP	36B	(69)	01		SPI023 73 36B	
XA535	TDD	MQ	35B	TB31BQ	00	=		BUFFER REG BIT 31
XA535	TDD	MQ	34B	(74)	01		SPI008 65 34B	
XA411	TQ2	A1	05A	TCCP00	00	=		MAIN TIMING COUNTER CLK 00-04
XA411	TQ2	A1	06A	(06)	01		TXCP3A T009SA 08 06A 10 07A	
XA412	TQ2	A1	05A	TCCP10	00	=		
XA412	TQ2	A1	06A	(06)	01		TC04BP SPI004 08 06A 10 07A	
XA411	TQ2	A2	02B	TCCP20	00	=		
XA411	TQ2	A2	04A	(01)	01		TC14BP SPI001 04 04A 05 03B	
XA412	TQ2	A2	02B	TCCP30	00	=		
XA412	TQ2	A2	04A	(01)	01		TC24BP SPI004 04 04A 05 03B	
XA411	TQ2	A3	04B	TCCP40	00	=		
XA411	TQ2	A3	02A	(09)	01		TC34BP SPI001 03 02A 07 03A	
XA412	TQ2	A3	04B	TCCP50	00	=		
XA412	TQ2	A3	02A	(09)	01		TC44BP SPI004 03 02A 07 03A	
XA411	TQ2	A4	07B	TCCP60	00	=		MAIN TIMING COUNTER CLK 60-64
XA411	TQ2	A4	05B	(15)	01		TC54BP SPI001 11 05B 13 06B	
XA411	TQ2	B1	12A	TCCP70	00	=		WRITE TIMING COUNTER CLK 0-4
XA411	TQ2	B1	13A	(22)	01		TXCP3A SPI001 24 13A 26 14A	
XA412	TQ2	B1	12A	TCCP80	00	=		
XA412	TQ2	B1	13A	(22)	01		TC74BP SPI004 24 13A 26 14A	
XA411	TQ2	B2	09A	TCCP90	00	=		WRITE TIMING COUNTER CLK 90/1
XA411	TQ2	B2	10A	(14)	01		TC84BP SPI001 18 10A 20 11A	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TCILKD4

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

FILE IDENT T39C1FC5 DATE 09-03-82 PAGE

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA444	TLD	C4	19B	TCILKD4	00	=		CABLE INTERLOCK DRIVER
XA444	TLD	C4	17B	(39)	01		SPI012 SPI017 35 17B 37 18B	
XA412	TQ2	D4	27B	TCILK0	00	=		CABLE INTERLOCK RECEIVER
XA412	TQ2	D4	25B	(51)	01		TCILKA4 SPI004 47 25B 49 26B	
XA510	TT3	F1	36B	TCRSCA	00	=		
XA510	TT3	F1	37B	(73)	01		TKA01Q TKA02P TSC13B 75 37B 77 38B 79 39B	
XA412	TQ2	B2	09A	TCRS0A	00	=		TCRS0B BUSS
XA412	TQ2	B2	10A	(14)	01		TCRS00 SPI004 18 10A 20 11A	
XA412	TQ2	B2	09A	TCRS0B	00	=		
XA412	TQ2	B2	09A	( )	01		TCRS0A TCRS1A TCRS2A 14 09A 21 10B 27 13B	
XA418	TS8	A1	05B	TCRS00	00	=		MAIN TIMING COUNTER RESET
XA418	TS8	A1	02B	(11)	01		TSTPRA TSTRRA TFSYRA TLADRA TCRSCA TWRGRA TXRS1B TSTR3A 01 02B 04 04A 05 03B 06 05A 07 03A 08 06A 10 07A 13 06B	
XA412	TQ2	B3	10B	TCRS1A	00	=		TCRS0B BUSS
XA412	TQ2	B3	08B	(21)	01		TCRS00 SPI004 17 08B 19 09B	
XA412	TQ2	B4	13B	TCRS2A	00	=		TCRS0B BUSS
XA412	TQ2	B4	11B	(27)	01		TCRS00 SPI004 23 11B 25 12B	
XA412	TQ2	C1	18A	TCRS7A	00	=		
XA412	TQ2	C1	19A	(38)	01		TCRS70 SPI004 40 19A 42 20A	
XA410	TD4	A1	05B	TCRS70	00	=		WRITE TIMING COUNTER RESET
XA410	TD4	A1	05A	(11)	01		TWRIRA TLRCRA TWRGRA TXRS1B 06 05A 08 06A 10 07A 13 06B	
XA523	TQ2	A1	05A	TCSDRA	00	=		
XA523	TQ2	A1	06A	(06)	01		TCSDRO SPI022 08 06A 10 07A	
XA524	TT3	A1	04A	TCSDRO	00	=		INPUT DELAY COUNTER RESET
XA524	TT3	A1	05A	(04)	01		TCSDOP TTSC00 TRWOCA 06 05A 08 06A 10 07A	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TCSD0A

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERMINAL	DESIGNATOR	FACTOR	COMMENT
XA525	TD4	A2	04B	TCSD0A	00	=		START NEW TAPE MOTION COMMAND
XA525	TD4	A2	02B	(09)	01		TCSD00 TCSD1P TCSD2Q TSCL18 01 02B 04 04A 05 03B 07 03A	
XA417	TDD	LI	38B	TCSD0I	00	=		
XA417	TDD	LI	38B	( )	01		SP1005 77 38B	
XA417	TDD	LN	39B	TCSD0N	00	=		
XA417	TDD	LN	39B	( )	01		TCSD2P 79 39B	
XA417	TDD	LP	37A	TCSD0P	00	=		
XA417	TDD	LP	37B	(76)	01		TSYN1A 75 37B	
XA417	TDD	LQ	38A	TCSD0Q	00	=		INPUT DELAY CONTROL E/E
XA417	TDD	LQ	39A	(78)	01		TXRS2B 80 39A	
XA524	TT3	A2	03A	TCSD0R	00	=		
XA524	TT3	A2	02B	(07)	01		TCSD0S TSNC1A TXRS2B 01 02B 03 02A 05 03B	
XA523	TQ2	A2	02B	TCSD0S	00	=		INPUT DELAY BUSY E/E
XA523	TQ2	A2	04A	(01)	01		TCSD0R TSYN1A 04 04A 05 03B	
XA527	TSB	C1	17B	TCSD00	00	=		
XA527	TSB	C1	15A	(35)	01		TREWIP TREWDP TSPACP TREADP TWRITP TWRIEP TSPAFP THISPP 30 15A 31 15B 34 16A 36 17A 37 18B 38 18A 40 19A 42 20A	
XA417	TDD	J1	32A	TCSD1I	00	=		
XA417	TDD	J1	32A	( )	01		TCSD2P 68 32A	
XA417	TDD	JN	33A	TCSD1N	00	=		
XA417	TDD	JN	33A	( )	01		TSCK3B 70 33A	
XA417	TDD	JP	31B	TCSD1P	00	=		
XA417	TDD	JP	31A	(59)	01		TCSDRA 66 31A	
XA417	TDD	JQ	32B	TCSD1Q	00	=		
XA417	TDD	JQ	33B	(61)	01		SP1006 63 33B	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TCSD10

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

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CONNECTOR	GROUP	TEST POINTS AND/OR	EQUATION	TERMINAL	DESIGNATOR	FACTOR	COMMENT
XA407	TQ2	B3 10B	TCSD10	00	=		
XA407	TQ2	B3 08B	(21)	01		TREWTP TREWOP 17 08B 19 09B	
XA525	TD4	B1 11B	TCSD2A	00	=		
XA525	TD4	B1 12A	(23)	01		TREWOP TCSD1P TCSD2Q TSCL1B 22 12A 24 13A 25 12B 26 14A	
			TCSD2I	00	=		
XA415	TDD	KI 29A	( )	01		TCSD1Q 62 29A	
			TCSD2N	00	=		
XA415	TDD	KN 28A	( )	01		TSCK3B 60 28A	
XA415	TDD	KP 30B	TCSD2P	00	=		
XA415	TDD	KP 30A	(57)	01		TCSDRA 64 30A	
XA415	TDD	KQ 29B	TCSD2Q	00	=		INPUT DELAY COUNTER BIT 2
XA415	TDD	KQ 28B	(55)	01		SPI002 53 28B	
XA523	TQ2	F1 37B	TCSD2O	00	=		
XA523	TQ2	F1 38B	(75)	01		TCSD2A SPI022 77 38B 79 39B	
XA418	TS8	B1 11B	TCZR0A	00	=		MAIN TIMING COUNTER IS ZERO
XA418	TS8	B1 09A	(23)	01		TC00BQ TC04BQ TC10BQ TC14BQ TC20BQ TC24BQ TCZR10 SPI007 14 09A 18 10A 19 09B 20 11A 22 12A 24 13A 25 12B 26 14A	
XA412	TQ2	C2 15A	TCZR00	00	=		
XA412	TQ2	C2 16A	(30)	01		TCZR0A SPI004 34 16A 36 17A	
XA418	TS8	C1 17B	TCZR1A	00	=		
XA418	TS8	C1 15A	(35)	01		TC30BQ TC34BQ TC40BQ TC44BQ TC50BQ TC54BQ TC60BQ TC64BQ 30 15A 31 15B 34 16A 36 17A 37 18B 38 18A 40 19A 42 20A	
XA412	TQ2	C3 16B	TCZR10	00	=		
XA412	TQ2	C3 14B	(33)	01		TCZR1A SPI004 29 14B 31 15B	
XA521	TS8	A1 05B	TCZR7A	00	=		WRITE TIMING COUNTER IS ZERO
XA521	TS8	A1 02B	(11)	01		TC70BQ TC74BQ TC80BQ TC84BQ TC90BQ TC91BQ SPI022 SPI019 01 02B 04 04A 05 03B 06 05A 07 03A 08 06A 10 07A 13 06B	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015

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TCZR70

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERMINAL	DESIGNATOR	FACTOR	COMMENT
XA520	TQ2	B3	10B	TCZR70	00	=		
XA520	TQ2	B3	08B	(21)	01		TCZR7A SPI021 17 08B 19 09B	
				TC00BI	00	=		
XA413	TDD	AI	06A	( )	01		TC04BP 08 06A	
				TC00BN	00	=		
XA413	TDD	AN	07A	( )	01		TCCP00 10 07A	
				TC00BP	00	=		
XA413	TDD	AP	05B	(11)	01		SPI004 06 05A	
				TC00BQ	00	=		
XA413	TDD	AQ	06B	(13)	01		TCRS0B 15 07B	
				TC01BI	00	=		
XA414	TDD	AI	06A	( )	01		TC00BQ 08 06A	
				TC01BN	00	=		
XA414	TDD	AN	07A	( )	01		TCCP00 10 07A	
				TC01BP	00	=		
XA414	TDD	AP	05B	(11)	01		SPI004 06 05A	
				TC01BQ	00	=		
XA414	TDD	AQ	06B	(13)	01		TCRS0B 15 07B	
				TC02BI	00	=		
XA415	TDD	AI	06A	( )	01		TC01BQ 08 06A	
				TC02BN	00	=		
XA415	TDD	AN	07A	( )	01		TCCP00 10 07A	
				TC02BP	00	=		
XA415	TDD	AP	05B	(11)	01		SPI005 06 05A	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TC02BQ

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UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERMINAL	RESISTOR	FACTOR	COMMENT
XA415	TDD	AQ	06B	TC02BQ	00 =			
XA415	TDD	AQ	07B	(13)	01		TCRS0B 15 07B	
				TC038I	00 =			
XA416	TDD	AI	05A	( )	01		TC02BQ 08 06A	
				TC03BN	00 =			
XA416	TDD	AN	07A	( )	01		TC03P0 10 07A	
XA416	TDD	AP	05B	TC03BP	00 =			
XA416	TDD	AP	05A	(11)	01		SPI005 06 05A	
XA416	TDD	AQ	06B	TC03BQ	00 =			
XA416	TDD	AQ	07B	(13)	01		TCRS0B 15 07B	
				TC04BI	00 =			
XA417	TDD	AI	05A	( )	01		TC03BQ 08 06A	
				TC04BN	00 =			
XA417	TDD	AN	07A	( )	01		TC03P0 10 07A	
XA417	TDD	AP	05B	TC04BP	00 =			
XA417	TDD	AP	05A	(11)	01		SPI005 06 05A	
XA417	TDD	AQ	06B	TC04BQ	00 =			MAIN TIMING COUNTER 1 QUS
XA417	TDD	AQ	07B	(13)	01		TCRS0B 15 07B	
				TC10BI	00 =			
XA413	TDD	BI	03B	( )	01		TC14BP 05 03B	
				TC10BN	00 =			
XA413	TDD	BN	02B	( )	01		TC03P10 01 02B	
XA413	TDD	BP	04B	TC10BP	00 =			
XA413	TDD	BP	04A	(09)	01		SPI002 04 04A	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TC10BQ

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UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA413	TDD	BQ	03A	TC10BQ	00	=		
XA413	TDD	BQ	02A	(07)	01		TCRSOB 03 02A	
				TC11B1	00	=		
XA414	TDD	BI	03B	( )	01		TC10BQ 05 03B	
				TC11B1	00	=		
XA414	TDD	BN	02B	( )	01		TCCP10 01 02B	
				TC11B1	00	=		
XA414	TDD	BP	04B	TC11BP	00	=		
XA414	TDD	BP	04A	(09)	01		SPI002 04 04A	
				TC11BQ	00	=		
XA414	TDD	BQ	03A	(07)	01		TCRSOB 03 02A	
				TC12B1	00	=		
XA415	TDD	BI	03B	( )	01		TC11BQ 05 03B	
				TC12B1	00	=		
XA415	TDD	BN	02B	( )	01		TCCP10 01 02B	
				TC12B1	00	=		
XA415	TDD	BP	04B	TC12BP	00	=		
XA415	TDD	BP	04A	(09)	01		SPI002 04 04A	
				TC12BQ	00	=		
XA415	TDD	BQ	03A	(07)	01		TCRSOB 03 02A	
				TC13B1	00	=		
XA416	TDD	BI	03B	( )	01		TC12BQ 05 03B	
				TC13B1	00	=		
XA416	TDD	BN	02B	( )	01		TCCP10 01 02B	
				TC13B1	00	=		
XA416	TDD	BP	04B	TC13BP	00	=		
XA416	TDD	BP	04A	(09)	01		SPI002 04 04A	





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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TC21BQ

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UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

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CONNECTOR	GROUP	TEST POINTS AND/OR	EQUATION	TERMINAL	DESIGNATOR	FACTOR	COMMENT
XA414	TDD	CQ 12B	TC21BQ	00	=		
XA414	TDD	CQ 13B	(25)	01		TCRS08 27 13B	
			TC22B1	00	=		
XA415	TDD	CI 13A	( )	01		TC21BQ 24 13A	
			TC228N	00	=		
XA415	TDD	CN 14A	( )	01		TCCP20 26 14A	
XA415	TDD	CP 11B	TC22BP	00	=		
XA415	TDD	CP 12A	(23)	01		SPI005 22 12A	
XA415	TDD	CQ 12B	TC22BQ	00	=		
XA415	TDD	CQ 13B	(25)	01		TCRS08 27 13B	
			TC23B1	00	=		
XA416	TDD	CI 13A	( )	01		TC22BQ 24 13A	
			TC238N	00	=		
XA416	TDD	CN 14A	( )	01		TCCP20 26 14A	
XA416	TDD	CP 11B	TC23BP	00	=		
XA416	TDD	CP 12A	(23)	01		SPI005 22 12A	
XA416	TDD	CQ 12B	TC23BQ	00	=		
XA416	TDD	CQ 13B	(25)	01		TCRS08 27 13B	
			TC24B1	00	=		
XA417	TDD	CI 13A	( )	01		TC23BQ 24 13A	
			TC248N	00	=		
XA417	TDD	CN 14A	( )	01		TCCP20 26 14A	
XA417	TDD	CP 11B	TC24BP	00	=		
XA417	TDD	CP 12A	(23)	01		SPI005 22 12A	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TC24BQ

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UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATION	FACTOR	COMMENT
XA417	TDD	CQ	12B	TC248Q	00	=		MAIN TIMING COUNTER IMS
XA417	TDD	CQ	13B	(25)	01		TCRS08 27 13B	
XA413	TDD	DI	10A	TC30BI	00	=	TC348P 18 10A	
XA413	TDD	DN	09A	TC30BN	00	=	TC30BN 14 09A	
XA413	TDD	DP	10B	TC30BP	00	=	SPI	
XA413	TDD	DP	11A	(21)	01		SPI002 20 11A	
XA413	TDD	DQ	09B	TC30BQ	00	=		
XA413	TDD	DQ	08B	(19)	01		TCRS08 17 08B	
XA414	TDD	DI	10A	TC31BI	00	=	TC308Q 18 10A	
XA414	TDD	DN	09A	TC31BN	00	=	TC308Q 14 09A	
XA414	TDD	DP	10B	TC31BP	00	=		
XA414	TDD	DP	11A	(21)	01		SPI002 20 11A	
XA414	TDD	DQ	09B	TC31BQ	00	=		
XA414	TDD	DQ	08B	(19)	01		TCRS08 17 08B	
XA415	TDD	DI	10A	TC32BI	00	=	TC318Q 18 10A	
XA415	TDD	DN	09A	TC32BN	00	=	TC318Q 14 09A	
XA415	TDD	DP	10B	TC32BP	00	=		
XA415	TDD	DP	11A	(21)	01		SPI002 20 11A	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TC32BQ

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	TEST POINT	FACTOR	COMMENT
			AND	OR					
XA415	TDD	DQ	09B		TC32BQ	00	=		
XA415	TDD	DQ	08B		(19)	01		TCRS0B 17 08B	
					TC338I	00	=		
XA416	TDD	DI	10A		( )	01		TC32BQ 18 10A	
					TC338N	00	=		
XA416	TDD	DN	09A		( )	01		TCCP30 14 09A	
XA416	TDD	DP	10B		TC338P	00	=		
XA416	TDD	DP	11A		(21)	01		SPI002 20 11A	
XA416	TDD	DQ	09B		TC338Q	00	=		
XA416	TDD	DQ	08B		(19)	01		TCRS0B 17 08B	
					TC348I	00	=		
XA417	TDD	DI	10A		( )	01		TC338Q 18 10A	
					TC348N	00	=		
XA417	TDD	DN	09A		( )	01		TCCP30 14 09A	
XA417	TDD	DP	10B		TC348P	00	=		
XA417	TDD	DP	11A		(21)	01		SPI006 20 11A	
XA417	TDD	DQ	09B		TC348Q	00	=		MAIN TIMING COUNTER 1 OMS
XA417	TDD	DQ	08B		(19)	01		TCRS0B 17 08B	
					TC408I	00	=		
XA413	TDD	EI	19A		( )	01		TC448P 40 19A	
					TC408N	00	=		
XA413	TDD	EN	20A		( )	01		TCCP40 42 20A	
XA413	TDD	EP	17B		TC408P	00	=		
XA413	TDD	EP	18A		(35)	01		SPI004 38 18A	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

LOGIC UNIT ASSEMBLY NO. 149015 REV. C INDEX TC40BQ  
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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA413	TDD	EQ	18B	TC40BQ	00	=		
XA413	TDD	EQ	19B	(37)	01		YCRS0B 39 19B	
				TC41BI	00	=		
XA414	TDD	EI	19A	( )	01		YC40BQ 40 19A	
				TC41BN	00	=		
XA414	TDD	EN	20A	( )	01		TCCP40 42 20A	
XA414	TDD	EP	17B	TC41BP	00	=		
XA414	TDD	EP	18A	(35)	01		SPI005 38 18A	
XA414	TDD	EQ	18B	TC41BQ	00	=		
XA414	TDD	EQ	19B	(37)	01		YCRS0B 39 19B	
				TC42BI	00	=		
XA415	TDD	EI	19A	( )	01		TC41BQ 40 19A	
				TC42BN	00	=		
XA415	TDD	EN	20A	( )	01		TCCP40 42 20A	
XA415	TDD	EP	17B	TC42BP	00	=		
XA415	TDD	EP	18A	(35)	01		SPI005 38 18A	
XA415	TDD	EQ	18B	TC42BQ	00	=		
XA415	TDD	EQ	19B	(37)	01		YCRS0B 39 19B	
				TC43BI	00	=		
XA416	TDD	EI	19A	( )	01		TC42BQ 40 19A	
				TC43BN	00	=		
XA416	TDD	EN	20A	( )	01		TCCP40 42 20A	
XA416	TDD	EP	17B	TC43BP	00	=		
XA416	TDD	EP	18A	(35)	01		SPI005 38 18A	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TC43BQ

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

FILE IDENT

T39CIFC5

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA416	TDD	EQ	18B	TC43BQ	00	=		
XA416	TDD	EQ	19B	(37)	01		TCR50B 39 19B	
				TC44BI	00	=		
XA417	TDD	EI	19A	( )	01		TC43BQ 40 19A	
				TC44BN	00	=		
XA417	TDD	EN	20A	( )	01		TCCP40 42 20A	
XA417	TDD	EP	17B	TC44BP	00	=		
XA417	TDD	EP	18A	(35)	01		SPI005 38 18A	
XA417	TDD	EQ	18B	TC44BQ	00	=		
XA417	TDD	EQ	19B	(37)	01		TCR50B 39 19B	MAIN TIMING COUNTER 1 00MS
				TC50BI	00	=		
XA413	TDD	FI	16A	( )	01		TC54BP 34 16A	
				TC50BN	00	=		
XA413	TDD	FN	15A	( )	01		TCCP50 30 15A	
XA413	TDD	FP	16B	TC50BP	00	=		
XA413	TDD	FP	17A	(33)	01		SPI002 36 17A	
XA413	TDD	FQ	15B	TC50BQ	00	=		
XA413	TDD	FQ	14B	(31)	01		TCR50B 29 14B	
				TC51BI	00	=		
XA414	TDD	FI	16A	( )	01		TC50BQ 34 16A	
				TC51BN	00	=		
XA414	TDD	FN	15A	( )	01		TCCP50 30 15A	
XA414	TDD	FP	16B	TC51BP	00	=		
XA414	TDD	FP	17A	(33)	01		SPI002 36 17A	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TC51BQ

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UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

FILE IDENT

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERMINAL DESIGNATION	FACTOR	COMMENT
XA414	TDD	FQ	15B	TC518Q	00 =		
XA414	TDD	FQ	14B	(31)	01	TCRS0B 29 14B	
				TC52BI	00 =		
XA415	TDD	FI	16A	( )	01	TC518Q 34 16A	
				TC52BN	00 =		
XA415	TDD	FN	15A	( )	01	TC52BN 30 15A	
XA415	TDD	FP	16B	TC52BP	00 =		
XA415	TDD	FP	17A	(33)	01	SPI002 36 17A	
XA415	TDD	FQ	15B	TC52BQ	00 =		
XA415	TDD	FQ	14B	(31)	01	TCRS0B 29 14B	
				TC53BI	00 =		
XA416	TDD	FI	16A	( )	01	TC52BQ 34 16A	
				TC53BN	00 =		
XA416	TDD	FN	15A	( )	01	TC53BN 30 15A	
XA416	TDD	FP	16B	TC53BP	00 =		
XA416	TDD	FP	17A	(33)	01	SPI002 36 17A	
XA416	TDD	FQ	15B	TC53BQ	00 =		
XA416	TDD	FQ	14B	(31)	01	TCRS0B 29 14B	
				TC54BI	00 =		
XA417	TDD	FI	16A	( )	01	TC53BQ 34 16A	
				TC54BN	00 =		
XA417	TDD	FN	15A	( )	01	TC54BN 30 15A	
XA417	TDD	FP	16B	TC54BP	00 =		
XA417	TDD	FP	17A	(33)	01	SPI006 36 17A	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TC54BQ

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

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CONNECTOR	UNIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERMINAL	DESIGNATOR	FACTOR	COMMENT
XA417	TDD	FQ	15B	TC54BQ	00	=		
XA417	TDD	FQ	14B	(31)	01	=	TCRS08 29 14B	
				TC60BI	00	=		
XA413	TDD	GI	25A	( )	01	=	TC64BP 54 25A	
				TC60BN	00	=		
XA413	TDD	GN	26A	( )	01	=	TCCP60 56 26A	
XA413	TDD	GP	25B	TC60BP	00	=		
XA413	TDD	GP	24A	(47)	01	=	SP1004 52 24A	
XA413	TDD	GQ	26B	TC60BQ	00	=		
XA413	TDD	GQ	27B	(49)	01	=	TCRS08 51 27B	
				TC61BI	00	=		
XA414	TDD	GI	25A	( )	01	=	TC60BQ 54 25A	
				TC61BN	00	=		
XA414	TDD	GN	26A	( )	01	=	TCCP60 56 26A	
XA414	TDD	GP	25B	TC61BP	00	=		
XA414	TDD	GP	24A	(47)	01	=	SP1005 52 24A	
XA414	TDD	GQ	26B	TC61BQ	00	=		
XA414	TDD	GQ	27B	(49)	01	=	TCRS08 51 27B	
				TC62BI	00	=		
XA415	TDD	GI	25A	( )	01	=	TC61BQ 54 25A	
				TC62BN	00	=		
XA415	TDD	GN	26A	( )	01	=	TCCP60 56 26A	
XA415	TDD	GP	25B	TC62BP	00	=		
XA415	TDD	GP	24A	(47)	01	=	SP1005 52 24A	



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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TC62BQ

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

FILE IDENT T39CIFC5 DATE 09-03-82 PAGE X263

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATION	FACTOR	COMMENT
XA415	TDD	GQ	26B	TC62BQ	00 =			
XA415	TDD	GQ	27B	(49)	01	YCR508 51 27B		
				TC63BI	00 =			
XA416	TDD	GI	25A	( )	01	YC62BQ 54 25A		
				TC63BN	00 =			
XA416	TDD	GN	26A	( )	01	YCCP60 56 26A		
XA416	TDD	GP	25B	TC63BP	00 =			
XA416	TDD	GP	24A	(47)	01	SPI005 52 24A		
XA416	TDD	GQ	26B	TC63BQ	00 =			
XA416	TDD	GQ	27B	(49)	01	YCR508 51 27B		
				TC64BI	00 =			
XA417	TDD	GI	25A	( )	01	YC63BQ 54 25A		
				TC64BN	00 =			
XA417	TDD	GN	26A	( )	01	YCCP60 56 26A		
XA417	TDD	GP	25B	TC64BP	00 =			
XA417	TDD	GP	24A	(47)	01	SPI005 52 24A		
XA417	TDD	GQ	26B	TC64BQ	00 =			
XA417	TDD	GQ	27B	(49)	01	YCR508 51 27B		MAIN TIMING COUNTER 1 DS
				TC70BI	00 =			
XA413	TDD	HI	22A	( )	01	YC74BP 48 22A		
				TC70BN	00 =			
XA413	TDD	HN	21A	( )	01	YCCP70 46 21A		
XA413	TDD	HP	24B	TC70BP	00 =			
XA413	TDD	HP	23A	(45)	01	SPI002 50 23A		

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TC70BQ

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

FILE IDENT T39CIFC5

DATE 09-03-82 PAGE

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	REFS	RATIO	FACTOR	COMMENT
XA413	TDD	HQ	23B	TC70BQ	00	=			
XA413	TDD	HQ	22B	(43)	01			TCRS7A 41 22B	
XA414	TDD	HI	22A	TC71BI	00	=			
XA414	TDD	HI	22A	( )	01			TC70BQ 48 22A	
XA414	TDD	HN	21A	TC71BN	00	=			
XA414	TDD	HN	21A	( )	01			TCCP70 46 21A	
XA414	TDD	HP	24B	TC71BP	00	=			
XA414	TDD	HP	23A	(45)	01			SPI002 50 23A	
XA414	TDD	HQ	23B	TC71BQ	00	=			
XA414	TDD	HQ	22B	(43)	01			TCRS7A 41 22B	
XA415	TDD	HI	22A	TC72BI	00	=			
XA415	TDD	HI	22A	( )	01			TC71BQ 48 22A	
XA415	TDD	HN	21A	TC72BN	00	=			
XA415	TDD	HN	21A	( )	01			TCCP70 46 21A	
XA415	TDD	HP	24B	TC72BP	00	=			
XA415	TDD	HP	23A	(45)	01			SPI002 50 23A	
XA415	TDD	HQ	23B	TC72BQ	00	=			
XA415	TDD	HQ	22B	(43)	01			TCRS7A 41 22B	
XA416	TDD	HI	22A	TC73BI	00	=			
XA416	TDD	HI	22A	( )	01			TC72BQ 48 22A	
XA416	TDD	HN	21A	TC73BN	00	=			
XA416	TDD	HN	21A	( )	01			TCCP70 46 21A	
XA416	TDD	HP	24B	TC73BP	00	=			
XA416	TDD	HP	23A	(45)	01			SPI002 50 23A	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TC73BQ

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

FILE IDENT T39CIFC5 DATE 09-03-82 PAGE 265

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESIGNATION	FACTOR	COMMENT
			AND	OR					
XA416	TDD	HQ	23B		TC738Q	00 =			
XA416	TDD	HQ	22B		(43)	01	TCR57A 41 22B		
					TC74BI	00 =			
XA417	TDD	HI	22A		( )	01	TC73BQ 48 22A		
					TC74BN	00 =			
XA417	TDD	HN	21A		( )	01	TCCP70 46 21A		
XA417	TDD	HP	24B		TC74BP	00 =			
XA417	TDD	HP	23A		(45)	01	SPI006 50 23A		
XA417	TDD	HQ	23B		TC748Q	00 =			WRITE TIMING COUNTER 1 QUS
XA417	TDD	HQ	22B		(43)	01	TCR57A 41 22B		
					TC80BI	00 =			
XA413	TDD	JI	32A		( )	01	TC84BP 68 32A		
					TC80BN	00 =			
XA413	TDD	JN	33A		( )	01	TCCP80 70 33A		
XA413	TDD	JP	31B		TC80BP	00 =			
XA413	TDD	JP	31A		(59)	01	SPI004 66 31A		
XA413	TDD	JQ	32B		TC80BQ	00 =			
XA413	TDD	JQ	33B		(61)	01	TCR57A 63 33B		
					TC81BI	00 =			
XA414	TDD	JI	32A		( )	01	TC80BQ 68 32A		
					TC81BN	00 =			
XA414	TDD	JN	33A		( )	01	TCCP80 70 33A		
XA414	TDD	JP	31B		TC81BP	00 =			
XA414	TDD	JP	31A		(59)	01	SPI005 66 31A		

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERMINAL	DESIGNATION	FACTOR	COMMENT
XA414	TDD	JQ	32B	TC81BQ	00 =			
XA414	TDD	JQ	33B	(61)	01	TCRS7A 63 33B		
				TC82BI	00 =			
XA415	TDD	JI	32A	( )	01	TC81BQ 68 32A		
				TC82BN	00 =			
XA415	TDD	JN	33A	( )	01	TCCP80 70 33A		
XA415	TDD	JP	31B	TC82BP	00 =			
XA415	TDD	JP	31A	(59)	01	SPI005 66 31A		
XA415	TDD	JQ	32B	TC82BQ	00 =			
XA415	TDD	JQ	33B	(61)	01	TCRS7A 63 33B		
				TC83BI	00 =			
XA416	TDD	JI	32A	( )	01	TC82BQ 68 32A		
				TC83BN	00 =			
XA416	TDD	JN	33A	( )	01	TCCP80 70 33A		
XA416	TDD	JP	31B	TC83BP	00 =			
XA416	TDD	JP	31A	(59)	01	SPI005 66 31A		
XA416	TDD	JQ	32B	TC83BQ	00 =			
XA416	TDD	JQ	33B	(61)	01	TCRS7A 63 33B		
				TC84BI	00 =			
XA417	TDD	MI	36A	( )	01	TC83BQ 71 36A		
				TC84BN	00 =			
XA417	TDD	MN	34A	( )	01	TCCP80 72 34A		
XA417	TDD	MP	35A	TC84BP	00 =			
XA417	TDD	MP	36B	(69)	01	SPI005 73 36B		

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TC84BQ

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

FILE IDENT

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR		EQUATION	TERM	TEST POINT	FACTOR	COMMENT
XA417	TDD	HQ	35B		TC84BQ	00	=		INPUT DELAY COUNTER BIT 1
XA417	TDD	HQ	34B	(74)		01		TCRS7A 65 34B	
XA413	TDD	KI	29A		TC908I	00	=		
						01		TC918P 62 29A	
XA413	TDD	KN	28A		TC908N	00	=		
						01		TCCP90 60 28A	
XA413	TDD	KP	30B		TC908P	00	=		
XA413	TDD	KP	30A	(57)		01		SPI002 64 30A	
XA413	TDD	KQ	29B		TC908Q	00	=		
XA413	TDD	KQ	28B	(55)		01		TCRS7A 53 28B	
XA414	TDD	KI	29A		TC918I	00	=		
						01		TC908Q 62 29A	
XA414	TDD	KN	28A		TC918N	00	=		
						01		TCCP90 60 28A	
XA414	TDD	KP	30B		TC918P	00	=		
XA414	TDD	KP	30A	(57)		01		SPI002 64 30A	
XA414	TDD	KQ	29B		TC918Q	00	=		WRITE TIMING COUNTER 40 OUS
XA414	TDD	KQ	28B	(55)		01		TCRS7A 53 28B	
XA520	TQ2	C1	18A		TDCP00	00	=		INPUT DATA REG CLKBITS 0-3
XA520	TQ2	C1	19A	(38)		01		TXED0A TXOD0A 40 19A 42 20A	
XA520	TQ2	C2	15A		TDCP10	00	=		
XA520	TQ2	C2	16A	(30)		01		TXED0A TXOD0A 34 16A 36 17A	
XA520	TQ2	C3	16B		TDCP20	00	=		
XA520	TQ2	C3	14B	(33)		01		TXED1A TXOD1A 29 14B 31 15B	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TDCP30

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM DESIGNATION	FACTOR	COMMENT
XA520	TQ2	C4	19B	TDCP30	00 =		
XA520	TQ2	C4	17B	(39)	01	TXED1A TXOD1A 35 17B 37 18B	
XA520	TQ2	D1	24A	TDCP40	00 =		
XA520	TQ2	D1	25A	(52)	01	TXED2A TXOD2A 54 25A 56 26A	
XA520	TQ2	D2	21A	TDCP50	00 =		
XA520	TQ2	D2	22A	(46)	01	TXED2A TXOD2A 48 22A 50 23A	
XA520	TQ2	D3	24B	TDCP60	00 =		
XA520	TQ2	D3	22B	(45)	01	TXED3A TXOD3A 41 22B 43 23B	
XA520	TQ2	D4	27B	TDCP70	00 =		INPUT DATA REG CLKBITS 28-31
XA520	TQ2	D4	25B	(51)	01	TXED3A TXOD3A 47 25B 49 26B	
XA543	TLD	D4	27B	TDIRCD4	00 =		DIRECTION DRIVER
XA543	TLD	D4	25B	(51)	01	TDIRIS SPI029 47 25B 49 26B	
XA406	TQ2	C1	18A	TDIRIR	00 =		
XA406	TQ2	C1	19A	(38)	01	TDIRIS TDIR1A 40 19A 42 20A	
XA407	TQ2	C1	18A	TDIRIS	00 =		OLD DIRECTION E/F
XA407	TQ2	C1	19A	(38)	01	TDIRIR TDIR10A 40 19A 42 20A	
XA541	TDD	CI	13A	TDIRSI	00 =		
				( )	01	TXR4CS 24 13A	
XA541	TDD	CN	14A	TDIRSN	00 =		
				( )	01	TXDV1B 26 14A	
XA541	TDD	CP	11B	TDIRSP	00 =		
XA541	TDD	CP	12A	(23)	01	TRDCAB 22 12A	
XA541	TDD	CQ	12B	TDIRSQ	00 =		NEW DIRECTION E/F
XA541	TDD	CQ	13B	(25)	01	SPI026 27 13B	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TDR10A

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
			AND	OR					
XA406	TQ2	D1	24A		TDR10A	00	=		
XA406	TQ2	D1	25A		(52)	01		TDIRSQ TSTP2S 54 25A 56 26A	
XA408	TQ2	D4	27B		TDR11A	00	=		
XA408	TQ2	D4	25B		(51)	01		TDIRSP TSTP2S 47 25B 49 26B	
XA437	TS8	F1	37A		TDRSCA	00	=		CLEAR INPUT DATA REG 4TH BYTE
XA437	TS8	F1	36A		(76)	01		TRTDC0 TKB00P TKB01P TKA01Q TKA02P TSCL3B SPI006 SPI011 71 36A 72 34A 73 36B 74 35B 75 37B 77 38B 78 38A 79 39B	
XA406	TQ2	E3	30B		TDRS0A	00	=		
XA406	TQ2	E3	28B		(57)	01		TDRS00 SPI001 53 28B 55 29B	
XA518	TT3	B2	09B		TDRS00	00	=		
XA518	TT3	B2	09A		(19)	01		TDRSCA TX0DRA TXRS2B 14 09A 17 08B 18 10A	
XA520	TQ2	E2	28A		TDRS1A	00	=		
XA520	TQ2	E2	29A		(60)	01		TDRS00 SPI021 62 29A 64 30A	
XA520	TQ2	E3	30B		TDRS2A	00	=		
XA520	TQ2	E3	28B		(57)	01		TDRS00 SPI022 53 28B 55 29B	
XA520	TQ2	E4	33B		TDRS3A	00	=		
XA520	TQ2	E4	31B		(63)	01		TDRS00 SPI022 59 31B 61 32B	
XA520	TQ2	F1	37B		TDRS4A	00	=		
XA520	TQ2	F1	38B		(75)	01		TDRS00 SPI022 77 38B 79 39B	
XA520	TQ2	F2	34A		TDRS5A	00	=		
XA520	TQ2	F2	36A		(72)	01		TDRS00 SPI022 71 36A 73 36B	
XA520	TQ2	F3	35A		TDRS6A	00	=		
XA520	TQ2	F3	34B		(69)	01		TDRS00 SPI022 65 34B 74 35B	
XA520	TQ2	F4	39A		TDRS7A	00	=		
XA520	TQ2	F4	37A		(80)	01		TDRS00 SPI022 76 37A 78 38A	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TDSC0A

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA407	TQ2	C4	19B	TDSC0A	00	=		
XA407	TQ2	C4	17B	(39)	01		TDSC1ER TDSC2ER 35 17B 37 18B	
XA406	TQ2	C4	19B	TDSC00	00	=		
XA406	TQ2	C4	17B	(39)	01		TDSC0A SPI001 35 17B 37 18B	
XA540	EOR	B1	10B	TDSC1ER	00	=		
XA540	EOR	B1	09B	(19)	01		TDIRSQ TDIRIR 17 09B 15 08B	
XA540	EOR	B2	13B	TDSC2ER	00	=		
XA540	EOR	B2	12B	(27)	01		TSPNSQ TSPNIR 23 12B 21 11B	
				TD00BI	00	=		
XA528	TDD	AI	06A	( )	01		TXROCS 08 06A	
				TD00BN	00	=		
XA528	TDD	AN	07A	( )	01		TDCP00 10 07A	
XA528	TDD	AP	05B	TD00BP	00	=		
XA528	TDD	AP	05A	(11)	01		TDRSOA 06 05A	
XA528	TDD	AQ	06B	TD00BQ	00	=		
XA528	TDD	AQ	07B	(13)	01		TROOCA 15 07B	INPUT DATA REG BIT0
				TD01BI	00	=		
XA528	TDD	BI	03B	( )	01		TXR1CS 05 03B	
				TD01BN	00	=		
XA528	TDD	BN	02B	( )	01		TDCP00 01 02B	
XA528	TDD	BP	04B	TD01BP	00	=		
XA528	TDD	BP	04A	(09)	01		TDRSOA 04 04A	
XA528	TDD	BQ	03A	TD01BQ	00	=		
XA528	TDD	BQ	02A	(07)	01		TRO1CA 03 02A	



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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TD02BI

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

FILE IDENT

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DATE 09-03-82

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM NATOR	DESIG- NATOR	FACTOR	COMMENT
XA528	TDD	CI	13A	TD02BI ( )	00 01	=	TXR2CS 24 13A	
XA528	TDD	CN	14A	TD02BN ( )	00 01	=	TDCP00 26 14A	
XA528	TDD	CP	11B	TD02BP (23 )	00 01	=	TDRS0A 22 12A	
XA528	TDD	CQ	12B	TD02BQ (25 )	00 01	=	TR02CA 27 13B	
XA528	TDD	DI	10A	TD03BI ( )	00 01	=	TXR3CS 18 10A	
XA528	TDD	DN	09A	TD03BN ( )	00 01	=	TDCP00 14 09A	
XA528	TDD	DP	10B	TD03BP (21 )	00 01	=	TDRS0A 20 11A	
XA528	TDD	DQ	09B	TD03BQ (19 )	00 01	=	TR03CA 17 08B	
XA528	TDD	EI	19A	TD04BI ( )	00 01	=	TXR4CS 40 19A	
XA528	TDD	EN	20A	TD04BN ( )	00 01	=	TDCP10 42 20A	
XA528	TDD	EP	17B	TD04BP (35 )	00 01	=	TDRS1A 38 18A	
XA528	TDD	EQ	18B	TD04BQ (37 )	00 01	=	TR04CA 39 19B	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TD05BI

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

FILE IDENT

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DATE 09-03-82

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERMINAL DESIGNATION	FACTOR	COMMENT
				TD05BI	00 =		
XA528	TDD	FI	16A	( )	01	TXR5CS 34 16A	
				TD05BN	00 =		
XA528	TDD	FN	15A	( )	01	TDCP10 30 15A	
XA528	TDD	FP	16B	TD05BP	00 =		
XA528	TDD	FP	17A	(33 )	01	TDRS1A 36 17A	
XA528	TDD	FQ	15B	TD05BQ	00 =		
XA528	TDD	FQ	14B	(31 )	01	TR05CA 29 14B	
				TD06BI	00 =		
XA528	TDD	GI	25A	( )	01	TXR6CS 54 25A	
				TD06BN	00 =		
XA528	TDD	GN	26A	( )	01	TDCP10 56 26A	
XA528	TDD	GP	25B	TD06BP	00 =		
XA528	TDD	GP	24A	(47 )	01	TDRS1A 52 24A	
XA528	TDD	GQ	26B	TD06BQ	00 =		
XA528	TDD	GQ	27B	(49 )	01	TR06CA 51 27B	
				TD07BI	00 =		
XA528	TDD	HI	22A	( )	01	TXR7CS 48 22A	
				TD07BN	00 =		
XA528	TDD	HN	21A	( )	01	TDCP10 46 21A	
XA528	TDD	HP	24B	TD07BP	00 =		
XA528	TDD	HP	23A	(45 )	01	TDRS1A 50 23A	
XA528	TDD	HQ	23B	TD07BQ	00 =		
XA528	TDD	HQ	22B	(43 )	01	TR07CA 41 22B	INPUT DATA REG. BIT7

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015

REV. C

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TD08BI

LOGIC

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T39C1FC5

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERMINAL	DESIGNATION	FACTOR	COMMENT
XA528	TDD	JI	32A	TD08BI	00 =			
				( )	01		YXROCS 68 32A	
				TD08BN	00 =			
XA528	TDD	JN	33A	( )	01		YDCP20 70 33A	
XA528	TDD	JP	31B	TD08BP	00 =			
XA528	TDD	JP	31A	(59 )	01		YDRS2A 66 31A	
XA528	TDD	JQ	32B	TD08BQ	00 =			INPUT DATA REG BITS
XA528	TDD	JQ	33B	(61 )	01		YR08CA 63 33B	
				TD09BI	00 =			
XA528	TDD	KI	29A	( )	01		YXRICS 62 29A	
				TD09BN	00 =			
XA528	TDD	KN	28A	( )	01		YDCP20 60 28A	
XA528	TDD	KP	30B	TD09BP	00 =			
XA528	TDD	KP	30A	(57 )	01		YDRS2A 64 30A	
XA528	TDD	KQ	29B	TD09BQ	00 =			
XA528	TDD	KQ	28B	(55 )	01		YR09CA 53 28B	
				TD10BI	00 =			
XA528	TDD	LI	38B	( )	01		YXR2CS 77 38B	
				TD10BN	00 =			
XA528	TDD	LN	39B	( )	01		YDCP20 79 39B	
XA528	TDD	LP	37A	TD10BP	00 =			
XA528	TDD	LP	37B	(76 )	01		YDRS2A 75 37B	
XA528	TDD	LQ	38A	TD10BQ	00 =			
XA528	TDD	LQ	39A	(78 )	01		YR10CA 80 39A	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

LOGIC

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015

REV. C

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TD11BI

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CONNECTOR	GROUP	TEST POINTS AND/OR	EQUATION	PERY	DESIGNATOR	FACTOR	COMMENT
XA528	TDD HI	36A	TD118I ( )	00 = 01		TXR3CS 71 36A	
XA528	TDD MN	34A	TD118N ( )	00 = 01		TDCP20 72 34A	
XA528	TDD MP	35A	TD118P (69 )	00 = 01		TDRS2A 73 36B	
XA528	TDD MQ	35B	TD118Q (74 )	00 = 01		TR11CA 65 34B	
XA529	TDD AI	06A	TD128I ( )	00 = 01		TXR4CS 08 06A	
XA529	TDD AN	07A	TD128N ( )	00 = 01		TDCP30 10 07A	
XA529	TDD AP	05B	TD128P (11 )	00 = 01		TDRS3A 06 05A	
XA529	TDD AQ	06B	TD128Q (13 )	00 = 01		TR12CA 15 07B	
XA529	TDD BI	03B	TD138I ( )	00 = 01		TXR5CS 05 03B	
XA529	TDD BN	02B	TD138N ( )	00 = 01		TDCP30 01 02B	
XA529	TDD BP	04B	TD138P (09 )	00 = 01		TDRS3A 04 04A	
XA529	TDD BQ	03A	TD138Q (07 )	00 = 01		TR13CA 03 02A	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TD20BI

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
				TD20BI	00 =			
XA529	TDD	JI	32A	( )	01		TXR4CS 68 32A	
				TD20BN	00 =			
XA529	TDD	JN	33A	( )	01		TDCP50 70 33A	
XA529	TDD	JP	31B		00 =			
XA529	TDD	JP	31A	(59 )	01		TDRS5A 66 31A	
XA529	TDD	JQ	32B		00 =			
XA529	TDD	JQ	33B	(61 )	01		TR20CA 63 33B	
				TD21BI	00 =			
XA529	TDD	KI	29A	( )	01		TXR5CS 62 29A	
				TD21BN	00 =			
XA529	TDD	KN	28A	( )	01		TDCP50 60 28A	
XA529	TDD	KP	30B		00 =			
XA529	TDD	KP	30A	(57 )	01		TDRS5A 64 30A	
XA529	TDD	KQ	29B		00 =			
XA529	TDD	KQ	28B	(55 )	01		TR21CA 53 28B	
				TD22BI	00 =			
XA529	TDD	LI	38B	( )	01		TXR6CS 77 38B	
				TD22BN	00 =			
XA529	TDD	LN	39B	( )	01		TDCP50 79 39B	
XA529	TDD	LP	37A		00 =			
XA529	TDD	LP	37B	(76 )	01		TDRS5A 75 37B	
XA529	TDD	LQ	38A		00 =			
XA529	TDD	LQ	39A	(78 )	01		TR22CA 80 39A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA529	TDD	MI	36A	TD23BI ( )	00 01	=	TXR7CS 71 36A	
XA529	TDD	MN	34A	TD23BN ( )	00 01	=	TDCP50 72 34A	
XA529	TDD	MP	35A	TD23BP (69 )	00 01	=	TDRS5A 73 36B	
XA529	TDD	MQ	35B	TD23BQ (74 )	00 01	=	TR23CA 65 34B	INPUT DATA REG BIT23
XA530	TDD	AI	06A	TD24BI ( )	00 01	=	TXROCS 08 06A	
XA530	TDD	AN	07A	TD24BN ( )	00 01	=	TDCP60 10 07A	
XA530	TDD	AP	05B	TD24BP (11 )	00 01	=	TDRS6A 06 05A	
XA530	TDD	AQ	06B	TD24BQ (13 )	00 01	=	TR24CA 15 07B	INPUT DATA REG BIT24
XA530	TDD	BI	03B	TD25BI ( )	00 01	=	TXR1CS 05 03B	
XA530	TDD	BN	02B	TD25BN ( )	00 01	=	TDCP60 01 02B	
XA530	TDD	BP	04B	TD25BP (09 )	00 01	=	TDRS6A 04 04A	READ/NO INTERRUPT COMMAND F/E
XA530	TDD	BQ	03A	TD25BQ (07 )	00 01	=	TR25CA 03 02A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TEST	DESIGNATOR	FACTOR	COMMENT
				TD14BI	00 =			
XA529	TDD	CI	13A	( )	01		YXR6CS 24 13A	
				TD14BN	00 =			
XA529	TDD	CN	14A	( )	01		YDCP30 26 14A	
XA529	TDD	CP	11B	TD14BP	00 =			
XA529	TDD	CP	12A	(23 )	01		YDRS3A 22 12A	
XA529	TDD	CQ	12B	TD14BQ	00 =			
XA529	TDD	CQ	13B	(25 )	01		TR14CA 27 13B	
				TD15BI	00 =			
XA529	TDD	DI	10A	( )	01		YXR7CS 18 10A	
				TD15BN	00 =			
XA529	TDD	DN	09A	( )	01		YDCP30 14 09A	
XA529	TDD	DP	10B	TD15BP	00 =			
XA529	TDD	DP	11A	(21 )	01		YDRS3A 20 11A	
XA529	TDD	DQ	09B	TD15BQ	00 =			INPUT DATA REG BIT15
XA529	TDD	DQ	08B	(19 )	01		YR15CA 17 08B	
				TD16BI	00 =			
XA529	TDD	EI	19A	( )	01		YXROCS 40 19A	
				TD16BN	00 =			
XA529	TDD	EN	20A	( )	01		YDCP40 42 20A	
XA529	TDD	EP	17B	TD16BP	00 =			
XA529	TDD	EP	18A	(35 )	01		YDRS4A 38 18A	
XA529	TDD	EQ	18B	TD16BQ	00 =			INPUT DATA REG BIT16
XA529	TDD	EQ	19B	(37 )	01		TR16CA 39 19B	

CONNECTOR	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
			TD17BI	00 =			
XA529	TDD	FI 16A	( )	01		TXR1CS 34 16A	
			TD17BN	00 =			
XA529	TDD	FN 15A	( )	01		TDCP40 30 15A	
XA529	TDD	FP 16B	TD17BP	00 =			
XA529	TDD	FP 17A	(33 )	01		TDRS4A 36 17A	
XA529	TDD	FQ 15B	TD17BQ	00 =			
XA529	TDD	FQ 14B	(31 )	01		TR17CA 29 14B	
			TD18BI	00 =			
XA529	TDD	GI 25A	( )	01		TXR2CS 54 25A	
			TD18BN	00 =			
XA529	TDD	GN 26A	( )	01		TDCP40 56 26A	
XA529	TDD	GP 25B	TD18BP	00 =			
XA529	TDD	GP 24A	(47 )	01		TDRS4A 52 24A	
XA529	TDD	GQ 26B	TD18BQ	00 =			
XA529	TDD	GQ 27B	(49 )	01		TR18CA 51 27B	
			TD19BI	00 =			
XA529	TDD	HI 22A	( )	01		TXR3CS 48 22A	
			TD19BN	00 =			
XA529	TDD	HN 21A	( )	01		TDCP40 46 21A	
XA529	TDD	HP 24B	TD19BP	00 =			
XA529	TDD	HP 23A	(45 )	01		TDRS4A 50 23A	
XA529	TDD	HQ 23B	TD19BQ	00 =			
XA529	TDD	HQ 22B	(43 )	01		TR19CA 41 22B	



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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

LOGIC UNIT ASSEMBLY NO. 149015 REV. C INDEX TD26BI  
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CONNECTOR	UNIT GROUP	GROUP	TEST POINTS AND/OR	EQUATION	TER J	DESIG- NATOR	FACTOR	COMMENT
				TD268I	00 =			
XA530	TDD	CI	13A	( )	01		TXR2CS 24 13A	
				TD268N	00 =			
XA530	TDD	CN	14A	( )	01		TDCP60 26 14A	
XA530	TDD	CP	11B	TD268P	00 =			
XA530	TDD	CP	12A	(23 )	01		TDRS6A 22 12A	
XA530	TDD	CQ	12B	TD268Q	00 =			
XA530	TDD	CQ	13B	(25 )	01		TR26CA 27 13B	
				TD278I	00 =			
XA530	TDD	DI	10A	( )	01		TXR3CS 18 10A	
				TD278N	00 =			
XA530	TDD	DN	09A	( )	01		TDCP60 14 09A	
XA530	TDD	DP	10B	TD278P	00 =			
XA530	TDD	DP	11A	(21 )	01		TDRS6A 20 11A	
XA530	TDD	DQ	09B	TD278Q	00 =			
XA530	TDD	DQ	08B	(19 )	01		TR27CA 17 08B	
				TD288I	00 =			
XA530	TDD	EI	19A	( )	01		TXR4CS 40 19A	
				TD288N	00 =			
XA530	TDD	EN	20A	( )	01		TDCP70 42 20A	
XA530	TDD	EP	17B	TD288P	00 =			
XA530	TDD	EP	18A	(35 )	01		TDRS7A 38 18A	
XA530	TDD	EQ	18B	TD288Q	00 =			
XA530	TDD	EQ	19B	(37 )	01		TR28CA 39 19B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	LOGIC	FACTOR	COMMENT
				TD29BI	00 =			
XA530	TDD	FI	16A	( )	01		TXR5CS 34 16A	
				TD29BN	00 =			
XA530	TDD	FN	15A	( )	01		TDCP70 30 15A	
XA530	TDD	FP	16B	TD29BP	00 =			
XA530	TDD	FP	17A	(33 )	01		TDRS7A 36 17A	
				TD29BQ	00 =			
XA530	TDD	FQ	15B	(31 )	01		TR29CA 29 14B	
				TD30BI	00 =			
XA530	TDD	GI	25A	( )	01		TXR6CS 54 25A	
				TD30BN	00 =			
XA530	TDD	GN	26A	( )	01		TDCP70 56 26A	
				TD30BP	00 =			
XA530	TDD	GP	25B	(47 )	01		TDRS7A 52 24A	
				TD30BQ	00 =			
XA530	TDD	GQ	26B	(49 )	01		TR30CA 51 27B	
				TD31BI	00 =			
XA530	TDD	HI	22A	( )	01		TXR7CS 48 22A	
				TD31BN	00 =			
XA530	TDD	HN	21A	( )	01		TDCP70 46 21A	
				TD31BP	00 =			
XA530	TDD	HP	24B	(45 )	01		TDRS7A 50 23A	
				TD31BQ	00 =			
XA530	TDD	HQ	23B	(43 )	01		TR31CA 41 22B	
							INPUT DATA	REG BIT31

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TEPY	DISC. FACTOR	FACTOR	COMMENT
XA527	TS8	A1	05B	TEBZRA	00	=		END OF BLOCK COUNTER IS ZERO
XA527	TS8	A1	02B	(11)	01		TEB0BP TEB1BP TEB2BP TEB3BP TEB4BP TEB5BP SPI019 SPI022 01 02B 04 04A 05 03B 06 05A 07 03A 08 06A 10 07A 13 06B	
XA428	TQ2	E1	31A	TEBZRO	00	=		
XA428	TQ2	E1	32A	(66)	01		TEBZRA SPI010 68 32A 70 33A	
XA436	TDD	GI	25A	TEBOBI	00	=		
XA436	TDD	GI	25A	( )	01		TEB09ER 54 25A	
XA436	TDD	GN	26A	TEBOBN	00	=		
XA436	TDD	GN	26A	( )	01		TEB1BQ 56 26A	
XA436	TDD	GP	25B	TEBOBP	00	=		
XA436	TDD	GP	24A	(47)	01		TEBORA 52 24A	
XA436	TDD	GQ	26B	TEBOBQ	00	=		EOB COUNTER BIT 0
XA436	TDD	GQ	27B	(49)	01		TEB0SA 51 27B	
XA527	TS8	B1	11B	TEBODA	00	=		
XA527	TS8	B1	09A	(23)	01		TXDEVS TXXB20 TXRAF7T TXRAF3T TXRAF5T SPI022 TXXA0Q TXXA3Q 14 09A 18 10A 19 09B 20 11A 22 12A 24 13A 25 12B 26 14A	
XA428	TQ2	C1	18A	TEBORA	00	=		
XA428	TQ2	C1	19A	(38)	01		TEBORO SPI007 40 19A 42 20A	
XA431	TT3	C1	17A	TEBORO	00	=		EOB COUNTER BIT 0 RESET
XA431	TT3	C1	18A	(36)	01		TEB0DA SPI013 TXRS0B 38 18A 40 19A 42 20A	
XA431	TT3	D2	23B	TEBOSA	00	=		EOB COUNTER BIT 0 SET
XA431	TT3	D2	22B	(43)	01		TXAS10 TXR0CS TXDV1B 41 22B 46 21A 48 22A	
XA540	EOR	F4	36A	TEB09ER	00	=		
XA540	EOR	F4	35A	(74)	01		TEB0BQ TXDEVR 72 35A 70 34A	
XA435	TDD	HI	22A	TEB1BI	00	=		
XA435	TDD	HI	22A	( )	01		TEB19ER 48 22A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
				TEB1BN	00			
XA435	TDD	HN	21A	( )	01		TEB2BQ 46 21A	
XA435	TDD	HP	24B	TEB1BP	00	=		
XA435	TDD	HP	23A	(45 )	01		TEB1RA 50 23A	
XA435	TDD	HQ	23B	TEB1BQ	00	=		
XA435	TDD	HQ	22B	(43 )	01		TEB1SA 41 22B	
XA428	TQ2	C2	15A	TEB1RA	00	=		
XA428	TQ2	C2	16A	(30 )	01		TEB1RO SPIO10 34 16A 36 17A	
XA431	TT3	C2	15B	TEB1RO	00	=		
XA431	TT3	C2	14B	(31 )	01		TEB0DA SPIO13 TXRS0B 29 14B 30 15A 34 16A	
XA431	TT3	E2	29B	TEB1SA	00	=		
XA431	TT3	E2	28B	(55 )	01		TTAS10 TXR1CS TXDV1B 53 28B 60 28A 62 29A	
XA540	EOR	A2	07B	TEB19ER	00	=		
XA540	EOR	A2	06B	(11 )	01		TEB1BQ TXDEV 09 06B 07 05B	
				TEB2BI	00	=		
XA434	TDD	JI	32A	( )	01		TEB29ER 68 32A	
				TEB2BN	00	=		
XA434	TDD	JN	33A	( )	01		TEB3BQ 70 33A	
XA434	TDD	JP	31B	TEB2BP	00	=		
XA434	TDD	JP	31A	(59 )	01		TEB2RA 66 31A	
XA434	TDD	JQ	32B	TEB2BQ	00	=		
XA434	TDD	JQ	33B	(61 )	01		TEB2SA 63 33B	
XA428	TQ2	C3	16B	TEB2RA	00	=		
XA428	TQ2	C3	14B	(33 )	01		TEB2RO SPIO10 29 14B 31 15B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA431	TT3	C3	19B	TEB2R0	00	=		
XA431	TT3	C3	16B	(39)	01		TEB0DA SPI013 TXRS0B 33 16B 35 17B 37 18B	
XA431	TT3	E3	33B	TEB2SA	00	=		
XA431	TT3	E3	30B	(63)	01		TTAS10 TXR2CS TXDV1B 57 30B 59 31B 61 32B	
XA540	EOR	A3	07A	TEB29ER	00	=		
XA540	EOR	A3	06A	(13)	01		TEB2BQ TXDEVR 14 06A 10 05A	
				TEB3BI	00	=		
XA436	TDD	HI	22A	( )	01		TEB39ER 48 22A	
				TEB3BN	00	=		
XA436	TDD	HN	21A	( )	01		TEB4BQ 46 21A	
XA436	TDD	HP	24B	TEB3BP	00	=		
XA436	TDD	HP	23A	(45)	01		TEB3RA 50 23A	
XA436	TDD	HQ	23B	TEB3BQ	00	=		
XA436	TDD	HQ	22B	(43)	01		TEB3SA 41 22B	
XA428	TQ2	C4	19B	TEB3RA	00	=		
XA428	TQ2	C4	17B	(39)	01		TEB3R0 SPI010 35 17B 37 18B	
XA431	TT3	D1	23A	TEB3R0	00	=		
XA431	TT3	D1	24A	(50)	01		TEB0DA SPI013 TXRS0B 52 24A 54 25A 56 26A	
XA431	TT3	F1	36B	TEB3SA	00	=		
XA431	TT3	F1	37B	(73)	01		TTAS10 TXR3CS TXDV1B 75 37B 77 38B 79 39B	
XA540	EOR	A4	04A	TEB39ER	00	=		
XA540	EOR	A4	03A	(08)	01		TEB3BQ TXDEVR 06 03A 04 02A	
				TEB4BI	00	=		
XA435	TDD	JI	32A	( )	01		TEB49ER 68 32A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA435	TDD	JN	33A	TEB4BN ( )	00 01	=	TEB5BQ 70 33A	
XA435	TDD	JP	31B	TEB4BP	00	=		
XA435	TDD	JP	31A	(59 )	01		TEB4RA 66 31A	
XA435	TDD	JQ	32B	TEB4BQ	00	=		
XA435	TDD	JQ	33B	(61 )	01		TEB4SA 63 33B	
XA439	TQ2	E2	28A	TEB4RA	00	=		
XA439	TQ2	E2	29A	(60 )	01		TEB4R0 SPI011 62 29A 64 30A	
XA426	TQ2	F3	35A	TEB4R0	00	=		
XA426	TQ2	F3	34B	(69 )	01		TEB5RA TXR52B 65 34B 74 35B	
XA432	TQ2	D3	24B	TEB4SA	00	=		
XA432	TQ2	D3	22B	(45 )	01		TEB4S0 TXDV1B 41 22B 43 23B	
XA428	TQ2	E3	30B	TEB4S0	00	=		
XA428	TQ2	E3	28B	(57 )	01		TXR2CR TXRAF5T 53 28B 55 29B	
XA540	EOR	A1	04B	TEB49ER	00	=		
XA540	EOR	A1	03B	(05 )	01		TEB4BQ TXDEV 03 03B 01 02B	
				TEB5BI	00	=		
XA434	TDD	KI	29A	( )	01		TEB590 62 29A	
				TEB5BN	00	=		
XA434	TDD	KN	28A	( )	01		TEB5KA 60 28A	
XA434	TDD	KP	30B	TEB5BP	00	=		
XA434	TDD	KP	30A	(57 )	01		TEB4RA 64 30A	
XA434	TDD	KQ	29B	TEB5BQ	00	=		
XA434	TDD	KQ	28B	(55 )	01		TEB5SA 53 28B	EOB COUNTER BYT 5

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM DESIGNATION	FACTOR	COMMENT
XA409	TT3	A3	07B	TEB5KA	00 =		EOB COUNTER CLOCK
XA409	TT3	A3	04B	(15)	01	YXEB1Q TEBZRA TXCP30 09 04B 11 05B 13 06B	
XA430	TT3	B3	13B	TEB5RA	00 =		
XA430	TT3	B3	10B	(27)	01	YXDEVS YXXB1P SPI014 21 10B 23 11B 25 12B	
XA426	TQ2	F2	34A	TEB5SA	00 =		
XA426	TQ2	F2	36A	(72)	01	TEB5S0 TXDV1B 71 36A 73 36B	
XA428	TQ2	E4	33B	TEB5S0	00 =		
XA428	TQ2	E4	31B	(63)	01	YXR3CR TXRAF5T 59 31B 61 32B	
XA428	TQ2	D3	24B	TEB590	00 =		
XA428	TQ2	D3	22B	(45)	01	TEB5BQ SPI010 41 22B 43 23B	
XA522	TQ2	D3	24B	TE0TCA	00 =		
XA522	TQ2	D3	22B	(45)	01	TE0T10X SPI022 41 22B 43 23B	
XA422	MUX	C1	24A	TE0TX1X	00 =		SPI008 50 24A
XA522	TQ2	C1	18A	TE0TOA	00 =		
XA522	TQ2	C1	19A	(38)	01	TE0T10X TSCL3B 40 19A 42 20A	
XA522	TQ2	B3	10B	TE0TOR	00 =		
XA522	TQ2	B3	08B	(21)	01	TE0T0S TE0T1A 17 08B 19 09B	
XA523	TQ2	B3	10B	TE0T0S	00 =		END OF TAPE (EOT) F/F
XA523	TQ2	B3	08B	(21)	01	TE0TOR TE0TOA 17 08B 19 09B	
XA525	TD4	B2	10B	TE0T1A	00 =		
XA525	TD4	B2	09A	(21)	01	TE0TCA YSTPOR UE0T1R TSCL3B 14 09A 18 10A 19 09B 20 11A	
XA545	DCF	B1	10B	TE0T10X4	00 =		EOT 1 RECEIVER
XA545	DCF	B1	13A	(27)	01	SPI029 36 13A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA545	DCF	B2	11B	TEOT10X	00	=		
XA545	DCF	B2	12B	(29)	01		SPIO28 31 12B	
XA515	TD4	B2	10B	TFPE1A	00	=		
XA515	TD4	B2	09A	(21)	01		TWENCO TWDBSA TSNC2S TSCK3B 14 09A 18 10A 19 09B 20 11A	
XA519	TQ2	B3	10B	TFPE2A	00	=		
XA519	TQ2	B3	08B	(21)	01		TWENCO TWDBSA 17 08B 19 09B	
XA545	DCF	C1	25B	TFPR1DX4	00	=		FILE PROJECT 1 RECEIVER
XA545	DCF	C1	29A	(46)	01		SPIO29 52 29A	
XA545	DCF	C2	26B	TFPR10X	00	=		
XA545	DCF	C2	27B	(47)	01		SPIO28 49 27B	
XA406	TQ2	F4	39A	TFSTRA	00	=		
XA406	TQ2	F4	37A	(80)	01		TFSTOS TFST1R 76 37A 78 38A	
XA418	TS8	F1	37A	TFSTOA	00	=		FLYING START
XA418	TS8	F1	36A	(76)	01		TSNC2S TBUSYS SPIO01 TDSC00 TSTPOR TSTP1R TSTP2R TSCK3B 71 36A 72 34A 73 36B 74 35B 75 37B 77 38B 78 38A 79 39B	
XA409	TT3	D1	23A	TFSTOR	00	=		
XA409	TT3	D1	24A	(50)	01		TFSTOS TFST1A TBUSYS 52 24A 54 25A 56 26A	
XA410	TD4	E2	30B	TFSTOS	00	=		FLYING START COUNTER BIT 0
XA410	TD4	E2	29B	(57)	01		TFSTOR TFSTOA TNSGKA SPIO153 55 29B 60 28A 62 29A 64 30A	
XA408	TQ2	E3	30B	TFST1A	00	=		
XA408	TQ2	E3	28B	(57)	01		TFST2S TSCK3B 53 28B 55 29B	
XA408	TQ2	F2	34A	TFST1R	00	=		
XA408	TQ2	F2	36A	(72)	01		TFST1S TFSTOS 71 36A 73 36B	
XA407	TQ2	F2	34A	TFST1S	00	=		FLYING START COUNTER BIT 1
XA407	TQ2	F2	36A	(72)	01		TFST1R TFST2A 71 36A 73 36B	



CONNECTOR	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA409	TT3	D2 23B	TFST2A	00	=		
XA409	TT3	D2 22B	(43)	01		TFST05 TCZR00 TSCK1B 41 22B 46 21A 48 22A	
XA409	TT3	F3 39A	TFST2R	00	=		
XA409	TT3	F3 35A	(80)	01		TFST2S TFST3A TXRS1B 69 35A 76 37A 78 38A	
XA407	TQ2	F3 35A	TFST2S	00	=		
XA407	TQ2	F3 34B	(69)	01		TFST2R TFST4A 65 34B 74 35B	FLYING START COUNTER BIT 2
XA406	TQ2	F2 34A	TFST3A	00	=		
XA406	TQ2	F2 36A	(72)	01		TFST1R TSCK1B 71 36A 73 36B	
XA409	TT3	D3 27B	TFST4A	00	=		
XA409	TT3	D3 24B	(51)	01		TFST1S T033M0 TSCK1B 45 24B 47 25B 49 26B	
XA508	TD4	C2 16B	TFST6A	00	=		
XA508	TD4	C2 15A	(33)	01		THISPQ TWRG2S TSCK3B SPI020 30 15A 31 15B 34 16A 36 17A	
			THISPI	00	=		
XA425	TDD	E1 19A	( )	01		THIS10 40 19A	
			THISPN	00	=		
XA425	TDD	EN 20A	( )	01		TXDV1B 42 20A	
XA425	TDD	EP 17B	THISPP	00	=		
XA425	TDD	EP 18A	(35)	01		TRDCAB 38 18A	
XA425	TDD	EQ 18B	THISPQ	00	=		
XA425	TDD	EQ 19B	(37)	01		SPI007 39 19B	NEW SPEED F/F
XA426	TQ2	D1 24A	THIS10	00	=		
XA426	TQ2	D1 25A	(52)	01		TXR092T TXRAF2T 54 25A 56 26A	
XA524	TT3	E2 29B	THSPRA	00	=		
XA524	TT3	E2 28B	(55)	01		T030M0 THSPRQ TSCL1B 53 28B 60 28A 62 29A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
				THSPRI	00	=		
XA514	TDD	KI	29A	( )	01		TXGN2A 62 29A	
				THSPRN	00	=		
XA514	TDD	KN	28A	( )	01		THSPRA 60 28A	
XA514	TDD	KP	30B	THSPRP	00	=		
XA514	TDD	KP	30A	(57 )	01		TBUSYS 64 30A	
XA514	TDD	KQ	29B	THSPRQ	00	=		
XA514	TDD	KQ	28B	(55 )	01		TFST6A 53 28B	
XA505	TQ2	F4	39A	TINT1A	00	=		
XA505	TQ2	F4	37A	(80 )	01		TINT10 SPI018 76 37A 78 38A	
XA503	TS8	F1	37A	TINT10	00	=		COMMAND END AND INTERRUPT
XA503	TS8	F1	36A	(76 )	01		TSCI1A TRWS2A TNSGDA TNSGMA TNSGAA TRWS4A TNDASA SPI012 71 36A 72 34A 73 36B 74 35B 75 37B 77 38B 78 38A 79 39B	
XA508	TD4	F2	35A	TINT2A	00	=		
XA508	TD4	F2	36A	(69 )	01		TINT10 TREDBP TREWDP TTASLP 71 36A 72 34A 73 36B 74 35B	
XA509	TT3	D2	23B	TINT5A	00	=		
XA509	TT3	D2	22B	(43 )	01		TINT50 TCSDQQ TRW14A 41 22B 46 21A 48 22A	
XA506	TQ2	B4	13B	TINT50	00	=		BOT OR EOT END AND INTERRUPT
XA506	TQ2	B4	11B	(27 )	01		UINT7A TINT9A 23 11B 25 12B	
XA502	TS8	F1	37A	TINT6A	00	=		
XA502	TS8	F1	36A	(76 )	01		TINT50 TCSDQQ TRW14A TREDBP TREWDP TTASLP TLAD2R SPI018 71 36A 72 34A 73 36B 74 35B 75 37B 77 38B 78 38A 79 39B	
XA509	TT3	E2	29B	TINT9A	00	=		
XA509	TT3	E2	28B	(55 )	01		TBOT1S TDIRIR TSCL3B 53 28B 60 28A 62 29A	
				TKA00I	00	=		
XA513	TDD	GI	25A	( )	01		SPI020 54 25A	

CONNECTOR	CARD CAGE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA513	TDD	GN	26A	TKA00N ( )	00 =	01	TKA02P 56 26A	
XA513	TDD	GP	25B	TKA00P ( )	00 =	01	TKR50A 52 24A	
XA513	TDD	GP	24A	(47)				
XA513	TDD	GQ	26B	TKA00Q ( )	00 =	01	TXRS1B 51 27B	BYTE TIMING COUNT CONTROL E/F
XA513	TDD	GQ	27B	(49)				
XA514	TDD	GI	25A	TKA01I ( )	00 =	01	TKA02P 54 25A	
XA514	TDD	GN	26A	TKA01N ( )	00 =	01	TSCL1B 56 26A	
XA514	TDD	GP	25B	TKA01P ( )	00 =	01	TKA00P 52 24A	
XA514	TDD	GP	24A	(47)				
XA514	TDD	GQ	26B	TKA01Q ( )	00 =	01	SPI020 51 27B	BYTE TIMING COUNTER BIT 1
XA514	TDD	GQ	27B	(49)				
XA412	TQ2	F1	37B	TKA010 ( )	00 =	01	TKA01P SPI004 77 38B 79 39B	
XA412	TQ2	F1	38B	(75)				
XA526	TDD	J1	32A	TKA02I ( )	00 =	01	TKA01Q 68 32A	
XA526	TDD	JN	33A	TKA02N ( )	00 =	01	TSCL1B 70 33A	
XA526	TDD	JP	31B	TKA02P ( )	00 =	01	TKA00P 66 31A	
XA526	TDD	JP	31A	(59)				
XA526	TDD	JQ	32B	TKA02Q ( )	00 =	01	SPI019 63 33B	BUFFER TIMING COUNTER BIT 2
XA526	TDD	JQ	33B	(61)				

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
			AND	OR					
XA505	TQ2	E3	30B		TKBCPO	00	=		
XA505	TQ2	E3	28B		(57)	01		TKA02Q TMSGIR 53 28B 55 29B	
XA505	TQ2	E2	28A		TKBRSA	00	=		
XA505	TQ2	E2	29A		(60)	01		TKBRSD SPI018 62 29A 64 30A	
XA508	T04	E2	30B		TKBRSD	00	=		
XA508	T04	E2	29B		(57)	01		TRENOA TNSGOA TNSG5A TXRS2B 55 29B 60 28A 62 29A 64 30A	
					TKB00I	00	=		
XA513	TDD	HI	22A		( )	01		TKB01P 48 22A	
					TKB00N	00	=		
XA513	TDD	HN	21A		( )	01		TKBCPO 46 21A	
XA513	TDD	HP	24B		TKB00P	00	=		
XA513	TDD	HP	23A		(45)	01		TKBRSA 50 23A	
XA513	TDD	HQ	23B		TKB00Q	00	=		BYTE COUNTER BIT 0
XA513	TDD	HQ	22B		(43)	01		SPI019 41 22B	
					TKB01I	00	=		
XA514	TDD	HI	22A		( )	01		TKB00Q 48 22A	
					TKB01N	00	=		
XA514	TDD	HN	21A		( )	01		TKBCPO 46 21A	
XA514	TDD	HP	24B		TKB01P	00	=		
XA514	TDD	HP	23A		(45)	01		TKBRSA 50 23A	
XA514	TDD	HQ	23B		TKB01Q	00	=		BYTE COUNTER BIT 1
XA514	TDD	HQ	22B		(43)	01		SPI019 41 22B	
					TKC00I	00	=		
XA514	TDD	JI	32A		( )	01		TKC02P 68 32A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIG- NATOR	FACTOR	COMMENT
				TKCOON	00 =			
XA514	TDD	JN	33A	( )	01		TKBCPO 70 33A	
XA514	TDD	JP	31B	TKCOOP	00 =			
XA514	TDD	JP	31A	(59 )	01		TKBRSA 66 31A	
XA514	TDD	JQ	32B	TKC00Q	00 =			FIRST BYTE COUNTERBIT 0
XA514	TDD	JQ	33B	(61 )	01		SP1020 63 33B	
				TKC01I	00 =			
XA513	TDD	JI	32A	( )	01		TKC00Q 68 32A	
				TKC01N	00 =			
XA513	TDD	JN	33A	( )	01		TKBCPO 70 33A	
XA513	TDD	JP	31B	TKC01P	00 =			
XA513	TDD	JP	31A	(59 )	01		TKBRSA 66 31A	
XA513	TDD	JQ	32B	TKC01Q	00 =			FIRST BYTE COUNTERBIT 1
XA513	TDD	JQ	33B	(61 )	01		SP1020 63 33B	
				TKC02I	00 =			
XA513	TDD	KI	29A	( )	01		TKC290 62 29A	
				TKC02N	00 =			
XA513	TDD	KN	28A	( )	01		TKBCPO 60 28A	
XA513	TDD	KP	30B	TKC02P	00 =			
XA513	TDD	KP	30A	(57 )	01		TKBRSA 64 30A	
XA513	TDD	KQ	29B	TKC02Q	00 =			FIRST BYTE COUNTERBIT 2
XA513	TDD	KQ	28B	(55 )	01		SP1019 53 28B	
XA412	TQ2	F2	34A	TKC29A	00 =			
XA412	TQ2	F2	36A	(72 )	01		TKC01P TKC02Q 71 36A 73 36B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
			AND	OR					
XA406	TQ2	D3	24B		TKC290	00	N		
XA406	TQ2	D3	22B		(45)	01		TKC01P TKC29A 41 22B 43 23B	
XA505	TQ2	D4	27B		TKRS0A	00	=		START BYTE TIMING COUNTER
XA505	TQ2	D4	25B		(51)	01		TKRS00 SPI018 47 25B 49 26B	
XA506	TQ2	D1	24A		TKRS00	00	=		
XA506	TQ2	D1	25A		(52)	01		TT0BPB TKRS1A 54 25A 56 26A	
XA505	TQ2	D2	21A		TKRS1A	00	=		
XA505	TQ2	D2	22A		(46)	01		TKRS10 SPI018 48 22A 50 23A	
XA503	TS8	E1	31B		TKRS10	00	=		
XA503	TS8	E1	29B		(59)	01		TT00BP TT01BP TT02BP TT03BP TT04BP TT05BP TT06BP TT07BP 55 29B 60 28A 61 32B 62 29A 64 30A 66 31A 68 32A 70 33A	
XA407	TQ2	F4	39A		TLADRA	00	=		
XA407	TQ2	F4	37A		(80)	01		TLAD0S TLAD1R 76 37A 78 38A	
XA509	TT3	F1	36B		TLAD0A	00	=		
XA509	TT3	F1	37B		(73)	01		TLAD0S TCZR00 TSCK1B 75 37B 77 38B 79 39B	
XA509	TT3	F2	35B		TLAD0R	00	=		
XA509	TT3	F2	34B		(74)	01		TLAD0S TLAD1A TXRS2B 65 34B 71 36A 72 34A	
XA510	TT3	F2	35B		TLAD0S	00	=		LOOK AHEAD DELAY COUNT BIT 0
XA510	TT3	F2	34B		(74)	01		TLAD0R TINT1A TINT5A 65 34B 71 36A 72 34A	
XA506	TQ2	C4	19B		TLAD1A	00	=		
XA506	TQ2	C4	17B		(39)	01		TLAD2S TSCL3B 35 17B 37 18B	
XA506	TQ2	C2	15A		TLAD1R	00	=		
XA506	TQ2	C2	16A		(30)	01		TLAD1S TLAD0S 34 16A 36 17A	
XA505	TQ2	C3	16B		TLAD1S	00	=		LOOK AHEAD DELAY COUNT BIT 1
XA505	TQ2	C3	14B		(33)	01		TLAD1R TLAD0A 29 14B 31 15B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA509	TT3	D1	23A	TLAD2A	00	=		
XA509	TT3	D1	24A	(50)	01		TLAD1S T003M0 TSCK1B 52 24A 54 25A 56 26A	
XA506	TQ2	A1	05A	TLAD2R	00	=		
XA506	TQ2	A1	06A	(06)	01		TLAD2S TSNC1A 08 06A 10 07A	
XA518	TT3	E3	33B	TLAD2S	00	=		LOOK AHEAD DELAY COUNT BIT 2
XA518	TT3	E3	30B	(63)	01		TLAD2R TLAD2A TXRS1B 57 30B 59 31B 61 32B	
XA505	TQ2	C4	19B	TLAD3A	00	=		
XA505	TQ2	C4	17B	(39)	01		TLAD1R TSCK1B 35 17B 37 18B	
XA509	TT3	C1	17A	TLAD3R	00	=		
XA509	TT3	C1	18A	(36)	01		TLAD3S TLAD3A TXRS1B 38 18A 40 19A 42 20A	
XA506	TQ2	A2	02B	TLAD3S	00	=		LOOK AHEAD DELAY COUNT BIT 3
XA506	TQ2	A2	04A	(01)	01		TLAD3R TLAD2A 04 04A 05 03B	
XA515	TD4	A1	05B	TLCCOA	00	=		
XA515	TD4	A1	05A	(11)	01		TWRITQ TXEB1Q TEBZRO TSCK3B 06 05A 08 06A 10 07A 13 06B	
XA518	TT3	C1	17A	TLCCOR	00	=		
XA518	TT3	C1	18A	(36)	01		TLCCOS TLCC1A TXRS2B 38 18A 40 19A 42 20A	
XA517	TQ2	D3	24B	TLCCOS	00	=		WRITE LRC CHAR COUNT BIT 0
XA517	TQ2	D3	22B	(45)	01		TLCCOR TLCCOA 41 22B 43 23B	
XA512	TQ2	B4	13B	TLCC1A	00	=		
XA512	TQ2	B4	11B	(27)	01		TLCC3S TSCK3B 23 11B 25 12B	
XA524	TT3	E3	33B	TLCC1R	00	=		
XA524	TT3	E3	30B	(63)	01		TLCC1S TLCC1A TXRS2B 57 30B 59 31B 61 32B	
XA525	TD4	E2	30B	TLCC1S	00	=		WRITE LRC CHAR COUNT BIT 1
XA525	TD4	E2	29B	(57)	01		TLCC1R TLCC2A SPI022 SPI021 55 29B 60 28A 62 29A 64 30A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERN	DESIGNATOR	FACTOR	COMMENT
XA515	TD4	F2	35A	TLCC2A	00	=		
XA515	TD4	F2	36A	(69)	01		TLCC05 TWRG1R TWRI0R TSCL3B 71 36A 72 34A 73 36B 74 35B	
XA511	TQ2	E4	33B	TLCC2R	00	=		
XA511	TQ2	E4	31B	(63)	01		TLCC2S TLCC1S 59 31B 61 32B	
XA512	TQ2	E3	30B	TLCC2S	00	=		WRITE LRC CHAR COUNT BIT 2
XA512	TQ2	E3	28B	(57)	01		TLCC2R TLCC4A 53 28B 55 29B	
XA512	TQ2	E4	33B	TLCC3A	00	=		
XA512	TQ2	E4	31B	(63)	01		TLCC2R TSCK1B 59 31B 61 32B	
XA510	TT3	E1	30A	TLCC3R	00	=		
XA510	TT3	E1	31A	(64)	01		TLCC3S TLCC3A TXRS2B 66 31A 68 32A 70 33A	
XA511	TQ2	E3	30B	TLCC3S	00	=		WRITE LRC CHAR COUNT BIT 3
XA511	TQ2	E3	28B	(57)	01		TLCC3R TLCC6A 53 28B 55 29B	
XA518	TT3	D2	23B	TLCC4A	00	=		
XA518	TT3	D2	22B	(43)	01		TLCC1S TCZR70 TSCL1B 41 22B 46 21A 48 22A	
XA518	TT3	D3	27B	TLCC6A	00	=		
XA518	TT3	D3	24B	(51)	01		TLCC2S T399U0 TSCL1B 45 24B 47 25B 49 26B	
XA524	TT3	B1	11A	TLPTBR	00	=		
XA524	TT3	B1	12A	(20)	01		TLPTBS TXXDDP TXRS2B 22 12A 24 13A 26 14A	
XA523	TQ2	B1	12A	TLPTBS	00	=		LOOP TEST BUSY F/E
XA523	TQ2	B1	13A	(22)	01		TLPTBR TXOD3A 24 13A 26 14A	
				TLPTOI	00	=		
XA413	TDD	MI	36A	( )	01		TXGN4A 71 36A	
				TLPTON	00	=		
XA413	TDD	MN	34A	( )	01		TLPT1A 72 34A	



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	INDICATOR	FACTOR	COMMENT
XA413	TDD	MP	35A	TLPTOP	00	=		
XA413	TDD	MP	36B	(69)	01		TXRS2B 73 36B	
XA413	TDD	MQ	35B	TLPTOQ	00	=		LOOP TEST COUNT BIT 0
XA413	TDD	MQ	34B	(74)	01		TXOD3A 65 34B	
XA522	TQ2	D2	21A	TLPT1A	00	=		
XA522	TQ2	D2	22A	(48)	01		TLPT1Q TXCP30 48 22A 50 23A	
XA414	TDD	MI	36A	TLPT1I	00	=		
XA414	TDD	MI	36A	( )	01		TLPTOQ 71 36A	
XA414	TDD	MN	34A	TLPT1N	00	=		
XA414	TDD	MN	34A	( )	01		TXCP10 72 34A	
XA414	TDD	MP	35A	TLPT1P	00	=		
XA414	TDD	MP	36B	(69)	01		TXRS2B 73 36B	
XA414	TDD	MQ	35B	TLPT1Q	00	=		LOOP TEST COUNT BIT 1
XA414	TDD	MQ	34B	(74)	01		SPI005 65 34B	
XA415	TDD	MI	36A	TLPT2I	00	=		
XA415	TDD	MI	36A	( )	01		TXGN4A 71 36A	
XA415	TDD	MN	34A	TLPT2N	00	=		
XA415	TDD	MN	34A	( )	01		TLPT5A 72 34A	
XA415	TDD	MP	35A	TLPT2P	00	=		
XA415	TDD	MP	36B	(69)	01		TXRS2B 73 36B	
XA415	TDD	MQ	35B	TLPT2Q	00	=		LOOP TEST COUNT BIT 2
XA415	TDD	MQ	34B	(74)	01		TLPT1A 65 34B	
XA416	TDD	MI	36A	TLPT3I	00	=		
XA416	TDD	MI	36A	( )	01		TLPT2Q 71 36A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
				TLPT3N	00	=		
XA416	TDD	MN	34A	( )	01		TXCP10 72 34A	
XA416	TDD	MP	35A	TLPT3P	00	=		
XA416	TDD	MP	36B	(69 )	01		TXRS2B 73 36B	
XA416	TDD	MQ	35B	TLPT3Q	00	=		LOOP TEST COUNT BIT 3
XA416	TDD	MO	34B	(74 )	01		SPI005 65 34B	
XA524	TT3	A3	07B	TLPT4R	00	=		
XA524	TT3	A3	04B	(15 )	01		TLPT4S TXDDP TXRS2B 09 04B 11 05B 13 06B	
XA523	TQ2	A3	04B	TLPT4S	00	=		LOOP TEST COUNT BIT 4
XA523	TQ2	A3	02A	(09 )	01		TLPT4R TLPT5A 03 02A 07 03A	
XA432	TQ2	B4	13B	TLPT5A	00	=		
XA432	TQ2	B4	11B	(27 )	01		TLPT3Q TXCP30 23 11B 25 12B	
XA507	TS8	D1	25B	TLRCAA	00	=		
XA507	TS8	D1	23B	(47 )	01		TLRC0Q TLRC1Q TLRC2Q TLRC3Q TLRC4Q TLRC5Q TLRC6Q TLRC7Q 43 23B 46 21A 48 22A 49 26B 50 23A 52 24A 54 25A 56 26A	
XA407	TQ2	E3	30B	TLRCA0	00	=		
XA407	TQ2	E3	28B	(57 )	01		TLRCAA SPI001 53 28B 55 29B	
XA408	TQ2	E4	33B	TLRCBA	00	=		LRC REG ALL ONE
XA408	TQ2	E4	31B	(63 )	01		TLRCPQ TLRCA0 59 31B 61 32B	
XA505	TQ2	C1	18A	TLRCB0	00	=		
XA505	TQ2	C1	19A	(38 )	01		TLRCBA SPI018 40 19A 42 20A	
XA518	TT3	D1	23A	TLRCCA	00	=		
XA518	TT3	D1	24A	(50 )	01		TLRCC0 TSNC1S TSCL1B 52 24A 54 25A 56 26A	
XA515	TD4	A2	04B	TLRCC0	00	=		
XA515	TD4	A2	02B	(09 )	01		TREADP TSPACP TWENCA TSPAFP 01 02B 04 04A 05 03B 07 03A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TIER	DESIGNATOR	FACTOR	COMMENT
XA540	EOR	D4	23A	TLRCPER	00	=		
XA540	EOR	D4	22A	(50)	01		TLRCPO SPI026 48 22A 46 21A	
				TLRCPI	00	=		
XA541	TDD	D1	10A	( )	01		TLRCPER 18 10A	
				TLRCPN	00	=		
XA541	TDD	DN	09A	( )	01		TTOPBO 14 09A	
XA541	TDD	DP	10B	TLRCPP	00	=		
XA541	TDD	DP	11A	(21)	01		SPI028 20 11A	
XA541	TDD	D0	09B	TLRCPO	00	=		LRC REG BIT P
XA541	TDD	D0	08B	(19)	01		TLRCSA 17 08B	
XA512	TQ2	F1	37B	TLRCRA	00	=		
XA512	TQ2	F1	38B	(75)	01		TLCCIS TLCC2R 77 38B 79 39B	
XA517	TQ2	C1	18A	TLRCSA	00	=		
XA517	TQ2	C1	19A	(38)	01		TLRCSO SPI021 40 19A 42 20A	
XA516	TQ2	D2	21A	TLRCSO	00	=		
XA516	TQ2	D2	22A	(46)	01		TLRCCA TXRS1B 48 22A 50 23A	
XA540	EOR	E1	30B	TLRCOER	00	=		
XA540	EOR	E1	29B	(59)	01		TLRCOQ SPI026 55 29B 53 28B	
				TLRCOI	00	=		
XA541	TDD	E1	19A	( )	01		TLRCOER 40 19A	
				TLRCON	00	=		
XA541	TDD	EN	20A	( )	01		TT00BQ 42 20A	
XA541	TDD	EP	17B	TLRCOP	00	=		
XA541	TDD	EP	18A	(35)	01		SPI026 38 18A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TRYS	DESIGNATOR	FACTOR	COMMENT
XA541	TDD	EQ	18B	TLRC0Q	00	=		
XA541	TDD	EQ	19B	(37)	01		TLRCSA 39 19B	LRC REG BIT 0
XA540	EOR	E2	33B	TLRC1ER	00	=		
XA540	EOR	E2	32B	(65)	01		TLRC1Q SPI026 63 32B 61 31B	
XA541	TDD	FI	16A	TLRC1I	00	=		
				( )	01		TLRC1ER 34 16A	
XA541	TDD	FN	15A	TLRC1N	00	=		
				( )	01		TT01BQ 30 15A	
XA541	TDD	FP	16B	TLRC1P	00	=		
XA541	TDD	FP	17A	(33)	01		SPI028 36 17A	
XA541	TDD	FQ	15B	TLRC1C	00	=		
XA541	TDD	FQ	14B	(31)	01		TLRCSA 29 14B	LRC REG BIT 1
XA540	EOR	E3	33A	TLRC2ER	00	=		
XA540	EOR	E3	32A	(68)	01		TLRC2Q SPI026 66 32A 64 31A	
XA541	TDD	GI	25A	TLRC2I	00	=		
				( )	01		TLRC2ER 54 25A	
XA541	TDD	GN	26A	TLRC2N	00	=		
				( )	01		TT02BQ 56 26A	
XA541	TDD	GP	25B	TLRC2P	00	=		
XA541	TDD	GP	24A	(47)	01		SPI026 52 24A	
XA541	TDD	GQ	26B	TLRC2Q	00	=		
XA541	TDD	GQ	27B	(49)	01		TLRCSA 51 27B	LRC REG BIT 2
XA540	EOR	E4	30A	TLRC3ER	00	=		
XA540	EOR	E4	29A	(62)	01		TLRC3Q SPI026 60 29A 57 28A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TIER	DESIGNATOR	FACTOR	COMMENT
XA541	TDD	HI	22A	TLRC3I ( )	00	=	TLRC3ER 48 22A	
XA541	TDD	HN	21A	TLRC3N ( )	00	=	TT03BQ 46 21A	
XA541	TDD	HP	24B	TLRC3P (45 )	00	=	SPI028 50 23A	
XA541	TDD	HP	23A		01			
XA541	TDD	HQ	23B	TLRC3Q (43 )	00	=	TLRC5A 41 22B	LRC REG BIT 3
XA541	TDD	HQ	22B		01			
XA540	EOR	F1	36B	TLRC4ER (73 )	00	=	TLRC4Q SPI026 71 35B 69 34B	
XA540	EOR	F1	35B		01			
XA541	TDD	JI	32A	TLRC4I ( )	00	=	TLRC4ER 68 32A	
XA541	TDD	JN	33A	TLRC4N ( )	00	=	TT04BQ 70 33A	
XA541	TDD	JP	31B	TLRC4P (59 )	00	=	SPI026 66 31A	
XA541	TDD	JP	31A		01			
XA541	TDD	JQ	32B	TLRC4Q (61 )	00	=	TLRC5A 63 33B	LRC REG BIT 4
XA541	TDD	JQ	33B		01			
XA540	EOR	F2	39B	TLRC5ER (79 )	00	=	TLRC5Q SPI026 77 38B 75 37B	
XA540	EOR	F2	38B		01			
XA541	TDD	KI	29A	TLRC5I ( )	00	=	TLRC5ER 62 29A	
XA541	TDD	KN	28A	TLRC5N ( )	00	=	TT05BQ 60 28A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	REG. MATOR	FACTOR	COMMENT
XA541	TDD	KP	30B	TLRC5P	00	=		
XA541	TDD	KP	30A	(57)	01		SPI028 64 30A	
XA541	TDD	KQ	29B	TLRC5Q	00	=		LRC REG BIT 5
XA541	TDD	KQ	28B	(55)	01		TLRCSA 53 28B	
XA540	EOR	F3	39A	TLRC6ER	00	=		
XA540	EOR	F3	38A	(80)	01		TLRC6Q SPI026 78 38A 76 37A	
XA541	TDD	LI	38B	TLRC6I	00	=		
				( )	01		TLRC6ER 77 38B	
XA541	TDD	LN	39B	TLRC6N	00	=		
				( )	01		TT06BQ 79 39B	
XA541	TDD	LP	37A	TLRC6P	00	=		
XA541	TDD	LP	37B	(76)	01		SPI026 75 37B	
XA541	TDD	LQ	38A	TLRC6Q	00	=		LRC REG BIT 6
XA541	TDD	LQ	39A	(78)	01		TLRCSA 80 39A	
XA541	TDD	MI	36A	TLRC7I	00	=		
				( )	01		TLRC7O 71 36A	
XA541	TDD	MN	34A	TLRC7N	00	=		
				( )	01		TT07BQ 72 34A	
XA541	TDD	MP	35A	TLRC7P	00	=		
XA541	TDD	MP	36B	(69)	01		SPI028 73 36B	
XA541	TDD	MQ	35B	TLRC7Q	00	=		LRC REG BIT 7
XA541	TDD	MQ	34B	(74)	01		TLRCSA 65 34B	
XA428	TQ2	D1	24A	TLRC7O	00	=		
XA428	TQ2	D1	25A	(52)	01		TLRC7O SPI010 54 25A 56 26A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TRIP	DESIGNATOR	FACTOR	COMMENT
XA516	TQ2	E2	28A	TLTEOA	00	=		
XA516	TQ2	E2	29A	(60)	01		TLPT45 TXEA00 62 29A 64 30A	
XA515	TD4	E2	30B	TMCP0A	00	=		
XA515	TD4	E2	29B	(57)	01		TS29CP TM03BQ TM04BQ TSCL3B 55 29B 60 28A 62 29A 64 30A	
XA517	TQ2	E2	28A	TMCP00	00	=		
XA517	TQ2	E2	29A	(60)	01		TMCP0A SPI021 62 29A 64 30A	
XA517	TQ2	E1	31A	TMRS0A	00	=		
XA517	TQ2	E1	32A	(66)	01		TMRS00 SPI021 68 32A 70 33A	
XA524	TI3	E1	30A	TMRS00	00	=		
XA524	TI3	E1	31A	(64)	01		TRENOA TXOD3A TXRS1B 66 31A 68 32A 70 33A	
XA514	TDD	FI	16A	TMWCBI	00	=		
XA514	TDD	FI	( )	( )	01		TM02BQ 34 16A	
XA514	TDD	FN	15A	TMWCBN	00	=		
XA514	TDD	FN	( )	( )	01		TMCP00 30 15A	
XA514	TDD	FP	16B	TMWCBP	00	=		
XA514	TDD	FP	17A	(33)	01		TMRS0A 36 17A	
XA514	TDD	FQ	15B	TMWC8Q	00	=		BUFFER REG FULL F/F
XA514	TDD	FQ	14B	(31)	01		SPI019 29 14B	
XA511	TQ2	A4	07B	TMOCPA	00	=		
XA511	TQ2	A4	05B	(15)	01		TM02BQ TM04BQ 11 05B 13 06B	
XA513	TDD	E1	19A	TM00BI	00	=		
XA513	TDD	E1	( )	( )	01		TXGN1A 40 19A	
XA513	TDD	EN	20A	TM00BN	00	=		
XA513	TDD	EN	( )	( )	01		TMOCPA 42 20A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS ANCHOR	EQUATION	TERM NO.	DESIG- NATOR	FACTOR	COMMENT
XA513	TDD	EP	17B	TMO0BP	00	=		
XA513	TDD	EP	18A	(35)	01		TXRS1B 38 18A	
XA513	TDD	EQ	18B	TMO0BQ	00	=		BUFFER INPUT CONTROL E/E
XA513	TDD	EQ	19B	(37)	01		TMO0SA 39 19B	
XA512	TQ2	A2	02B	TMO0SA	00	=		
XA512	TQ2	A2	04A	(01)	01		TMO0S0 SPI020 04 04A 05 03B	
XA511	TQ2	A2	02B	TMO0S0	00	=		
XA511	TQ2	A2	04A	(01)	01		TRMROA TWRQSA 04 04A 05 03B	
				TMO1BI	00	=		
XA513	TDD	FI	16A	( )	01		TXGN1A 34 16A	
				TMO1BN	00	=		
XA513	TDD	FN	15A	( )	01		TMICPA 30 15A	
XA513	TDD	FP	16B	TMO1BP	00	=		
XA513	TDD	FP	17A	(33)	01		TXRS1B 36 17A	
XA513	TDD	FQ	15B	TMO1BQ	00	=		BUFFER OUTPUT CONTROL E/E
XA513	TDD	FQ	14B	(31)	01		TMO1SA 29 14B	
XA512	TQ2	A3	04B	TMO1SA	00	=		
XA512	TQ2	A3	02A	(09)	01		TMO1S0 SPI020 03 02A 07 03A	
XA511	TQ2	A3	04B	TMO1S0	00	=		
XA511	TQ2	A3	02A	(09)	01		TREQ2A TWR12A 03 02A 07 03A	
				TMO2BI	00	=		
XA526	TDD	MI	36A	( )	01		TMO0BQ 71 36A	
				TMO2BN	00	=		
XA526	TDD	MN	34A	( )	01		TMO3BQ 72 34A	



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA526	TDD	MP	35A	TMO2BP	00	=		
XA526	TDD	MP	36B	( 69 )	01		SPT022 73 36B	
XA526	TDD	MQ	35B	TMO2BQ	00	=		BYTE TIMING COUNTER BIT 2
XA526	TDD	MQ	34B	( 74 )	01		SPT019 65 34B	
XA514	TDD	EI	19A	TMO3BI	00	=		
				( )	01		TMO4BP 40 19A	
XA514	TDD	EN	20A	TMO3BN	00	=		
				( )	01		TSCLIB 42 20A	
XA514	TDD	EP	17B	TMO3BP	00	=		
XA514	TDD	EP	18A	( 35 )	01		TMO3RA 38 18A	
XA514	TDD	EQ	18B	TMO3BQ	00	=		BUFFER TIMING COUNTER BIT 3
XA514	TDD	EQ	19B	( 37 )	01		SPT020 39 19B	
XA511	TQ2	B1	12A	TMO3RA	00	=		
XA511	TQ2	B1	13A	( 22 )	01		TMO0BP TMO1BP 24 13A 26 14A	
XA526	TDD	KI	29A	TMO4BI	00	=		
				( )	01		TMO3BQ 62 29A	
XA526	TDD	KN	28A	TMO4BN	00	=		
				( )	01		TSCLIB 60 28A	
XA526	TDD	KP	30B	TMO4BP	00	=		
XA526	TDD	KP	30A	( 57 )	01		TMO3RA 64 30A	
XA526	TDD	KQ	29B	TMO4BQ	00	=		BUFFER TIMING COUNTER BIT 4
XA526	TDD	KQ	28B	( 55 )	01		SPT023 53 28B	
XA512	TQ2	A4	07B	TM1CPA	00	=		
XA512	TQ2	A4	05B	( 15 )	01		TMO2BP TMO4BQ 11 05B 13 06B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OH	EQUATION	TERMINATOR	DESIGNATOR	FACTOR	COMMENT
XA509	TT3	D3	27B	TNDASA	00	=		
XA509	TT3	D3	24B	(51)	01		TNDA10 TRW14A TNDA3A 45 24B 47 25B 49 26B	NO DATA ERROR
XA515	TD4	D2	24B	TNDA1A	00	=		
XA515	TD4	D2	23B	(45)	01		TWRI0S TNSG0R TC41BP TSCL3B 43 23B 46 21A 48 22A 50 23A	
XA512	TQ2	D3	24B	TNDA10	00	=		
XA512	TQ2	D3	22B	(45)	01		TNDA1A TNDA2A 41 22B 43 23B	
XA503	TS8	B1	11B	TNDA2A	00	=		
XA503	TS8	B1	09A	(23)	01		TSTR2S TSPNSQ TC60BP TWRIEP TSCK3B SPI003 SPI012 SPI018 14 09A 18 10A 19 09B 20 11A 22 12A 24 13A 25 12B 26 14A	
XA409	TT3	E1	30A	TNDA3A	00	=		
XA409	TT3	E1	31A	(64)	01		TSPACQ TXASLA TXBSLA 66 31A 68 32A 70 33A	
XA509	TT3	B3	13B	TNSGAA	00	=		
XA509	TT3	B3	10B	(27)	01		TNSGAO TNSG2S TSCK3B 21 10B 23 11B 25 12B	SPACE REC/FILE HIPT
XA505	TQ2	F2	34A	TNSGAO	00	=		
XA505	TQ2	F2	36A	(72)	01		TREADP TWRITP 71 36A 73 36B	
XA510	TT3	B2	09B	TNSGCA	00	=		
XA510	TT3	B2	09A	(19)	01		TSPACQ TNSG2S TSCK3B 14 09A 17 08B 18 10A	SPACE RECORD REQUEST/START
XA410	TD4	F1	37A	TNSGDA	00	=		
XA410	TD4	F1	37B	(76)	01		TNSGDO TXEB1Q TEBZRO TSCK3B 75 37B 77 38B 78 38A 79 39B	
XA508	TD4	A1	05B	TNSGDO	00	=		
XA508	TD4	A1	05A	(11)	01		TSPACP TSPAFP THISPP SPI020 06 05A 08 06A 10 07A 13 06B	
XA508	TD4	A2	04B	TNSGEA	00	=		
XA508	TD4	A2	02B	(09)	01		TSPAFQ TTMDCO TNSG2S TSCK3B 01 02B 04 04A 05 03B 07 03A	SPACE FILE REQUEST
XA508	TD4	D2	24B	TNSGIA	00	=		
XA508	TD4	D2	23B	(45)	01		TTMDCO TNSG2S TSCL3B SPI020 43 23B 46 21A 48 22A 50 23A	TAPE MARK DETECTED

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA508	TD4	B2	10B	TNSGJA	00	=		
XA508	TD4	B2	09A	(21)	01		TDIRIS YLRBA TNSG2S TSCK3B 14 09A 18 10A 19 09B 20 11A	LRC PARITY ERROR DETECTED
XA510	TT3	B3	13B	TNSGKA	00	=		
XA510	TT3	B3	10B	(27)	01		TNSGK0 TNSG2S TSCK3B 21 10B 23 11B 25 12B	SPACE FILE START
XA505	TQ2	D1	24A	TNSGK0	00	=		
XA505	TQ2	D1	25A	(52)	01		TSPAFP TSPACP 54 25A 56 26A	
XA509	TT3	F3	39A	TNSGMA	00	=		
XA509	TT3	F3	35A	(80)	01		TWRIEQ TWRG2S TSCL3B 69 35A 76 37A 78 38A	ERASE STOP
XA503	TS8	A1	05B	TNSGOA	00	=		
XA503	TS8	A1	02B	(11)	01		TKC00P TKC02P TKA010 TKA02P TSCK3B SPI018 SPI012 SPI003 01 02B 04 04A 05 03B 06 05A 07 03A 08 06A 10 07A 13 06B	SET CHARACTER DETECT
XA508	TD4	D1	25B	TNSGOR	00	=		
XA508	TD4	D1	26B	(47)	01		TNSGOS TNSG1A TRENOS TXRS2B 49 26B 52 24A 54 25A 56 26A	
XA512	TQ2	D1	24A	TNSGOS	00	=		
XA512	TQ2	D1	25A	(52)	01		TNSGOR TNSGOA 54 25A 56 26A	GAP DETECT COUNTERBIT 0
XA509	TT3	C2	15B	TNSG1A	00	=		
XA509	TT3	C2	14B	(31)	01		TNSG1S TSCK3B SPI020 29 14B 30 15A 34 16A	
XA508	TD4	C1	17B	TNSG1R	00	=		
XA508	TD4	C1	18B	(35)	01		TNSG1S TNSG3A TRENOS TXRS1B 37 18B 38 18A 40 19A 42 20A	
XA512	TQ2	D2	21A	TNSG1S	00	=		
XA512	TQ2	D2	22A	(46)	01		TNSG1R TNSG2A 48 22A 50 23A	GAP DETECT COUNTERBIT 1
XA508	TD4	B1	11B	TNSG2A	00	=		
XA508	TD4	B1	12A	(23)	01		TNSGOS TSPNIS T080U0 TSCK1B 22 12A 24 13A 25 12B 26 14A	
XA509	TT3	B1	11A	TNSG2R	00	=		
XA509	TT3	B1	12A	(20)	01		TNSG2S TNSG5A TXRS1B 22 12A 24 13A 26 14A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	RESISTOR	FACTOR	COMMENT
			AND	OR					
XA512	TQ2	C3	16B		TNSG2S	00	=		
XA512	TQ2	C3	14B		(33)	01		TNSG2R TNSG4A 29 14B 31 15B	GAP DETECT COUNTERBIT 2
XA511	TQ2	D2	21A		TNSG3A	00	=		
XA511	TQ2	D2	22A		(46)	01		TNSG2S TSCK3B 48 22A 50 23A	
XA509	TT3	B2	09B		TNSG3R	00	=		
XA509	TT3	B2	09A		(19)	01		TNSG3S TNSG7A TXRS1B 14 09A 17 08B 18 10A	
XA506	TQ2	F4	39A		TNSG3S	00	=		
XA506	TQ2	F4	37A		(80)	01		TNSG3R TNSG6A 76 37A 78 38A	GAP DETECT COUNTERBIT 3
XA510	TT3	B1	11A		TNSG4A	00	=		
XA510	TT3	B1	12A		(20)	01		TNSG1S T002M0 TSCK1B 22 12A 24 13A 26 14A	
XA512	TQ2	C4	19B		TNSG5A	00	=		
XA512	TQ2	C4	17B		(39)	01		TNSG3S TSCL1B 35 17B 37 18B	
XA511	TQ2	C3	16B		TNSG6A	00	=		
XA511	TQ2	C3	14B		(33)	01		TNSG2S TSCK3B 29 14B 31 15B	
XA511	TQ2	C4	19B		TNSG7A	00	=		
XA511	TQ2	C4	17B		(39)	01		TNSG2R TSCK3B 35 17B 37 18B	
XA519	TQ2	E1	31A		TRCP00	00	=		
XA519	TQ2	E1	32A		(66)	01		TRRCOA TLPT5A 68 32A 70 33A	OUTPUT DATA REG CLK BITS 0-3
XA519	TQ2	E2	28A		TRCP10	00	=		
XA519	TQ2	E2	29A		(60)	01		TRRCOA TLPT5A 62 29A 64 30A	
XA519	TQ2	E3	30B		TRCP20	00	=		
XA519	TQ2	E3	28B		(57)	01		TRRCOA TLPT5A 53 28B 55 29B	
XA519	TQ2	E4	33B		TRCP30	00	=		
XA519	TQ2	E4	31B		(63)	01		TRRCOA TLPT5A 59 31B 61 32B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TRNG	LOGIC FACTOR	FACTOR	COMMENT
XA519	TQ2	F1	37B	TRCP40	00	=		
XA519	TQ2	F1	38B	(75)	01		YRRCOA TLPT5A 77 38B 79 39B	
XA519	TQ2	F2	34A	TRCP50	00	=		
XA519	TQ2	F2	36A	(72)	01		YRRCOA TLPT5A 71 36A 73 36B	
XA519	TQ2	F3	35A	TRCP60	00	=		
XA519	TQ2	F3	34B	(69)	01		YRRCOA TLPT5A 65 34B 74 35B	
XA519	TQ2	F4	39A	TRCP70	00	=		OUTPUT DATA REG CLK BIT 28-31
XA519	TQ2	F4	37A	(80)	01		YRRCOA TLPT5A 76 37A 78 38A	
XA516	TQ2	D1	24A	TRDBCA	00	=		
XA516	TQ2	D1	25A	(52)	01		TRDBCOX SPI021 54 25A 56 26A	
XA546	DCF	C3	30B	TRDBCDX4	00	=		READ CLOCK RECEIVER
XA546	DCF	C3	29A	(55)	01		SPI029 52 29A	
XA546	DCF	C4	29B	TRDBCOX	00	=		
XA546	DCF	C4	28B	(56)	01		SPI030 51 28B	
XA543	TLD	D1	24A	TRDBED4	00	=		READ ENABLE DRIVER
XA543	TLD	D1	25A	(52)	01		TRENOS SPI029 54 25A 56 26A	
XA546	DCF	C1	25B	TRDBPD4	00	=		TAPE DATA P RECEIVER
XA546	DCF	C1	29A	(46)	01		SPI029 52 29A	
XA546	DCF	C2	26B	TRDBPOX	00	=		
XA546	DCF	C2	27B	(47)	01		SPI028 49 27B	
XA546	DCF	A1	02B	TRDBODX4	00	=		TAPE DATA 0 RECEIVER
XA546	DCF	A1	05A	(07)	01		SPI029 06 05A	
XA546	DCF	A2	03B	TRDBO0X	00	=		
XA546	DCF	A2	04B	(09)	01		SPI028 11 04B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERMS	DESIGNATOR	FACTOR	COMMENT
XA546	DCF	A3	07B	TRDB1DX4	00	=		
XA546	DCF	A3	05A	(17)	01		SPI029 06 05A	TAPE DATA 1 RECEIVER
XA546	DCF	A4	06B	TRDB10X	00	=		
XA546	DCF	A4	05B	(15)	01		SPI030 13 05B	
XA546	DCF	A5	08B	TRDB2DX4	00	=		
XA546	DCF	A5	05A	(14)	01		SPI029 06 05A	TAPE DATA 2 RECEIVER
XA546	DCF	A6	07A	TRDB20X	00	=		
XA546	DCF	A6	06A	(10)	01		SPI013 08 06A	
XA546	DCF	A7	02A	TRDB3DX4	00	=		
XA546	DCF	A7	05A	(01)	01		SPI029 06 05A	TAPE DATA 3 RECEIVER
XA546	DCF	A8	03A	TRDB30X	00	=		
XA546	DCF	A8	04A	(03)	01		SPI015 04 04A	
XA546	DCF	B1	10B	TRDB4DX4	00	=		
XA546	DCF	B1	13A	(27)	01		SPI029 36 13A	TAPE DATA 4 RECEIVER
XA546	DCF	B2	11B	TRDB40X	00	=		
XA546	DCF	B2	12B	(29)	01		SPI028 31 12B	
XA546	DCF	B3	15B	TRDB5DX4	00	=		
XA546	DCF	B3	13A	(37)	01		SPI029 36 13A	TAPE DATA 5 RECEIVER
XA546	DCF	B4	14B	TRDB50X	00	=		
XA546	DCF	B4	13B	(35)	01		SPI030 33 13B	
XA546	DCF	B5	16A	TRDB6DX4	00	=		
XA546	DCF	B5	13A	(41)	01		SPI029 36 13A	TAPE DATA 6 RECEIVER
XA546	DCF	B6	15A	TRDB60X	00	=		
XA546	DCF	B6	14A	(40)	01		SPI013 38 14A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS (AND OR)	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA546	DCF	B7	10A	TRDB7DX4	00	=		TAPE DATA 7 RECEIVER
XA546	DCF	B7	13A	(23)	01	=	SPI029 36 13A	
XA546	DCF	B8	11A	TRDB70X	00	=		
XA546	DCF	B8	12A	(30)	01	=	SPI015 34 12A	
XA412	TQ2	D1	24A	TRDCAB	00	=		
XA412	TQ2	D1	24A	( )	01	=	TRDCCA TRDCDA 52 24A 46 21A	
XA412	TQ2	D1	24A	TRDCCA	00	=		TRDCAB BUSS
XA412	TQ2	D1	25A	(52)	01	=	TRDCC0 SPI004 54 25A 56 26A	
XA439	TQ2	E4	33B	TRDCC0	00	=		
XA439	TQ2	E4	31B	(63)	01	=	TXRS0B SPI011 59 31B 61 32B	
XA412	TQ2	D2	21A	TRDCDA	00	=		TRDCAB BUSS
XA412	TQ2	D2	22A	(46)	01	=	TRDCC0 SPI004 48 22A 50 23A	
XA522	TQ2	F3	35A	TRDY1A	00	=		
XA522	TQ2	F3	34B	(69)	01	=	TADSW2X TRDY10X 65 34B 74 35B	
XA546	DCF	D1	32B	TRDY1DX4	00	=		READY 1 RECEIVER
XA546	DCF	D1	36A	(55)	01	=	SPI029 72 36A	
XA546	DCF	D2	33B	TRDY10X	00	=		
XA546	DCF	D2	34B	(69)	01	=	SPI028 71 34B	
XA425	TDD	J1	32A	TREADI	00	=		
XA425	TDD	J1	32A	( )	01	=	TRED10 68 32A	
XA425	TDD	JN	33A	TREADN	00	=		
XA425	TDD	JN	33A	( )	01	=	TXDV1B 70 33A	
XA425	TDD	JP	31B	TREADP	00	=		
XA425	TDD	JP	31A	(59)	01	=	TRDCAB 66 31A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TRM	DESIGNATOR	FACTOR	COMMENT
XA425	TDD	JQ	32B	TREADQ	00	=		
XA425	TDD	JQ	33B	(61)	01		SPI007 63 33B	READ/INTERRUPT COMMAND F/E
				TREDBI	00	=		
XA425	TDD	KI	29A	( )	01		TRED20 62 29A	
				TREDBN	00	=		
XA425	TDD	KN	28A	( )	01		TXDV1B 60 28A	
XA425	TDD	KP	30B	TREDBP	00	=		
XA425	TDD	KP	30A	(57)	01		TRDCAB 64 30A	
XA425	TDD	KQ	29B	TREDBQ	00	=		
XA425	TDD	KQ	28B	(55)	01		SPI006 53 28B	
XA426	TQ2	D3	24B	TRED10	00	=		
XA426	TQ2	D3	22B	(45)	01		TXRAF3T TXRAF4T 41 22B 43 23B	
XA426	TQ2	C2	15A	TRED20	00	=		
XA426	TQ2	C2	16A	(30)	01		TXRAF4T SPI007 34 16A 36 17A	
XA508	TD4	F1	37A	TREG00	00	=		
XA508	TD4	F1	37B	(76)	01		TREG1A TREG2A TREG3A T009SA 75 37B 77 38B 78 38A 79 39B	ERASE STOP
XA506	TQ2	E2	28A	TREG1A	00	=		
XA506	TQ2	E2	29A	(60)	01		TEBZRO TC40BP 62 29A 64 30A	
XA506	TQ2	E3	30B	TREG2A	00	=		
XA506	TQ2	E3	28B	(57)	01		TEB4BP TC52BP 53 28B 55 29B	
XA506	TQ2	F1	37B	TREG3A	00	=		
XA506	TQ2	F1	38B	(75)	01		TEB5BP TC54BP 77 38B 79 39B	
XA516	TQ2	F4	39A	TRENOA	00	=		
XA516	TQ2	F4	37A	(80)	01		TSPNIS TSTR90 76 37A 78 38A	



CONNECTOR	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA518	TT3	E1 30A	TRENOR	00	=		
XA518	TT3	E1 31A	(64)	01		TREN0S TLAD0R TXRS1B 66 31A 68 32A 70 33A	
XA517	TQ2	F2 34A	TREN0S	00	=		READ REG READ ENABLE E/E
XA517	TQ2	F2 36A	(72)	01		TREN0R TREN0A 71 36A 73 36B	
XA515	TD4	F1 37A	TREN1A	00	=		
XA515	TD4	F1 37B	(76)	01		TREADQ TXEB1Q TEBZRO TSCL3B 75 37B 77 38B 78 38A 79 39B	
XA521	TS8	F1 37A	TREN1R	00	=		
XA521	TS8	F1 36A	(76)	01		TREN1S TREN1A TB0TOR SPI023 TS29CP TNSG1R TXRS1B TNSGMA 71 36A 72 34A 73 36B 74 35B 75 37B 77 38B 78 38A 79 39B	
XA517	TQ2	F3 35A	TREN1S	00	=		FORWARD READ ENABLE E/E
XA517	TQ2	F3 34B	(69)	01		TREN1R TREN2A 65 34B 74 35B	
XA518	TT3	F2 35B	TREN2A	00	=		
XA518	TT3	F2 34B	(74)	01		TSPNIS TDIR1S TSTR90 65 34B 71 36A 72 34A	
XA507	TS8	F1 37A	TREN2R	00	=		
XA507	TS8	F1 36A	(76)	01		TREN2S TREN1A TNSG5A TXXDIA TXRS2B TREN3A SPI003 SPI020 71 36A 72 34A 73 36B 74 35B 75 37B 77 38B 78 38A 79 39B	
XA506	TQ2	F2 34A	TREN2S	00	=		
XA506	TQ2	F2 36A	(72)	01		TREN2R TREN2A 71 36A 73 36B	
XA517	TQ2	F4 39A	TREN3A	00	=		
XA517	TQ2	F4 37A	(80)	01		TREDBQ TS2890 76 37A 78 38A	
XA506	TQ2	D2 21A	TREQCA	00	=		
XA506	TQ2	D2 22A	(46)	01		TXXDDQ TXXCSD 48 22A 50 23A	
			TREQCI	00	=		
XA425	TDD	MI 36A	( )	01		SPI006 71 36A	
			TREQCN	00	=		
XA425	TDD	MN 34A	( )	01		TREQ3A 72 34A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA425	TDD	MP	35A	TREQCP	00	=		
XA425	TDD	MP	36B	(69)	01		TREQRA 73 36B	
XA425	TDD	MQ	35B	TREQCQ	00	=		
XA425	TDD	MQ	34B	(74)	01		TXRS2B 65 34B	READ REQUEST DELAYCONTROL FF
XA506	TQ2	D3	24B	TREQDA	00	=		
XA506	TQ2	D3	22B	(45)	01		TXXDIQ TXXCSO 41 22B 43 23B	
XA506	TQ2	D4	27B	TREQRA	00	=		
XA506	TQ2	D4	25B	(51)	01		TREQRO SPI018 47 25B 49 26B	
XA505	TQ2	E4	33B	TREQRO	00	=		
XA505	TQ2	E4	31B	(63)	01		TREQCA TREQDA 59 31B 61 32B	
XA504	TS8	F1	37A	TREQQA	00	=		
XA504	TS8	F1	36A	(76)	01		TREQ1R TREN2S TREADQ TMWCBQ TSCK1B SPI003 SPI012 SPI018 71 36A 72 34A 73 36B 74 35B 75 37B 77 38B 78 38A 79 39B	
				TREQOI	00	=		
XA513	TDD	CI	13A	( )	01		TREQ1P 24 13A	
				TREQON	00	=		
XA513	TDD	CN	14A	( )	01		TSCL1B 26 14A	
XA513	TDD	CP	11B	TREQOP	00	=		
XA513	TDD	CP	12A	(23)	01		TREQCP 22 12A	
XA513	TDD	CQ	12B	TREQOQ	00	=		
XA513	TDD	CQ	13B	(25)	01		SPI020 27 13B	READ REQUEST DELAYBIT 0
XA510	TT3	E2	29B	TREQOR	00	=		
XA510	TT3	E2	28B	(55)	01		TREQOS TREQ1A TXRS2B 53 28B 60 28A 62 29A	
XA506	TQ2	E1	31A	TREQOS	00	=		
XA506	TQ2	E1	32A	(66)	01		TREQOR TREQQA 68 32A 70 33A	

CONNECTOR	UNIT GROUP	GROUP	TEST POINTS AND/OR	EQUATION	TEST ITEM	DESIGN- NATOR	FACTOR	COMMENT
XA506	TQ2	F3	35A	TREQ1A	00 =			
XA506	TQ2	F3	34B	(69)	01		TREQ1S TSCK1B 65 34B 74 35B	
				TREQ1I	00 =			
XA514	TDD	CI	13A	( )	01		TREQ0Q 24 13A	
				TREQ1N	00 =			
XA514	TDD	CN	14A	( )	01		TSC11B 26 14A	
XA514	TDD	CP	11B	TREQ1P	00 =			
XA514	TDD	CP	12A	(23)	01		TREQCP 22 12A	
XA514	TDD	CQ	12B	TREQ1Q	00 =			
XA514	TDD	CQ	13B	(25)	01		SPI020 27 13B	
XA510	TT3	E3	33B	TREQ1R	00 =			
XA510	TT3	E3	30B	(63)	01		TREQ1S TREQ3A TXRS2B 57 30B 59 31B 61 32B	
XA512	TQ2	C2	15A	TREQ1S	00 =			
XA512	TQ2	C2	16A	(30)	01		TREQ1R TREQ2A 34 16A 36 17A	
XA505	TQ2	B4	13B	TREQ2A	00 =			
XA505	TQ2	B4	11B	(27)	01		TREQ0S TSCK3B 23 11B 25 12B	
				TREQ2I	00 =			
XA513	TDD	LI	38B	( )	01		TREQ5A 77 38B	
				TREQ2N	00 =			
XA513	TDD	LN	39B	( )	01		TREQ1P 79 39B	
XA513	TDD	LP	37A	TREQ2P	00 =			
XA513	TDD	LP	37B	(76)	01		TREQCP 75 37B	
XA513	TDD	LQ	38A	TREQ2Q	00 =			
XA513	TDD	LQ	39A	(78)	01		SPI019 80 39A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA525	TD4	A1	05B	TREQ3A	00	=		
XA525	TD4	A1	05A	(11)	01		TREQ2Q TREQ3P TREQ4Q TSCL3B 06 05A 08 06A 10 07A 13 06B	
				TREQ3I	00	=		
XA514	TDD	DI	10A	( )	01		TREQ4P 18 10A	
				TREQ3N	00	=		
XA514	TDD	DN	09A	( )	01		TREQ2P 14 09A	
XA514	TDD	DP	10B	TREQ3P	00	=		
XA514	TDD	DP	11A	(21)	01		TREQCP 20 11A	
XA514	TDD	DQ	09B	TREQ3Q	00	=		
XA514	TDD	DQ	08B	(19)	01		SPI019 17 08B	
XA507	TS8	E1	31B	TREQ4A	00	=		
XA507	TS8	E1	29B	(59)	01		TREQ1S TREN2S TM02BP TM03BP TM04BQ TS29CP TSCL3B SPI020 55 29B 60 28A 61 32B 62 29A 64 30A 66 31A 68 32A 70 33A	
				TREQ4I	00	=		
XA526	TDD	LI	38B	( )	01		TREQ3Q 77 38B	
				TREQ4N	00	=		
XA526	TDD	LN	39B	( )	01		TREQ2P 79 39B	
XA526	TDD	LP	37A	TREQ4P	00	=		
XA526	TDD	LP	37B	(76)	01		TREQCP 75 37B	
XA526	TDD	LQ	38A	TREQ4Q	00	=		
XA526	TDD	LQ	39A	(78)	01		SPI022 80 39A	READ REQUEST DELAYBIT 4
XA524	TT3	F2	35B	TREQ5A	00	=		
XA524	TT3	F2	34B	(74)	01		TREQ2Q SPI024 SPI025 65 34B 71 36A 72 34A	
XA506	TQ2	E4	33B	TREWCA	00	=		
XA506	TQ2	E4	31B	(63)	01		TCSD10 TRUNCO 59 31B 61 32B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA543	TLD	D3	24B	TREWCD4	00	=		REWIND DRIVER
XA543	TLD	D3	22B	(45)	01		TRUNAS TREWCO 41 22B 43 23B	
XA511	TQ2	D1	24A	TREWCO	00	=		
XA511	TQ2	D1	25A	(52)	01		SPI020 TREWCA 54 25A 56 26A	
XA425	TDD	BI	03B	TREWDI	00	=		
XA425	TDD	BI		( )	01		TREW20 05 03B	
XA425	TDD	BN	02B	TREWDN	00	=		
XA425	TDD	BN		( )	01		TX0V1B 01 02B	
XA425	TDD	BP	04B	TREWDP	00	=		
XA425	TDD	BP	04A	(09)	01		TRDCAB 04 04A	
XA425	TDD	BQ	03A	TREWDQ	00	=		REWIND/NO INTERRUPT CMND F/F
XA425	TDD	BQ	02A	(07)	01		SPI007 03 02A	
XA413	TDD	LI	38B	TREWII	00	=		
XA413	TDD	LI		( )	01		TXIRCO 77 38B	
XA413	TDD	LN	39B	TREWLN	00	=		
XA413	TDD	LN		( )	01		TX0V1B 79 39B	
XA413	TDD	LP	37A	TREWLP	00	=		
XA413	TDD	LP	37B	(76)	01		TRDCAB 75 37B	
XA413	TDD	LQ	38A	TREWLI	00	=		REWIND/INTERRUPT COMMAND F/F
XA413	TDD	LQ	39A	(78)	01		SPI004 80 39A	
XA545	DCF	D1	32B	TREWDX4	00	=		REWINDING 1 RECEIVER
XA545	DCF	D1	36A	(65)	01		SPI029 72 36A	
XA425	TDD	AI	06A	TREWAI	00	=		
XA425	TDD	AI		( )	01		TRW190 08 06A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESIGN VALUE	FACTOR	COMMENT
			AND	OR					
					TREW1N	00	=		
XA425	TDD	AN	07A		( )	01		TXDV1B 10 07A	
XA425	TDD	AP	05B		TREW1P	00	=		
XA425	TDD	AP	05A		(11 )	01		TRW12A 06 05A	
XA425	TDD	AQ	06B		TREW1Q	00	=		
XA425	TDD	AQ	07B		(13 )	01		TXRS1B 15 07B	REWINDING 1 REMEMBER E/F
XA545	DCF	D2	33B		TREW10X	00	=		
XA545	DCF	D2	34B		(69 )	01		SPI02B 71 34B	
					TREW2I	00	=		
XA414	TDD	LI	38B		( )	01		TRW290 77 38B	
					TREW2N	00	=		
XA414	TDD	LN	39B		( )	01		TXDV1B 79 39B	
XA414	TDD	LP	37A		TREW2P	00	=		
XA414	TDD	LP	37B		(76 )	01		TRW22A 75 37B	
XA414	TDD	LQ	38A		TREW2Q	00	=		
XA414	TDD	LQ	39A		(78 )	01		TXRS2B 80 39A	REWINDING 2 REMEMBER E/F
XA426	TQ2	B2	09A		TREW20	00	=		
XA426	TQ2	B2	10A		(14 )	01		TXR095T SPI007 18 10A 20 11A	
XA439	TQ2	E3	30B		TREW3A	00	=		
XA439	TQ2	E3	28B		(57 )	01		TREW30 SPI011 53 28B 55 29B	
					TREW3I	00	=		
XA415	TDD	LI	38B		( )	01		TRW390 77 38B	
					TREW3N	00	=		
XA415	TDD	LN	39B		( )	01		TXDV1B 79 39B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TRV	DESIGNATOR	FACTOR	COMMENT
XA415	TDD	LP	37A	TREW3P	00	=		
XA415	TDD	LP	37B	(76)	01		TRW32A 75 37B	
XA415	TDD	LQ	38A	TREW3Q	00	=		REWINDING 3 REMEMBER F/F
XA415	TDD	LQ	39A	(78)	01		TXRS2B 80 39A	
XA429	TD4	E2	30B	TREW30	00	=		
XA429	TD4	E2	29B	(57)	01		TXR094T TXR095T TXR092T TXRAF2T 55 29B 60 28A 62 29A 64 30A	
				TREW4I	00	=		
XA416	TDD	LI	38B	( )	01		TRW490 77 38B	
				TREW4N	00	=		
XA416	TDD	LN	39B	( )	01		TXDV1B 79 39B	
XA416	TDD	LP	37A	TREW4P	00	=		
XA416	TDD	LP	37B	(76)	01		TRW42A 75 37B	
XA416	TDD	LQ	38A	TREW4Q	00	=		REWINDING 4 REMEMBER F/F
XA416	TDD	LQ	39A	(78)	01		TXRS2B 80 39A	
XA502	TS8	O1	25B	TRMROA	00	=		INPUT REQUEST TO BUFFER
XA502	TS8	O1	23B	(47)	01		TRYDCO TREN1S TXB00P TXB01Q TKA01P TKA02Q TSCL3B SPI018 43 23B 46 21A 48 22A 49 26B 50 23A 52 24A 54 25A 56 26A	
XA521	TS8	E1	31B	TRRCOA	00	=		TRANSFER BUFFER TO OUTPUT
XA521	TS8	E1	29B	(59)	01		TWTSTP TS29CP TMO2BP TMO3BP TMO4BQ TSCL3B SPI019 SPI022 55 29B 60 28A 61 32B 62 29A 64 30A 66 31A 68 32A 70 33A	
XA516	TQ2	B1	12A	TRRS0A	00	=		OUTPUT DATA REG EVEN RESET
XA516	TQ2	B1	13A	(22)	01		TEB4R0 SPI020 24 13A 26 14A	
XA516	TQ2	C1	18A	TRRS1A	00	=		OUTPUT DATA REG ODD RESET
XA516	TQ2	C1	19A	(38)	01		TEB4R0 SPI021 40 19A 42 20A	
XA516	TQ2	B2	09A	TRRS2A	00	=		
XA516	TQ2	B2	10A	(14)	01		TEB4R0 SPI020 18 10A 20 11A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TRYS	DESIGNATOR	FACTOR	COMMENT
XA516	TQ2	C2	15A	TRRS3A	00	=		
XA516	TQ2	C2	16A	(30)	01		TEB4R0 SPI021 34 16A 36 17A	
XA516	TQ2	B3	10B	TRRS4A	00	=		
XA516	TQ2	B3	08B	(21)	01		TEB4R0 SPI020 17 08B 19 09B	
XA516	TQ2	C3	16B	TRRS5A	00	=		
XA516	TQ2	C3	14B	(33)	01		TEB4R0 SPI021 29 14B 31 15B	
XA516	TQ2	B4	13B	TRRS6A	00	=		
XA516	TQ2	B4	11B	(27)	01		TEB4R0 SPI020 23 11B 25 12B	
XA516	TQ2	C4	19B	TRRS7A	00	=		
XA516	TQ2	C4	17B	(39)	01		TEB4R0 SPI021 35 17B 37 18B	
XA516	TQ2	A1	05A	TRST0A	00	=		OUTPUT DATA REG EVEN SET
XA516	TQ2	A1	06A	(06)	01		TRST00 SPI020 08 06A 10 07A	
XA517	TQ2	A1	05A	TRST00	00	=		
XA517	TQ2	A1	06A	(06)	01		TWRT6A SPI021 08 06A 10 07A	
XA516	TQ2	A2	02B	TRST1A	00	=		OUTPUT DATA REG ODD SET
XA516	TQ2	A2	04A	(01)	01		TRST10 SPI020 04 04A 05 03B	
XA517	TQ2	A2	02B	TRST10	00	=		
XA517	TQ2	A2	04A	(01)	01		TWRT7A SPI021 04 04A 05 03B	
XA516	TQ2	A3	04B	TRST4A	00	=		
XA516	TQ2	A3	02A	(09)	01		TRST00 SPI020 03 02A 07 03A	
XA516	TQ2	A4	07B	TRST5A	00	=		
XA516	TQ2	A4	05B	(15)	01		TRST10 SPI020 11 05B 13 06B	
XA412	TQ2	E1	31A	TRTDC0	00	=		
XA412	TQ2	E1	32A	(66)	01		TREADP SPI004 68 32A 70 33A	



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TRF	DESIGNATOR	FACTOR	COMMENT
XA431	TT3	B2	09B	TRTDEA	00	=		
XA431	TT3	B2	09A	(19)	01		TREADQ TREN2S TXEA00 14 09A 17 08B 18 10A	
XA503	TS8	C1	17B	TRTDPA	00	=		SET LATERAL PARTYERROR
XA503	TS8	C1	15A	(35)	01		TREN1S TT0PBPR TKA01Q TKA02Q TSCL3B SPI018 SPI012 SPI003 30 15A 31 15B 34 16A 36 17A 37 18B 38 18A 40 19A 42 20A	
XA504	TS8	A1	05B	TRTDOA	00	=		TRANSFER BYTE 0
XA504	TS8	A1	02B	(11)	01		TRTDCO TREN1S TKB00P TKB01P TKA01Q TKA02Q TSCK3B SPI018 01 02B 04 04A 05 03B 06 05A 07 03A 08 06A 10 07A 13 06B	
XA505	TQ2	F3	35A	TRTD00	00	=		
XA505	TQ2	F3	34B	(69)	01		TRTDOA SPI018 65 34B 74 35B	
XA504	TS8	B1	11B	TRTD1A	00	=		TRANSFER BYTE 1
XA504	TS8	B1	09A	(23)	01		TRTDCO TREN1S TKB00Q TKB01P TKA01Q TKA02Q TSCK3B SPI018 14 09A 18 10A 19 09B 20 11A 22 12A 24 13A 25 12B 26 14A	
XA412	TQ2	E2	28A	TRTD10	00	=		
XA412	TQ2	E2	29A	(60)	01		TRTD1A SPI004 62 29A 64 30A	
XA504	TS8	C1	17B	TRTD2A	00	=		TRANSFER BYTE 2
XA504	TS8	C1	15A	(35)	01		TRTDCO TREN1S TKB00Q TKB01Q TKA01Q TKA02Q TSCL3B SPI018 30 15A 31 15B 34 16A 36 17A 37 18B 38 18A 40 19A 42 20A	
XA412	TQ2	E3	30B	TRTD20	00	=		
XA412	TQ2	E3	28B	(57)	01		TRTD2A SPI004 53 28B 55 29B	
XA504	TS8	D1	25B	TRTD3A	00	=		TRANSFER BYTE 3
XA504	TS8	D1	23B	(47)	01		TRTDCO TREN1S TKB00P TKB01Q TKA01Q TKA02Q TSCK3B SPI018 43 23B 46 21A 48 22A 49 26B 50 23A 52 24A 54 25A 56 26A	
XA412	TQ2	E4	33B	TRTD30	00	=		
XA412	TQ2	E4	31B	(63)	01		TRTD3A SPI004 59 31B 61 32B	
XA521	TS8	B1	11B	TRTOCA	00	=		READ COMMAND IOU TIMEOUT
XA521	TS8	B1	09A	(23)	01		TREADQ TXONLO TREN1S TMWCBO TM02BQ TM03BQ TM04BP TSCL3B 14 09A 18 10A 19 09B 20 11A 22 12A 24 13A 25 12B 26 14A	
XA502	TS8	A1	05B	TRUNAR	00	=		
XA502	TS8	A1	02B	(11)	01		TRUNAS TSTP6A SPI009 TRUN3A TXRS1B SPI018 SPI003 SPI012 01 02B 04 04A 05 03B 06 05A 07 03A 08 06A 10 07A 13 06B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA505	TQ2	A3	04B	TRUNAS	00	=		
XA505	TQ2	A3	02A	(09)	01	=	TRUNAR TSTR2A 03 02A 07 03A	MOTOR RUN CONTROL F/F
XA519	TQ2	B4	13B	TRUNCA	00	=		
XA519	TQ2	B4	11B	(27)	01	=	TDIRIR TSPNIR 23 11B 25 12B	
XA543	TLD	D2	21A	TRUNCD4	00	=		
XA543	TLD	D2	22A	(46)	01	=	TRUNAS TREWCA 48 22A 50 23A	RUN DRIVER
XA520	TQ2	B4	13B	TRUNC0	00	=		
XA520	TQ2	B4	11B	(27)	01	=	TRUNCA SPI021 23 11B 25 12B	
XA408	TQ2	D1	24A	TRUN3A	00	=		
XA408	TQ2	D1	25A	(52)	01	=	TDIRIR TBT10X 54 25A 56 26A	
XA521	TS8	C1	17B	TRWCOA	00	=		
XA521	TS8	C1	15A	(35)	01	=	TS29CP TMO2BQ TMO3BP TMO4BQ TSCL3B SPI003 SPI022 SPI019 30 15A 31 15B 34 16A 36 17A 37 18B 38 18A 40 19A 42 20A	WRITE INTO BUFFER REG
XA406	TQ2	B4	13B	TRWSOA	00	=		
XA406	TQ2	B4	11B	(27)	01	=	TRWSOS TSCK3B 23 11B 25 12B	
XA409	TT3	A1	04A	TRWSOR	00	=		
XA409	TT3	A1	05A	(04)	01	=	TRWSOS TRWS1A TXRS1B 06 05A 08 06A 10 07A	
XA407	TQ2	A3	04B	TRWSOS	00	=		
XA407	TQ2	A3	02A	(09)	01	=	TRWSOR TSTR6A 03 02A 07 03A	REWIND COUNTER BIT0
XA406	TQ2	A4	07B	TRWS1A	00	=		
XA406	TQ2	A4	05B	(15)	01	=	TRWS1S TSCK1B 11 05B 13 06B	
XA409	TT3	A2	03A	TRWS1R	00	=		
XA409	TT3	A2	02B	(07)	01	=	TRWS1S TRWS3A TXRS1B 01 02B 03 02A 05 03B	
XA407	TQ2	A4	07B	TRWS1S	00	=		
XA407	TQ2	A4	05B	(15)	01	=	TRWS1R TRWSOA 11 05B 13 06B	REWIND COUNTER BIT1

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA409	TT3	B2	09B	TRWS2A	00	=		
XA409	TT3	B2	09A	(19)	01		TREWDQ TRWSOS TSCK3B 14 09A 17 08B 18 10A	
XA406	TQ2	A3	04B	TRWS3A	00	=		
XA406	TQ2	A3	02A	(09)	01		TRWSOR TSCK3B 03 02A 07 03A	
XA410	TD4	B2	10B	TRWS4A	00	=		
XA410	TD4	B2	09A	(21)	01		TCSD10 TRWOCA TRWSOS TSCK3B 14 09A 18 10A 19 09B 20 11A	
XA523	TQ2	A4	07B	TRWOCA	00	=		
XA523	TQ2	A4	05B	(15)	01		TREW10X SPI022 11 05B 13 06B	
XA523	TQ2	E1	31A	TRW10A	00	=		
XA523	TQ2	E1	32A	(66)	01		TYS1BS TREW1Q 68 32A 70 33A	
XA522	TQ2	E1	31A	TRW12A	00	=		
XA522	TQ2	E1	32A	(66)	01		TYS1BS TCSD20 68 32A 70 33A	
XA523	TQ2	D4	27B	TRW14A	00	=		
XA523	TQ2	D4	25B	(51)	01		TRW140 SPT022 47 25B 49 26B	
XA525	TD4	D1	25B	TRW140	00	=		
XA525	TD4	D1	26B	(47)	01		TRW10A TRW20A TRW30A TRW40A 49 26B 52 24A 54 25A 56 26A	
XA536	TQ2	E1	31A	TRW19A	00	=		
XA536	TQ2	E1	32A	(66)	01		TYS190 TREW20 68 32A 70 33A	
XA537	TQ2	E1	31A	TRW19C	00	=		
XA537	TQ2	E1	32A	(66)	01		TRW19A TREW1P 68 32A 70 33A	
XA523	TQ2	E2	28A	TRW20A	00	=		
XA523	TQ2	E2	29A	(60)	01		TYS2BS TREW2Q 62 29A 64 30A	
XA522	TQ2	E2	28A	TRW22A	00	=		
XA522	TQ2	E2	29A	(60)	01		TYS2BS TCSD20 62 29A 64 30A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGN NOTOR	FACTOR	COMMENT
XA536	TQ2	E2	28A	TRW29A	00	=		
XA536	TQ2	E2	29A	(60)	01		TTS290 TREW20 62 29A 64 30A	
XA537	TQ2	E2	28A	TRW290	00	=		
XA537	TQ2	E2	29A	(60)	01		TRW29A TREW2P 62 29A 64 30A	
XA523	TQ2	E3	30B	TRW30A	00	=		
XA523	TQ2	E3	28B	(57)	01		TTS38S TREW3Q 53 28B 55 29B	
XA522	TQ2	E3	30B	TRW32A	00	=		
XA522	TQ2	E3	28B	(57)	01		TTS38S TCSD20 53 28B 55 29B	
XA536	TQ2	E3	30B	TRW39A	00	=		
XA536	TQ2	E3	28B	(57)	01		TTS390 TREW20 53 28B 55 29B	
XA537	TQ2	E3	30B	TRW390	00	=		
XA537	TQ2	E3	28B	(57)	01		TRW39A TREW3P 53 28B 55 29B	
XA523	TQ2	E4	33B	TRW40A	00	=		
XA523	TQ2	E4	31B	(63)	01		TTS48S TREW4Q 59 31B 61 32B	
XA522	TQ2	E4	33B	TRW42A	00	=		
XA522	TQ2	E4	31B	(63)	01		TTS48S TCSD20 59 31B 61 32B	
XA536	TQ2	E4	33B	TRW49A	00	=		
XA536	TQ2	E4	31B	(63)	01		TTS490 TREW20 59 31B 61 32B	
XA537	TQ2	E4	33B	TRW490	00	=		
XA537	TQ2	E4	31B	(63)	01		TRW49A TREW4P 59 31B 61 32B	
				TR008I	00	=		
XA530	TDD	J1	32A	( )	01		TB008Q 68 32A	
				TR008N	00	=		
XA530	TDD	JN	33A	( )	01		TRCP00 70 33A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	USING NATOP	FACTOR	COMMENT
XA530	TDD	JP	31B	TR00BP	00	=		
XA530	TDD	JP	31A	(59)	01		TRR50A 66 31A	
XA530	TDD	JQ	32B	TR00BQ	00	=		OUTPUT DATA REG BIT 0
XA530	TDD	JQ	33B	(61)	01		TRST0A 63 33B	
XA536	TQ2	A1	05A	TR00CA	00	=		
XA536	TQ2	A1	06A	(06)	01		TT00BQ TRT000 08 06A 10 07A	
XA530	TDD	KI	29A	TR01BI	00	=		
				( )	01		TB01BQ 62 29A	
XA530	TDD	KN	28A	TR01BN	00	=		
				( )	01		TRCP00 60 28A	
XA530	TDD	KP	30B	TR01BP	00	=		
XA530	TDD	KP	30A	(57)	01		TRRS1A 64 30A	
XA530	TDD	KQ	29B	TR01BQ	00	=		
XA530	TDD	KQ	28B	(55)	01		TRST1A 53 28B	
XA536	TQ2	A2	02B	TR01CA	00	=		
XA536	TQ2	A2	04A	(01)	01		TT01BQ TRT000 04 04A 05 03B	
XA530	TDD	LI	38B	TR02BI	00	=		
				( )	01		TB02BQ 77 38B	
XA530	TDD	LN	39B	TR02BN	00	=		
				( )	01		TRCP00 79 39B	
XA530	TDD	LP	37A	TR02BP	00	=		
XA530	TDD	LP	37B	(76)	01		TRR50A 75 37B	
XA530	TDD	LQ	38A	TR02BQ	00	=		
XA530	TDD	LQ	39A	(78)	01		TRST0A 80 39A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA536	TQ2	A3	04B	TR02CA	00	=		
XA536	TQ2	A3	02A	(09)	01		TT02BQ TRTD00 03 02A 07 03A	
				TR03BI	00	=		
XA530	TDD	MI	36A	( )	01		TB03BQ 71 36A	
				TR03BN	00	=		
XA530	TDD	MN	34A	( )	01		TRCP00 72 34A	
XA530	TDD	MP	35A	TR03BP	00	=		
XA530	TDD	MP	36B	(69)	01		TRRS1A 73 36B	
XA530	TDD	MQ	35B	TR03BQ	00	=		
XA530	TDD	MQ	34B	(74)	01		TRST1A 65 34B	
XA536	TQ2	A4	07B	TR03CA	00	=		
XA536	TQ2	A4	05B	(15)	01		TT03BQ TRTD00 11 05B 13 06B	
				TR04BI	00	=		
XA531	TDD	AI	06A	( )	01		TB04BQ 08 06A	
				TR04BN	00	=		
XA531	TDD	AN	07A	( )	01		TRCP10 10 07A	
XA531	TDD	AP	05B	TR04BP	00	=		
XA531	TDD	AP	05A	(11)	01		TRRS0A 06 05A	
XA531	TDD	AQ	06B	TR04BQ	00	=		
XA531	TDD	AQ	07B	(13)	01		TRST0A 15 07B	
XA536	TQ2	B1	12A	TR04CA	00	=		
XA536	TQ2	B1	13A	(22)	01		TT04BQ TRTD00 24 13A 26 14A	
				TR05BI	00	=		
XA531	TDD	BI	03B	( )	01		TB05BQ 05 03B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS (AND OR)	EQUATION	TERM	DISC. INDIC. NATOR	FACTOR	COMMENT
				TR05BN	00 =			
XA531	TDD	BN	02B	( )	01		TRCP10 01 02B	
XA531	TDD	BP	04B	TR05BP	00 =			
XA531	TDD	BP	04A	(09 )	01		TRRS1A 04 04A	
XA531	TDD	BQ	03A	TR05BQ	00 =			
XA531	TDD	BQ	02A	(07 )	01		TRST1A 03 02A	
XA536	TQ2	B2	09A	TR05CA	00 =			
XA536	TQ2	B2	10A	(14 )	01		TT05BQ TRT000 18 10A 20 11A	
				TR06BI	00 =			
XA531	TDD	CI	13A	( )	01		TB06BQ 24 13A	
				TR06BN	00 =			
XA531	TDD	CN	14A	( )	01		TRCP10 26 14A	
XA531	TDD	CP	11B	TR06BP	00 =			
XA531	TDD	CP	12A	(23 )	01		TRRS0A 22 12A	
XA531	TDD	CQ	12B	TR06BQ	00 =			
XA531	TDD	CQ	13B	(25 )	01		TRST0A 27 13B	
XA536	TQ2	B3	10B	TR06CA	00 =			
XA536	TQ2	B3	08B	(21 )	01		TT06BQ TRT000 17 08B 19 09B	
				TR07BI	00 =			
XA531	TDD	DI	10A	( )	01		TB07BQ 18 10A	
				TR07BN	00 =			
XA531	TDD	DN	09A	( )	01		TRCP10 14 09A	
XA531	TDD	DP	10B	TR07BP	00 =			
XA531	TDD	DP	11A	(21 )	01		TRRS1A 20 11A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA531	TDD	DQ	09B	TR07BQ	00	=		
XA531	TDD	DQ	08B	(19)	01		TRST1A 17 08B	OUTPUT DATA REG BIT 7
XA536	TQ2	B4	13B	TR07CA	00	=		
XA536	TQ2	B4	11B	(27)	01		TT07BQ TRT000 23 11B 25 12B	
XA531	TDD	EI	19A	TR08BI	00	=		
				( )	01		TB08BQ 40 19A	
XA531	TDD	EN	20A	TR08BN	00	=		
				( )	01		TRCP20 42 20A	
XA531	TDD	EP	17B	TR08BP	00	=		
XA531	TDD	EP	18A	(35)	01		TRRS2A 38 18A	
XA531	TDD	EQ	18B	TR08BQ	00	=		
XA531	TDD	EQ	19B	(37)	01		TRST0A 39 19B	OUTPUT DATA REG BIT 8
XA536	TQ2	C1	18A	TR08CA	00	=		
XA536	TQ2	C1	19A	(38)	01		TT00BQ TRT010 40 19A 42 20A	
XA531	TDD	FI	16A	TR09BI	00	=		
				( )	01		TB09BQ 34 16A	
XA531	TDD	FN	15A	TR09BN	00	=		
				( )	01		TRCP20 30 15A	
XA531	TDD	FP	16B	TR09BP	00	=		
XA531	TDD	FP	17A	(33)	01		TRRS3A 36 17A	
XA531	TDD	FQ	15B	TR09BQ	00	=		
XA531	TDD	FQ	14B	(31)	01		TRST1A 29 14B	
XA536	TQ2	C2	15A	TR09CA	00	=		
XA536	TQ2	C2	16A	(30)	01		TT01BQ TRT010 34 16A 36 17A	



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESIG. NA TOR	FACTOR	COMMENT
			AND	OR					
XA531	TDD	G1	25A		TR10BI ( )	00 =			
						01	TB10BQ 54 25A		
XA531	TDD	GN	26A		TR10BN ( )	00 =			
						01	TRCP20 56 26A		
XA531	TDD	GP	25B		TR10BP ( )	00 =			
XA531	TDD	GP	24A		(47)	01	TRRSZA 52 24A		
XA531	TDD	GQ	26B		TR10BQ ( )	00 =			
XA531	TDD	GQ	27B		(49)	01	TRSTOA 51 27B		
XA536	TQ2	C3	16B		TR10CA ( )	00 =			
XA536	TQ2	C3	14B		(33)	01	TT02BQ TRTD10 29 14B 31 15B		
XA531	TDD	HI	22A		TR11BI ( )	00 =			
						01	TB11BQ 48 22A		
XA531	TDD	HN	21A		TR11BN ( )	00 =			
						01	TRCP20 46 21A		
XA531	TDD	HP	24B		TR11BP ( )	00 =			
XA531	TDD	HP	23A		(45)	01	TRRSZA 50 23A		
XA531	TDD	HQ	23B		TR11BQ ( )	00 =			
XA531	TDD	HQ	22B		(43)	01	TRSTIA 41 22B		
XA536	TQ2	C4	19B		TR11CA ( )	00 =			
XA536	TQ2	C4	17B		(39)	01	TT03BQ TRTD10 35 17B 37 18B		
XA531	TDD	JI	32A		TR12BI ( )	00 =			
						01	TB12BQ 68 32A		
XA531	TDD	JN	33A		TR12BN ( )	00 =			
						01	TRCP30 70 33A		

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA531	TDD	JP	31B	TR12BP	00	=		
XA531	TDD	JP	31A	(59)	01		TRRS2A 66 31A	
XA531	TDD	JQ	32B	TR12BQ	00	=		
XA531	TDD	JQ	33B	(61)	01		TRSTOA 63 33B	
XA536	TQ2	D1	24A	TR12CA	00	=		
XA536	TQ2	D1	25A	(52)	01		TT04BQ TRTD10 54 25A 56 26A	
				TR13BI	00	=		
XA531	TDD	KI	29A	( )	01		TB13BQ 62 29A	
				TR13BN	00	=		
XA531	TDD	KN	28A	( )	01		TRCP30 60 28A	
XA531	TDD	KP	30B	TR13BP	00	=		
XA531	TDD	KP	30A	(57)	01		TRRS3A 64 30A	
XA531	TDD	KQ	29B	TR13BQ	00	=		
XA531	TDD	KQ	28B	(55)	01		TRST1A 53 28B	
XA536	TQ2	D2	21A	TR13CA	00	=		
XA536	TQ2	D2	22A	(46)	01		TT05BQ TRTD10 48 22A 50 23A	
				TR14BI	00	=		
XA531	TDD	LI	38B	( )	01		TB14BQ 77 38B	
				TR14BN	00	=		
XA531	TDD	LN	39B	( )	01		TRCP30 79 39B	
XA531	TDD	LP	37A	TR14BP	00	=		
XA531	TDD	LP	37B	(76)	01		TRRS2A 75 37B	OUTPUT DATA REG BIT 15
XA531	TDD	LQ	38A	TR14BQ	00	=		
XA531	TDD	LQ	39A	(78)	01		TRSTOA 80 39A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA536	TQ2	D3	24B	TR14CA	00	=		
XA536	TQ2	D3	22B	(45)	01		TT06BQ TRTD10 41 22B 43 23B	
				TR15BI	00	=		
XA531	TDD	MI	36A	( )	01		TB15BQ 71 36A	
				TR15BN	00	=		
XA531	TDD	MN	34A	( )	01		TRCP30 72 34A	
XA531	TDD	MP	35A	TR15BP	00	=		
XA531	TDD	MP	36B	(69)	01		TRRS3A 73 36B	
XA531	TDD	MQ	35B	TR15BQ	00	=		
XA531	TDD	MQ	34B	(74)	01		TRSY1A 65 34B	
XA536	TQ2	D4	27B	TR15CA	00	=		
XA536	TQ2	D4	25B	(51)	01		TT07BQ TRTD10 47 25B 49 26B	
				TR16BI	00	=		
XA532	TDD	AI	06A	( )	01		TB16BQ 08 06A	
				TR16BN	00	=		
XA532	TDD	AN	07A	( )	01		TRCP40 10 07A	
XA532	TDD	AP	05B	TR16BP	00	=		
XA532	TDD	AP	05A	(11)	01		TRRS4A 06 05A	
XA532	TDD	AQ	06B	TR16BQ	00	=		
XA532	TDD	AQ	07B	(13)	01		TRST4A 15 07B	OUTPUT DATA REG BIT 16
XA537	TQ2	A1	05A	TR16CA	00	=		
XA537	TQ2	A1	06A	(06)	01		TT00BQ TRTD20 08 06A 10 07A	
				TR17BI	00	=		
XA532	TDD	BI	03B	( )	01		TB17BQ 05 03B	

CONNECTOR	CRUISE TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
				TR17BN	00	=		
XA532	TDD	BN	02B	( )	01		TRCP40 01 02B	
XA532	TDD	BP	04B	TR17BP	00	=		
XA532	TDD	BP	04A	(09 )	01		TRRS5A 04 04A	
XA532	TDD	BQ	03A	TR17BQ	00	=		
XA532	TDD	BQ	02A	(07 )	01		TRST5A 03 02A	
XA537	TQ2	A2	02B	TR17CA	00	=		
XA537	TQ2	A2	04A	(01 )	01		TT01BQ TRTD20 04 04A 05 03B	
				TR18BI	00	=		
XA532	TDD	CI	13A	( )	01		TB18BQ 24 13A	
				TR18BN	00	=		
XA532	TDD	CN	14A	( )	01		TRCP40 26 14A	
XA532	TDD	CP	11B	TR18BP	00	=		
XA532	TDD	CP	12A	(23 )	01		TRRS4A 22 12A	
XA532	TDD	CQ	12B	TR18BQ	00	=		
XA532	TDD	CQ	13B	(25 )	01		TRST4A 27 13B	
XA537	TQ2	A3	04B	TR18CA	00	=		
XA537	TQ2	A3	02A	(09 )	01		TT02BQ TRTD20 03 02A 07 03A	
				TR19BI	00	=		
XA532	TDD	DI	10A	( )	01		TB19BQ 18 10A	
				TR19BN	00	=		
XA532	TDD	DN	09A	( )	01		TRCP40 14 09A	
XA532	TDD	DP	10B	TR19BP	00	=		
XA532	TDD	DP	11A	(21 )	01		TRRS5A 20 11A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA532	TDD	DQ	09B	TR19BQ	00	=		
XA532	TDD	DQ	08B	(19)	01		TRST5A 17 08B	
XA537	TQ2	A4	07B	TR19CA	00	=		
XA537	TQ2	A4	05B	(15)	01		TT03BQ TRTD20 11 05B 13 06B	
XA532	TDD	E1	19A	TR20BI	00	=		
				( )	01		TB20BQ 40 19A	
XA532	TDD	EN	20A	TR20BN	00	=		
				( )	01		TRCP50 42 20A	
XA532	TDD	EP	17B	TR20BP	00	=		
XA532	TDD	EP	18A	(35)	01		TRRS4A 38 18A	
XA532	TDD	EQ	18B	TR20BQ	00	=		
XA532	TDD	EQ	19B	(37)	01		TRST4A 39 19B	
XA537	TQ2	B1	12A	TR20CA	00	=		
XA537	TQ2	B1	13A	(22)	01		TT04BQ TRTD20 24 13A 26 14A	
XA532	TDD	FI	16A	TR21BI	00	=		
				( )	01		TB21BQ 34 16A	
XA532	TDD	FN	15A	TR21BN	00	=		
				( )	01		TRCP50 30 15A	
XA532	TDD	FP	16B	TR21BP	00	=		
XA532	TDD	FP	17A	(33)	01		TRRS5A 36 17A	
XA532	TDD	FQ	15B	TR21BQ	00	=		
XA532	TDD	FQ	14B	(31)	01		TRST5A 29 14B	
XA537	TQ2	B2	09A	TR21CA	00	=		
XA537	TQ2	B2	10A	(14)	01		TT05BQ TRTD20 18 10A 20 11A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DEFINITION	FACTOR	COMMENT
XA532	TDD	GI	25A	TR22BI ( )	00 01	=	TB22BQ 54 25A	
XA532	TDD	GN	26A	TR22BN ( )	00 01	=	TRCP50 56 26A	
XA532	TDD	GP	25B	TR22BP (47 )	00 01	=	TRRS4A 52 24A	
XA532	TDD	GQ	26B	TR22BQ (49 )	00 01	=	TRST4A 51 27B	
XA537	TQ2	B3	10B	TR22CA (21 )	00 01	=	TT06BQ TRT020 17 08B 19 09B	
XA532	TDD	HI	22A	TR23BI ( )	00 01	=	TB23BQ 48 22A	
XA532	TDD	HN	21A	TR23BN ( )	00 01	=	TRCP50 46 21A	
XA532	TDD	HP	24B	TR23BP (45 )	00 01	=	TRRS5A 50 23A	
XA532	TDD	HQ	23B	TR23BQ (43 )	00 01	=	TRST5A 41 22B	OUTPUT DATA REG BIT 23
XA537	TQ2	B4	13B	TR23CA (27 )	00 01	=	TT07BQ TRT020 23 11B 25 12B	
XA532	TDD	JI	32A	TR24BI ( )	00 01	=	TB24BQ 68 32A	
XA532	TDD	JN	33A	TR24BN ( )	00 01	=	TRCP60 70 33A	

CONNECTOR	UNIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIG- NATOR	FACTOR	COMMENT
XA532	TDD	JP	31B	TR24BP	00	=		
XA532	TDD	JP	31A	(59)	01		TRRS6A 66 31A	
XA532	TDD	JQ	32B	TR24BQ	00	=		OUTPUT DATA REG BIT 24
XA532	TDD	JQ	33B	(61)	01		TRST4A 63 33B	
XA537	TQ2	C1	18A	TR24CA	00	=		
XA537	TQ2	C1	19A	(38)	01		TT00BQ TRT030 40 19A 42 20A	
XA532	TDD	KI	29A	TR25BI	00	=		
				( )	01		TB25BQ 62 29A	
XA532	TDD	KN	28A	TR25BN	00	=		
				( )	01		TRCP60 60 28A	
XA532	TDD	KP	30B	TR25BP	00	=		
XA532	TDD	KP	30A	(57)	01		TRRS7A 64 30A	
XA532	TDD	KQ	29B	TR25BQ	00	=		
XA532	TDD	KQ	28B	(55)	01		TRST5A 53 28B	
XA537	TQ2	C2	15A	TR25CA	00	=		
XA537	TQ2	C2	16A	(30)	01		TT01BQ TRT030 34 16A 36 17A	
XA532	TDD	LI	38B	TR26BI	00	=		
				( )	01		TB26BQ 77 38B	
XA532	TDD	LN	39B	TR26BN	00	=		
				( )	01		TRCP60 79 39B	
XA532	TDD	LP	37A	TR26BP	00	=		
XA532	TDD	LP	37B	(76)	01		TRRS6A 75 37B	
XA532	TDD	LQ	38A	TR26BQ	00	=		
XA532	TDD	LQ	39A	(78)	01		TRST4A 80 39A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA537	TQ2	C3	16B	TR26CA	00	=		
XA537	TQ2	C3	14B	(33)	01		TT02BQ TRTD30 29 14B 31 15B	
				TR27BI	00	=		
XA532	TDD	MI	36A	( )	01		TB27BQ 71 36A	
				TR27BN	00	=		
XA532	TDD	MN	34A	( )	01		TRCP60 72 34A	
XA532	TDD	MP	35A	TR27BP	00	=		
XA532	TDD	MP	36B	(69)	01		TRR57A 73 36B	
XA532	TDD	MQ	35B	TR27BQ	00	=		
XA532	TDD	MQ	34B	(74)	01		TRST5A 65 34B	
XA537	TQ2	C4	19B	TR27CA	00	=		
XA537	TQ2	C4	17B	(39)	01		TT03BQ TRTD30 35 17B 37 18B	
				TR28BI	00	=		
XA533	TDD	AI	06A	( )	01		TB28BQ 08 06A	
				TR28BN	00	=		
XA533	TDD	AN	07A	( )	01		TRCP70 10 07A	
XA533	TDD	AP	05B	TR28BP	00	=		
XA533	TDD	AP	05A	(11)	01		TRR56A 06 05A	
XA533	TDD	AQ	06B	TR28BQ	00	=		
XA533	TDD	AQ	07B	(13)	01		TRST4A 15 07B	
XA537	TQ2	D1	24A	TR28CA	00	=		
XA537	TQ2	D1	25A	(52)	01		TT04BQ TRTD30 54 25A 56 26A	
				TR29BI	00	=		
XA533	TDD	BI	03B	( )	01		TB29BQ 05 03B	



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
				TR29BN	00 =			
XA533	TDD	BN	02B	( )	01		TRCP70 01 02B	
XA533	TDD	BP	04B	TR29BP	00 =			
XA533	TDD	BP	04A	(09 )	01		TRRS7A 04 04A	
XA533	TDD	BQ	03A	TR29BQ	00 =			
XA533	TDD	BQ	02A	(07 )	01		TRST5A 03 02A	
XA537	TQ2	D2	21A	TR29CA	00 =			
XA537	TQ2	D2	22A	(46 )	01		TT05BQ TRTD30 48 22A 50 23A	
				TR30B1	00 =			
XA533	TDD	CI	13A	( )	01		TB30B0 24 13A	
				TR30BN	00 =			
XA533	TDD	CN	14A	( )	01		TRCP70 26 14A	
XA533	TDD	CP	11B	TR30BP	00 =			
XA533	TDD	CP	12A	(23 )	01		TRRS6A 22 12A	
XA533	TDD	CQ	12B	TR30BQ	00 =			
XA533	TDD	CQ	13B	(25 )	01		TRST4A 27 13B	
XA537	TQ2	D3	24B	TR30CA	00 =			
XA537	TQ2	D3	22B	(45 )	01		TT06B0 TRTD30 41 22B 43 23B	
				TR31B1	00 =			
XA533	TDD	DI	10A	( )	01		TB31B0 18 10A	
				TR31BN	00 =			
XA533	TDD	DN	09A	( )	01		TRCP70 14 09A	
XA533	TDD	DP	10B	TR31BP	00 =			
XA533	TDD	DP	11A	(21 )	01		TRRS7A 20 11A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
			AND	OR					
XA533	TDD	DQ	09B		TR31BQ	00	=		OUTPUT DATA REG BIT 31
XA533	TDD	DQ	08B		(19)	01		TRST5A 17 08B	
XA537	TQ2	D4	27B		TR31CA	00	=		
XA537	TQ2	D4	25B		(51)	01		TT07BQ TRTD30 47 25B 49 26B	
XA503	TS8	D1	25B		TSBZY0	00	=		CONTROLLER BUSS
XA503	TS8	D1	23B		(47)	01		TCSDOR TSNCOR TINT2A TINT6A TXXDIR TLPTBR SPI012 SPI018 43 23B 46 21A 48 22A 49 26B 50 23A 52 24A 54 25A 56 26A	
XA509	TT3	E3	33B		TSCI1A	00	=		
XA509	TT3	E3	30B		(63)	01		TSNC2S TBUSYR TSCL3B 57 30B 59 31B 61 32B	
					TSCK1B	00	=		
XA411	TQ2	C1	18A		( )	01		TSCK10 TSCL10 TSCM10 TSCN10 38 18A 30 15A 33 16B 39 19B	PHASE 1 OF 2 PHASECLOCK
XA411	TQ2	C1	18A		TSCK10	00	=		TSCK1B BUSS
XA411	TQ2	C1	19A		(38)	01		TXCP1A SPI001 40 19A 42 20A	
					TSCK3B	00	=		
XA411	TQ2	E1	31A		( )	01		TSCK30 TSCL30 TSCM30 TSCN30 66 31A 60 28A 57 30B 63 33B	PHASE 3 OF 2 PHASECLOCK
XA411	TQ2	E1	31A		TSCK30	00	=		TSCK3B BUSS
XA411	TQ2	E1	32A		(66)	01		TXCP3A SPI001 68 32A 70 33A	
					TSCL1B	00	=		
XA411	TQ2	D1	24A		( )	01		TSCP10 TSCQ10 TSCR10 TSCS10 52 24A 46 21A 45 24B 51 27B	
XA411	TQ2	C2	15A		TSCL10	00	=		TSCK1B BUSS
XA411	TQ2	C2	16A		(30)	01		TXCP1A SPI001 34 16A 36 17A	
					TSCL3B	00	=		
XA411	TQ2	F1	37B		( )	01		TSCP30 TSCQ30 TSCR30 TSCS30 75 37B 72 34A 69 35A 80 39A	
XA411	TQ2	E2	28A		TSCL30	00	=		TSCK3B BUSS
XA411	TQ2	E2	29A		(60)	01		TXCP3A SPI001 62 29A 64 30A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM.	DESIGNATOR	FACTOR	COMMENT
XA411	TQ2	C3	16B	TSCM10	00	=		
XA411	TQ2	C3	14B	(33)	01		TXCP1A SPI001 29 14B 31 15B	TSCK1B BUSS
XA411	TQ2	E3	30B	TSCM30	00	=		
XA411	TQ2	E3	28B	(57)	01		TXCP3A SPI001 53 28B 55 29B	TSCK3B BUSS
XA411	TQ2	C4	19B	TSCN10	00	=		
XA411	TQ2	C4	17B	(39)	01		TXCP1A SPI001 35 17B 37 18B	TSCK1B BUSS
XA411	TQ2	E4	33B	TSCN30	00	=		
XA411	TQ2	E4	31B	(63)	01		TXCP3A SPI001 59 31B 61 32B	TSCK3B BUSS
XA411	TQ2	D1	24A	TSCP10	00	=		
XA411	TQ2	D1	25A	(52)	01		TXCP1A SPI001 54 25A 56 26A	TSC11B BUSS
XA411	TQ2	F1	37B	TSCP30	00	=		
XA411	TQ2	F1	38B	(75)	01		TXCP3A SPI004 77 38B 79 39B	TSC13B BUSS
XA411	TQ2	D2	21A	TSCQ10	00	=		
XA411	TQ2	D2	22A	(46)	01		TXCP1A SPI001 48 22A 50 23A	TSC11B BUSS
XA411	TQ2	F2	34A	TSCQ30	00	=		
XA411	TQ2	F2	36A	(72)	01		TXCP3A SPI004 71 36A 73 36B	TSC13B BUSS
XA411	TQ2	D3	24B	TSCR10	00	=		
XA411	TQ2	D3	22B	(45)	01		TXCP1A SPI001 41 22B 43 23B	TSC11B BUSS
XA411	TQ2	F3	35A	TSCR30	00	=		
XA411	TQ2	F3	34B	(69)	01		TXCP3A SPI004 65 34B 74 35B	TSC13B BUSS
XA411	TQ2	D4	27B	TSCS10	00	=		
XA411	TQ2	D4	25B	(51)	01		TXCP1A SPI001 47 25B 49 26B	TSC11B BUSS
XA411	TQ2	F4	39A	TSCS30	00	=		
XA411	TQ2	F4	37A	(80)	01		TXCP3A SPI004 76 37A 78 38A	TSC13B BUSS

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM.	DESIGNATOR	FACTOR	COMMENT
			AND	OR					
XA502	TS8	C1	17B		TSNC0A	00	=		
XA502	TS8	C1	15A		(35)	01		TSNC0S TSNC2R TBUSYR TLAD2S TSCL3B SPI003 SPI018 SPI012 30 15A 31 15B 34 16A 36 17A 37 18B 38 18A 40 19A 42 20A	
XA410	TD4	D1	25B		TSNCOR	00	=		
XA410	TD4	D1	26B		(47)	01		TSNCOS TINT1A TINT5A TXRS1B 49 26B 52 24A 54 25A 56 26A	
XA412	TQ2	D3	24B		TSNCOS	00	=		INPUT SYNC COUNTERBIT 0
XA412	TQ2	D3	22B		(45)	01		TSNCOR TCSD0A 41 22B 43 23B	
XA511	TQ2	A1	05A		TSNC1A	00	=		
XA511	TQ2	A1	06A		(06)	01		TSNC2S TSCK3B 08 06A 10 07A	
XA506	TQ2	B1	12A		TSNC1R	00	=		
XA506	TQ2	B1	13A		(22)	01		TSNC1S TSNC1A 24 13A 26 14A	
XA505	TQ2	B1	12A		TSNC1S	00	=		INPUT SYNC COUNTERBIT 1
XA505	TQ2	B1	13A		(22)	01		TSNC1R TSNC0A 24 13A 26 14A	
XA506	TQ2	B2	09A		TSNC2A	00	=		
XA506	TQ2	B2	10A		(14)	01		TSNC1S TSCK1B 18 10A 20 11A	
XA506	TQ2	B3	10B		TSNC2R	00	=		
XA506	TQ2	B3	08B		(21)	01		TSNC2S TSNC3A 17 08B 19 09B	
XA505	TQ2	B3	10B		TSNC2S	00	=		INPUT SYNC COUNTERBIT 2
XA505	TQ2	B3	08B		(21)	01		TSNC2R TSNC2A 17 08B 19 09B	
XA505	TQ2	B2	09A		TSNC3A	00	=		
XA505	TQ2	B2	10A		(14)	01		TSNC1R TSCK1B 18 10A 20 11A	
					TSPACI	00	=		
XA425	T00	CI	13A		( )	01		TSPA10 24 13A	
					TSPACN	00	=		
XA425	T00	CN	14A		( )	01		TXDV1B 26 14A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM DESIGNATOR	FACTOR	COMMENT
XA425	TDD	CP	11B	TSPACP	00 =		
XA425	TDD	CP	12A	(23)	01	TRDCAB 22 12A	
XA425	TDD	CQ	12B	TSPACC	00 =		SPACE FOR REV COMMAND F/E
XA425	TDD	CQ	13B	(25)	01	SPI007 27 13B	
XA425	TDD	DI	10A	TSPAFI	00 =		
				( )	01	TSPA20 18 10A	
XA425	TDD	DN	09A	TSPAFN	00 =		
				( )	01	TXDV1B 14 09A	
XA425	TDD	DP	10B	TSPAFP	00 =		
XA425	TDD	DP	11A	(21)	01	TRDCAB 20 11A	
XA425	TDD	DQ	09B	TSPAFQ	00 =		SPACE FILE FOR COMMAND F/E
XA425	TDD	DQ	08B	(19)	01	SPI006 17 08B	
XA426	TQ2	D2	21A	TSPA10	00 =		
XA426	TQ2	D2	22A	(46)	01	TXR090T TXR098T 48 22A 50 23A	
XA426	TQ2	C1	18A	TSPA20	00 =		
XA426	TQ2	C1	19A	(38)	01	TXR099T SPI007 40 19A 42 20A	
XA543	TLD	F1	37B	TSPDCD4	00 =		SPEED DRIVER
XA543	TLD	F1	38B	(75)	01	TSPNIS SPI029 77 38B 79 39B	
XA406	TQ2	D2	21A	TSPI0A	00 =		
XA406	TQ2	D2	22A	(46)	01	TSPNSQ TSTP2S 48 22A 50 23A	
XA407	TQ2	D4	27B	TSPI1A	00 =		
XA407	TQ2	D4	25B	(51)	01	TSPNSP TSTP2S 47 25B 49 26B	
XA406	TQ2	C2	15A	TSPNIR	00 =		
XA406	TQ2	C2	16A	(30)	01	TSPNIS TSPI1A 34 16A 36 17A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA407	TQ2	C2	15A	TSPNIS	00	=		
XA407	TQ2	C2	16A	(30)	01		TSPNIR TSP10A 34 16A 36 17A	OLD SPEED F/F (7.50R 60T/PS)
XA542	TDD	CI	13A	TSPNSI	00	=		
XA542	TDD	CI	13A	( )	01		TREW3A 24 13A	
XA542	TDD	CN	14A	TSPNSN	00	=		
XA542	TDD	CN	14A	( )	01		TXDV1B 26 14A	
XA542	TDD	CP	11B	TSPNSP	00	=		
XA542	TDD	CP	12A	(23)	01		TROCAB 22 12A	
XA542	TDD	CQ	12B	TSPNSQ	00	=		
XA542	TDD	CQ	13B	(25)	01		SPT029 27 13B	NEW SPEED
XA406	TQ2	C3	16B	TSRS0A	00	=		
XA406	TQ2	C3	14B	(33)	01		TSRS00 SPI001 29 14B 31 15B	
XA409	TT3	C3	19B	TSRS00	00	=		
XA409	TT3	C3	16B	(39)	01		TANC2A TXRS1B SPI001 33 16B 35 17B 37 18B	
XA407	TQ2	C3	16B	TSRS1A	00	=		
XA407	TQ2	C3	14B	(33)	01		TSRS00 SPI001 29 14B 31 15B	
XA407	TQ2	E2	28A	TSTPRA	00	=		
XA407	TQ2	E2	29A	(60)	01		TSTPOS TSTP1R 62 29A 64 30A	
XA510	TT3	C1	17A	TSTPOR	00	=		
XA510	TT3	C1	18A	(36)	01		TSTPOS TSTP1A TXRS1B 38 18A 40 19A 42 20A	
XA410	TD4	E1	31B	TSTPOS	00	=		
XA410	TD4	E1	32B	(59)	01		TSTPOR TINT5A TSTP2A TSTP4A 61 32B 66 31A 68 32A 70 33A	STOP DELAY BIT 0
XA408	TQ2	E1	31A	TSTP1A	00	=		
XA408	TQ2	E1	32A	(66)	01		TSTP2S TSCK3B 68 32A 70 33A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA408	TQ2	E2	28A	TSTP1R	00	=		
XA408	TQ2	E2	29A	(60)	01		TSTP1S TSTP0S 62 29A 64 30A	
XA407	TQ2	E1	31A	TSTP1S	00	=		STOP DELAY BIT 1
XA407	TQ2	E1	32A	(66)	01		TSTP1R TSTP6A 68 32A 70 33A	
XA409	TT3	E2	29B	TSTP2A	00	=		
XA409	TT3	E2	28B	(55)	01		TLAD3S TSNCOR TSCK3B 53 28B 60 28A 62 29A	
XA408	TQ2	F1	37B	TSTP2R	00	=		
XA408	TQ2	F1	38B	(75)	01		TSTP2S TSTR2A 77 38B 79 39B	
XA409	TT3	F2	35B	TSTP2S	00	=		STOP DELAY BIT 2
XA409	TT3	F2	34B	(74)	01		TSTP2R TSTP8A TXRS1B 65 34B 71 36A 72 34A	
XA433	TD4	F1	37A	TSTP4A	00	=		
XA433	TD4	F1	37B	(76)	01		TSNC2S TBUSYS TDSCOA TSCL3B 75 37B 77 38B 78 38A 79 39B	
XA409	TT3	E3	33B	TSTP6A	00	=		
XA409	TT3	E3	30B	(63)	01		TSTP0S TCZROO TSCK1B 57 30B 59 31B 61 32B	
XA431	TT3	F2	35B	TSTP8A	00	=		
XA431	TT3	F2	34B	(74)	01		TSTP1S TSTP90 TSCL1B 65 34B 71 36A 72 34A	
XA408	TQ2	F3	35A	TSTP9A	00	=		
XA408	TQ2	F3	34B	(69)	01		TSPNIS T082M0 65 34B 74 35B	
XA432	TQ2	C4	19B	TSTP90	00	=		
XA432	TQ2	C4	17B	(39)	01		TSTP9A TC51BQ 35 17B 37 18B	
XA409	TT3	C2	15B	TSTRCO	00	=		
XA409	TT3	C2	14B	(31)	01		TSTP0R TSTP1R TSTP2R 29 14B 30 15A 34 16A	
XA509	TT3	A3	07B	TSTRRA	00	=		
XA509	TT3	A3	04B	(15)	01		TSTR1R TSTROS TSTP0R 09 04B 11 05B 13 06B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
			AND/OR					
XA525	T04	C2	16B	TSTR0A	00	=		
XA525	T04	C2	15A	(33)	01		TSNC2R TBUSYS TSTR00 TSCL3B 30 15A 31 15B 34 16A 36 17A	
XA409	TT3	F1	36B	TSTR0R	00	=		
XA409	TT3	F1	37B	(73)	01		TSTR0S TSTR1A TBUSYS 75 37B 77 38B 79 39B	
XA406	TQ2	D4	27B	TSTR0S	00	=		START DELAY BIT 0
XA406	TQ2	D4	25B	(51)	01		TSTR0R TSTR0A 47 25B 49 26B	
XA406	TQ2	F3	35A	TSTR1A	00	=		
XA406	TQ2	F3	34B	(69)	01		TSTR2S TSK3B 65 34B 74 35B	
XA409	TT3	B1	11A	TSTR1R	00	=		
XA409	TT3	B1	12A	(20)	01		TSTR1S TSTR0S TSTP0R 22 12A 24 13A 26 14A	
XA408	TQ2	B1	12A	TSTR1S	00	=		START DELAY BIT 1
XA408	TQ2	B1	13A	(22)	01		TSTR1R TSTR2A 24 13A 26 14A	
XA410	TD4	B1	11B	TSTR2A	00	=		
XA410	TD4	B1	12A	(23)	01		TSTR0S TSTP0R TC2R00 TSK1B 22 12A 24 13A 25 12B 26 14A	
XA431	TT3	F3	39A	TSTR2R	00	=		
XA431	TT3	F3	35A	(80)	01		TSTR2S TSTP6A TXRS0B 69 35A 76 37A 78 38A	
XA409	TT3	C1	17A	TSTR2S	00	=		START DELAY BIT 2
XA409	TT3	C1	18A	(36)	01		TSTR2R TSTR4A TSTR6A 38 18A 40 19A 42 20A	
XA406	TQ2	E1	31A	TSTR3A	00	=		
XA406	TQ2	E1	32A	(66)	01		TSTR1S TBOT0S 68 32A 70 33A	
XA406	TQ2	E2	28A	TSTR3R	00	=		
XA406	TQ2	E2	29A	(60)	01		TSTR3S TSTR1S 62 29A 64 30A	
XA407	TQ2	D1	24A	TSTR3S	00	=		START DELAY BIT 3
XA407	TQ2	D1	25A	(52)	01		TSTR3R TSTR3A 54 25A 56 26A	



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	FORM	DESIGNATOR	FACTOR	COMMENT
XA410	TD4	C2	16B	TSTR4A	00	=		
XA410	TD4	C2	15A	(33)	01		TSTR3S TBTOR TSTR80 TSCK1B 30 15A 31 15B 34 16A 36 17A	
XA408	TQ2	F4	39A	TSTR5A	00	=		
XA408	TQ2	F4	37A	(80)	01		T033M0 TSPNIS 76 37A 78 38A	
XA410	TD4	D2	24B	TSTR6A	00	=		
XA410	TD4	D2	23B	(45)	01		TSTR1S TSTR3R TSTR60 TSCK1B 43 23B 46 21A 48 22A 50 23A	
XA407	TQ2	E4	33B	TSTR60	00	=		
XA407	TQ2	E4	31B	(63)	01		TSTR5A T066MA 59 31B 61 32B	
XA407	TQ2	D2	21A	TSTR7A	00	=		
XA407	TQ2	D2	22A	(46)	01		T200M0 TWRITP 48 22A 50 23A	
XA408	TQ2	D2	21A	TSTR80	00	=		
XA408	TQ2	D2	22A	(46)	01		TSTR7A T300MA 48 22A 50 23A	
XA525	TD4	E1	31B	TSTR90	00	=		WRITE COUNT 7BUS
XA525	TD4	E1	32B	(59)	01		TSTR4A TSTR6A TFST4A THSPRA 61 32B 66 31A 68 32A 70 33A	
XA418	TS8	D1	25B	TSYN1A	00	=		MOTION COMMAND START
XA418	TS8	D1	23B	(47)	01		TXDV1B TXR091T TXR093T TXR096T TXR097T SPI006 SPI007 SPI003 43 23B 46 21A 48 22A 49 26B 50 23A 52 24A 54 25A 56 26A	
XA526	TDD	A1	06A	TS14BI	00	=		
XA526	TDD	A1	06A	( )	01		SPI022 08 06A	
XA526	TDD	AN	07A	TS14BN	00	=		
XA526	TDD	AN	07A	( )	01		SPI019 10 07A	
XA526	TDD	AP	05B	TS14BP	00	=		
XA526	TDD	AP	05A	(11)	01		TSR50A 06 05A	
XA526	TDD	AQ	06B	TS14BQ	00	=		TAPE MARK DETECTED/F
XA526	TDD	AQ	07B	(13)	01		TNSG1A 15 07B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA406	TQ2	B3	10B	TS1590	00	=		
XA406	TQ2	B3	08B	(21)	01		TCILK0 SPI001 17 08B 19 09B	
				TS238I	00	=		
XA526	TDD	BI	03B	( )	01		SPI003 05 03B	
				TS238N	00	=		
XA526	TDD	BN	02B	( )	01		SPI008 01 02B	
XA526	TDD	BP	04B	TS238P	00	=		
XA526	TDD	BP	04A	(09)	01		TSR50A 04 04A	
XA526	TDD	BQ	03A	TS238Q	00	=		
XA526	TDD	BQ	02A	(07)	01		TS235A 03 02A	COMPUTER DATA PARITY ERROR
XA407	TQ2	F1	37B	TS235A	00	=		
XA407	TQ2	F1	38B	(75)	01		TS2350 SPI001 77 38B 79 39B	
XA406	TQ2	F1	37B	TS2350	00	=		
XA406	TQ2	F1	38B	(75)	01		TXDPEA TXODEA 77 38B 79 39B	
				TS26CI	00	=		
XA526	TDD	EI	19A	( )	01		SPI022 40 19A	
				TS26CN	00	=		
XA526	TDD	EN	20A	( )	01		SPI019 42 20A	
XA526	TDD	EP	17B	TS26CP	00	=		
XA526	TDD	EP	18A	(35)	01		TSRS1A 38 18A	
XA526	TDD	EQ	18B	TS26CQ	00	=		
XA526	TDD	EQ	19B	(37)	01		TFPE1A 39 19B	FILE PROTECT ERROR
XA409	TT3	B3	13B	TS2890	00	=		
XA409	TT3	B3	10B	(27)	01		TS238P TS30BP TS318P 21 10B 23 11B 25 12B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS (AND/OR)	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
				TS29CI	00 =			
XA526	TDD	FI	16A	( )	01		SPI003 34 16A	
				TS29CN	00 =			
XA526	TDD	FN	15A	( )	01		SPI008 30 15A	
XA526	TDD	FP	16B		00 =			
XA526	TDD	FP	17A	(33 )	01		TSRS1A 36 17A	
XA526	TDD	FQ	15B		00 =			TIMING ERROR
XA526	TDD	FQ	14B	(31 )	01		TS29SA 29 14B	
XA506	TQ2	A4	07B		00 =			
XA506	TQ2	A4	05B	(15 )	01		TS29S0 SPI018 11 05B 13 06B	
XA505	TQ2	A4	07B		00 =			
XA505	TQ2	A4	05B	(15 )	01		TRT0CA TWT0CA 11 05B 13 06B	
				TS30B1	00 =			
XA526	TDD	CI	13A	( )	01		SPI022 24 13A	
				TS30BN	00 =			
XA526	TDD	CN	14A	( )	01		SPI019 26 14A	
XA526	TDD	CP	11B		00 =			
XA526	TDD	CP	12A	(23 )	01		TSRS0A 22 12A	
XA526	TDD	CQ	12B		00 =			LONGITUDINAL PARITY ERROR
XA526	TDD	CQ	13B	(25 )	01		TNSGJA 27 13B	
				TS30C1	00 =			
XA526	TDD	GI	25A	( )	01		SPI022 54 25A	
				TS30CN	00 =			
XA526	TDD	GN	26A	( )	01		SPI019 56 26A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERN	RELATIONSHIP	FACTOR	COMMENT
XA526	TDD	GP	25B	TS30CP	00	=		
XA526	TDD	GP	24A	(47)	01		TSRS1A 52 24A	
XA526	TDD	GQ	26B	TS30CQ	00	=		
XA526	TDD	GQ	27B	(49)	01		TNDASA 51 27B	NO DATA ERROR
XA526	TDD	DI	10A	TS318I	00	=		
				( )	01		SPI003 18 10A	
XA526	TDD	DN	09A	TS318N	00	=		
				( )	01		SPI008 14 09A	
XA526	TDD	DP	10B	TS318P	00	=		
XA526	TDD	DP	11A	(21)	01		TSRS0A 20 11A	
XA526	TDD	DQ	09B	TS318Q	00	=		
XA526	TDD	DQ	08B	(19)	01		TRTDPA 17 08B	LATERAL PARITY ERROR
XA526	TDD	HI	22A	TS31CI	00	=		
				( )	01		SPI003 48 22A	
XA526	TDD	HN	21A	TS31CN	00	=		
				( )	01		SPI008 46 21A	
XA526	TDD	HP	24B	TS31CP	00	=		
XA526	TDD	HP	23A	(45)	01		TSRS1A 50 23A	
XA526	TDD	HQ	23B	TS31CQ	00	=		
XA526	TDD	HQ	22B	(43)	01		TRWS4A 41 22B	MODOR ERROR
XA432	TQ2	B3	10B	TTASLA	00	=		
XA432	TQ2	B3	08B	(21)	01		TTAS10 TXOV1B 17 08B 19 09B	
XA539	TDD	BI	03B	TTASLI	00	=		
				( )	01		TTAS10 05 03B	

CONNECTOR	UNIT GROUP	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIG- NATOR	FACTOR	COMMENT
				TTASLN	00 =			
XA539	TDD	BN	02B	( )	01		TXDVIB 01 02B	
XA539	TDD	BP	04B	TTASLP	00 =			
XA539	TDD	BP	04A	(09 )	01		TROCAB 04 04A	
XA539	TDD	BQ	03A	TTASLQ	00 =			LOAD EOB COUNTER COMMAND E/F
XA539	TDD	BQ	02A	(07 )	01		SPI026 03 02A	
XA426	TQ2	A4	07B	TTAS10	00 =			
XA426	TQ2	A4	05B	(15 )	01		TXR093T SPI007 11 05B 13 06B	
XA517	TQ2	B1	12A	TTCPO0	00 =			
XA517	TQ2	B1	13A	(22 )	01		TRDBCA SPI021 24 13A 26 14A	
XA517	TQ2	B2	09A	TTCP10	00 =			
XA517	TQ2	B2	10A	(14 )	01		TRDBCA SPI021 18 10A 20 11A	
XA510	TT3	F3	39A	TTMDCA	00 =			
XA510	TT3	F3	35A	(80 )	01		SPI003 TKCOOP TKC01Q 69 35A 76 37A 78 38A	
XA505	TQ2	C2	15A	TTMDC0	00 =			
XA505	TQ2	C2	16A	(30 )	01		TTMDCA SPI018 34 16A 36 17A	
XA524	TT3	F3	39A	TTRSCA	00 =			
XA524	TT3	F3	35A	(80 )	01		TKA01P TKA02Q TSCL3B 69 35A 76 37A 78 38A	
XA517	TQ2	B3	10B	TTRS0A	00 =			
XA517	TQ2	B3	08B	(21 )	01		TTRS00 SPI021 17 08B 19 09B	
XA518	TT3	B3	13B	TTRS00	00 =			
XA518	TT3	B3	10B	(27 )	01		TTRSCA TRENOS TWRIEP 21 10B 23 11B 25 12B	
XA517	TQ2	B4	13B	TTRS1A	00 =			
XA517	TQ2	B4	11B	(27 )	01		TTRS00 SPI021 23 11B 25 12B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DEFINITION	FACTOR	COMMENT
XA410	TD4	A2	04B	TTSC00	00	=		
XA410	TD4	A2	02B	(09)	01		TTSC1A TTSC2A TTSC3A TTSC4A 01 02B 04 04A 05 03B 07 03A	
XA408	TQ2	A1	05A	TTSC1A	00	=		
XA408	TQ2	A1	06A	(06)	01		TTS1BQ TTS1BS 08 06A 10 07A	
XA408	TQ2	A2	02B	TTSC2A	00	=		
XA408	TQ2	A2	04A	(01)	01		TTS2BQ TTS2BS 04 04A 05 03B	
XA408	TQ2	A3	04B	TTSC3A	00	=		
XA408	TQ2	A3	02A	(09)	01		TTS3BQ TTS3BS 03 02A 07 03A	
XA408	TQ2	A4	07B	TTSC4A	00	=		
XA408	TQ2	A4	05B	(15)	01		TTS4BQ TTS4BS 11 05B 13 06B	
XA407	TQ2	B4	13B	TTSC90	00	=		
XA407	TQ2	B4	11B	(27)	01		TSTP2R SPI001 23 11B 25 12B	
XA541	TDD	AI	06A	TTS1BI	00	=		
XA541	TDD	AI	06A	( )	01		TTS190 08 06A	
XA541	TDD	AN	07A	TTS1BN	00	=		
XA541	TDD	AN	07A	( )	01		TXDV1B 10 07A	
XA541	TDD	AP	05B	TTS1BP	00	=		
XA541	TDD	AP	05A	(11)	01		TXRS0B 06 05A	
XA541	TDD	AQ	06B	TTS1BQ	00	=		
XA541	TDD	AQ	07B	(13)	01		SPI026 15 07B	TRANSPORT 1 SELECTED NEW
XA406	TQ2	A1	05A	TTS1BR	00	=		
XA406	TQ2	A1	06A	(06)	01		TTS1BS TTS11A 08 06A 10 07A	
XA407	TQ2	A1	05A	TTS1BS	00	=		
XA407	TQ2	A1	06A	(06)	01		TTS1BR TTS10A 08 06A 10 07A	TRANSPORT 2 SELECTED OLD

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIG-NATOR	FACTOR	COMMENT
XA408	TQ2	B2	09A	TTS10A	00	=		
XA408	TQ2	B2	10A	(14)	01		TTS1BQ TTSC90 18 10A 20 11A	
XA408	TQ2	B3	10B	TTS11A	00	=		
XA408	TQ2	B3	08B	(21)	01		TTS1BP TTSC90 17 08B 19 09B	
XA536	TQ2	F1	37B	TTS19A	00	=		
XA536	TQ2	F1	38B	(75)	01		TXR0CR TXR1CR 77 38B 79 39B	
XA537	TQ2	F1	37B	TTS190	00	=		
XA537	TQ2	F1	38B	(75)	01		TTS19A SPI025 77 38B 79 39B	
XA541	TDD	B1	03B	TTS2B1	00	=		
XA541	TDD	B1	03B	( )	01		TTS290 05 03B	
XA541	TDD	BN	02B	TTS2BN	00	=		
XA541	TDD	BN	02B	( )	01		TXDV1B 01 02B	
XA541	TDD	BP	04B	TTS2BP	00	=		
XA541	TDD	BP	04A	(09)	01		TXRS0B 04 04A	
XA541	TDD	BQ	03A	TTS2BQ	00	=		TRANSPORT 2 SELECTED NEW
XA541	TDD	BQ	02A	(07)	01		SPI024 03 02A	
XA406	TQ2	A2	02B	TTS2BR	00	=		
XA406	TQ2	A2	04A	(01)	01		TTS2BS TTS21A 04 04A 05 03B	
XA407	TQ2	A2	02B	TTS2BS	00	=		TRANSPORT 2 SELECTED OLD
XA407	TQ2	A2	04A	(01)	01		TTS2BR TTS20A 04 04A 05 03B	
XA408	TQ2	B4	13B	TTS20A	00	=		
XA408	TQ2	B4	11B	(27)	01		TTS2BQ TTSC90 23 11B 25 12B	
XA408	TQ2	C1	18A	TTS21A	00	=		
XA408	TQ2	C1	19A	(38)	01		TTS2BP TTSC90 40 19A 42 20A	

CONNECTOR	GROUP TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERN	DEFINITION	FACTOR	COMMENT
XA536	TQ2	F2	34A	TTS29A	00	=		
XA536	TQ2	F2	36A	(72)	01		TXROCR TXR1CS 71 36A 73 36B	
XA537	TQ2	F2	34A	TTS290	00	=		
XA537	TQ2	F2	36A	(72)	01		TTS29A SPI025 71 36A 73 36B	
XA542	TDD	A1	06A	TTS3BI	00	=		
				( )	01		TTS390 08 06A	
XA542	TDD	AN	07A	TTS3BN	00	=		
				( )	01		TXDV1B 10 07A	
XA542	TDD	AP	05B	TTS3BP	00	=		
XA542	TDD	AP	05A	(11)	01		TXRS0B 06 05A	
XA542	TDD	AQ	06B	TTS3BQ	00	=		
XA542	TDD	AQ	07B	(13)	01		SPI029 15 07B	TRANSPORT 3 SELECTED NEW
XA406	TQ2	B1	12A	TTS3BR	00	=		
XA406	TQ2	B1	13A	(22)	01		TTS3BS TTS31A 24 13A 26 14A	
XA407	TQ2	B1	12A	TTS3BS	00	=		
XA407	TQ2	B1	13A	(22)	01		TTS3BR TTS30A 24 13A 26 14A	TRANSPORT 3 SELECTED OLD
XA408	TQ2	C2	15A	TTS30A	00	=		
XA408	TQ2	C2	16A	(30)	01		TTS3BQ TTSC90 34 16A 36 17A	
XA408	TQ2	C3	16B	TTS31A	00	=		
XA408	TQ2	C3	14B	(33)	01		TTS3BP TTSC90 29 14B 31 15B	
XA536	TQ2	F3	35A	TTS39A	00	=		
XA536	TQ2	F3	34B	(69)	01		TXROCS TXR1CR 65 34B 74 35B	
XA537	TQ2	F3	35A	TTS390	00	=		
XA537	TQ2	F3	34B	(69)	01		TTS39A SPI025 65 34B 74 35B	



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATION	FACTOR	COMMENT
				TTS4BI	00 =			
XA542	TDD	BI	03B	( )	01	TTS490 05 03B		
				TTS4BN	00 =			
XA542	TDD	BN	02B	( )	01	YXDVB 01 02B		
XA542	TDD	BP	04B	TTS4BP	00 =			
XA542	TDD	BP	04A	(09 )	01	YXRS0B 04 04A		
XA542	TDD	BQ	03A	TTS4BQ	00 =			TRANSPORT 4 SELECTED NEW
XA542	TDD	BQ	02A	(07 )	01	SPI02B 03 02A		
XA406	TQ2	B2	09A	TTS4BR	00 =			
XA406	TQ2	B2	10A	(14 )	01	TTS4BS TTS41A 18 10A 20 11A		
XA407	TQ2	B2	09A	TTS4BS	00 =			TRANSPORT 4 SELECTED OLD
XA407	TQ2	B2	10A	(14 )	01	TTS4BR TTS40A 18 10A 20 11A		
XA408	TQ2	C4	19B	TTS40A	00 =			
XA408	TQ2	C4	17B	(39 )	01	TTS4BQ TTSC90 35 17B 37 18B		
XA408	TQ2	D3	24B	TTS41A	00 =			
XA408	TQ2	D3	22B	(45 )	01	TTS4BP TTSC90 41 22B 43 23B		
XA536	TQ2	F4	39A	TTS49A	00 =			
XA536	TQ2	F4	37A	(80 )	01	TXROCS TXRICS 76 37A 78 38A		
XA537	TQ2	F4	39A	TTS490	00 =			
XA537	TQ2	F4	37A	(80 )	01	TTS49A SPI025 76 37A 78 38A		
				TTOPBI	00 =			
XA542	TDD	DI	10A	( )	01	TRDBPOX 18 10A		
				TTOPBN	00 =			
XA542	TDD	DN	09A	( )	01	TTCP00 14 09A		

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
			AND OR					
XA542	TDD	DP	10B	TT0PBP	00	=		
XA542	TDD	DP	11A	( 21 )	01	=	TTRSOA 20 11A	
XA438	PAR	F1	39A	TT0PBPR	00	=		
XA438	PAR	F1	35B	( 80 )	01	=	TT00BQ TT018Q TT028Q TT038Q TT048Q TT058Q TT068Q TT078Q 71 35B 73 36B 75 37B 77 38B 76 37A 74 36A 72 35A 70 34A	READ REGISTER PARITY CHECKER
XA438	PAR	F1	39B	( )	02	+	TT0PBQ 79 39B	
XA542	TDD	DQ	09B	TT0PBQ	00	=		
XA542	TDD	DQ	08B	( 19 )	01	=	SPI028 17 08B	READ REG BIT P
XA542	TDD	EI	19A	TT00BI	00	=	TRDB00X 40 19A	
XA542	TDD	EN	20A	TT00BN	00	=	TTCP00 42 20A	
XA542	TDD	EP	17B	TT00BP	00	=		
XA542	TDD	EP	18A	( 35 )	01	=	TTRSOA 38 18A	
XA542	TDD	EQ	18B	TT00BQ	00	=		
XA542	TDD	EQ	19B	( 37 )	01	=	SPI029 39 19B	READ REG BIT 0
XA542	TDD	FI	16A	TT01BI	00	=	TRDB10X 34 16A	
XA542	TDD	FN	15A	TT01BN	00	=	TTCP00 30 15A	
XA542	TDD	FP	16B	TT01BP	00	=		
XA542	TDD	FP	17A	( 33 )	01	=	TTRSOA 36 17A	
XA542	TDD	FQ	15B	TT01BQ	00	=		
XA542	TDD	FQ	14B	( 31 )	01	=	SPI028 29 14B	READ REG BIT 1
XA542	TDD	GI	25A	TT02BI	00	=	TRDB20X 54 25A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
				TT02BN	00	=		
XA542	TDD	GN	26A	( )	01		TTCP00 56 26A	
XA542	TDD	GP	25B	TT02BP	00	=		
XA542	TDD	GP	24A	(47 )	01		TTRS0A 52 24A	
XA542	TDD	GQ	26B	TT02BQ	00	=		READ REG BIT 2
XA542	TDD	GQ	27B	(49 )	01		SPI029 51 27B	
				TT03BI	00	=		
XA542	TDD	HI	22A	( )	01		TRDB30X 48 22A	
				TT03BN	00	=		
XA542	TDD	HN	21A	( )	01		TTCP00 46 21A	
XA542	TDD	HP	24B	TT03BP	00	=		
XA542	TDD	HP	23A	(45 )	01		TTRS0A 50 23A	
XA542	TDD	HQ	23B	TT03BQ	00	=		READ REG BIT 3
XA542	TDD	HQ	22B	(43 )	01		SPI028 41 22B	
				TT04BI	00	=		
XA542	TDD	JI	32A	( )	01		TRDB40X 68 32A	
				TT04BN	00	=		
XA542	TDD	JN	33A	( )	01		TTCP10 70 33A	
XA542	TDD	JP	31B	TT04BP	00	=		
XA542	TDD	JP	31A	(59 )	01		TTRS1A 66 31A	
XA542	TDD	JQ	32B	TT04BQ	00	=		READ REG BIT 4
XA542	TDD	JQ	33B	(61 )	01		SPI029 63 33B	
				TT05BI	00	=		
XA542	TDD	KI	29A	( )	01		TRDB50X 62 29A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
				TT058N	00	=		
XA542	TDD	KN	28A	( )	01		TTCP10 60 28A	
XA542	TDD	KP	30B	TT058P	00	=		
XA542	TDD	KP	30A	(57 )	01		TTRS1A 64 30A	
XA542	TDD	KQ	29B	TT058Q	00	=		
XA542	TDD	KQ	28B	(55 )	01		SPI028 53 28B	READ REG BIT 5
				TT068I	00	=		
XA542	TDD	LI	38B	( )	01		TRDB60X 77 38B	
				TT068M	00	=		
XA542	TDD	LN	39B	( )	01		TTCP10 79 39B	
XA542	TDD	LP	37A	TT068P	00	=		
XA542	TDD	LP	37B	(76 )	01		TTRS1A 75 37B	
XA542	TDD	LQ	38A	TT068Q	00	=		
XA542	TDD	LQ	39A	(78 )	01		SPI029 80 39A	READ REG BIT 6
				TT078I	00	=		
XA542	TDD	MI	36A	( )	01		TRDB70X 71 36A	
				TT078N	00	=		
XA542	TDD	MN	34A	( )	01		TTCP10 72 34A	
XA542	TDD	MP	35A	TT078P	00	=		
XA542	TDD	MP	36B	(69 )	01		TTRS1A 73 36B	
XA542	TDD	MQ	35B	TT078Q	00	=		
XA542	TDD	MQ	34B	(74 )	01		SPI028 65 34B	READ REG BIT 7
XA509	TT3	C3	19B	TWDBCA	00	=		
XA509	TT3	C3	16B	(39 )	01		TWDEPR TC71BQ TC72BP 33 16B 35 17B 37 18B	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TWDBCD4

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

LOGIC

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATION	FACTOR	COMMENT
XA543	TLD	C2	15A	TWDBCD4	00 =			WRITE STROBE DRIVER
XA543	TLD	C2	16A	(30)	01	TWDBCO TWRI2S 34 16A 36 17A		
XA511	TQ2	D3	24B	TWDBCO	00 =			
XA511	TQ2	D3	22B	(45)	01	TWDBCA SPI020 41 22B 43 23B		
XA543	TLD	C3	16B	TWDBED4	00 =			WRITE ENABLE DRIVER
XA543	TLD	C3	14B	(33)	01	TWEN30 SPI029 29 14B 31 15B		
				TWDBPB4	00 =			
XA543	TLD	C1	18A	( )	01	TWDBPD 38 18A		WRITE DATA P DRIVERS
XA443	TLD	D4	27B	( )	02 +	UWLCPD 51 27B		
XA543	TLD	C1	18A	TWDBPD	00 =			TWDBPB4 BUSS
XA543	TLD	C1	19A	(38)	01	TW078PR TWDDFO 40 19A 42 20A		
XA517	TQ2	D4	27B	TWDBSA	00 =			
XA517	TQ2	D4	25B	(51)	01	TFPR10X SPI021 47 25B 49 26B		
				TWDBOB4	00 =			
XA543	TLD	A1	05A	( )	01	TWDBOD 06 05A		WRITE DATA O DRIVERS
XA443	TLD	E1	31A	( )	02 +	UWLCOD 66 31A		
XA543	TLD	A1	05A	TWDBOD	00 =			TWDBOB4 BUSS
XA543	TLD	A1	06A	(06)	01	TW00DTA TWDDFO 08 06A 10 07A		
				TWDB1B4	00 =			
XA543	TLD	A2	02B	( )	01	TWDB1D 01 02B		
XA443	TLD	E2	28A	( )	02 +	UWLC1D 60 28A		
XA543	TLD	A2	02B	TWDB1D	00 =			WRITE DATA 1 DRIVERS
XA543	TLD	A2	04A	(01)	01	TW00DTB TWDDFO 04 04A 05 03B		
				TWDB2B4	00 =			
XA543	TLD	A3	04B	( )	01	TWDB2D 09 04B		WRITE DATA 2 DRIVERS
XA443	TLD	E3	30B	( )	02 +	UWLC2D 57 30B		

DRAWING NUMBER 149015-860

UNIT ASSEMBLY NO. 149015 REV. C INDEX TWDB2D

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UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

CONNECTOR	UNIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA543	TLD	A3	04B	TWDB2D	00	=		
XA543	TLD	A3	02A	(09)	01		TW00DTC TWDFE0 03 02A 07 03A	TWDB2B4 BUSS
				TWDB3B4	00	=		
XA543	TLD	A4	07B	( )	01		TW0B3D 15 07B	WRITE DATA 3 DRIVERS
XA443	TLD	E4	33B	( )	02	+	UWLC3D 63 33B	
XA543	TLD	A4	07B	TWDB3D	00	=		
XA543	TLD	A4	05B	(15)	01		TW00DTC TWDFE0 11 05B 13 06B	TWDB3B4 BUSS
				TWDB4B4	00	=		
XA543	TLD	B1	12A	( )	01		TW0B4D 22 12A	WRITE DATA 4 DRIVERS
XA443	TLD	F1	37B	( )	02	+	UWLC4D 75 37B	
XA543	TLD	B1	12A	TWDB4D	00	=		
XA543	TLD	B1	13A	(22)	01		TW04DTA TWDFD0 24 13A 26 14A	TWDB4B4 BUSS
				TWDB5B4	00	=		
XA543	TLD	B2	09A	( )	01		TW0B5D 14 09A	WRITE DATA 5 DRIVERS
XA443	TLD	F2	34A	( )	02	+	UWLC5D 72 34A	
XA543	TLD	B2	09A	TWDB5D	00	=		
XA543	TLD	B2	10A	(14)	01		TW04DTB TWDFD0 18 10A 20 11A	TWDB5B4 BUSS
				TWDB6B4	00	=		
XA543	TLD	B3	10B	( )	01		TW0B6D 21 10B	
XA443	TLD	F3	35A	( )	02	+	UWLC6D 69 35A	
XA543	TLD	B3	10B	TWDB6D	00	=		
XA543	TLD	B3	08B	(21)	01		TW04DTC TWDFD0 17 08B 19 09B	WRITE DATA 6 DRIVERS
				TWDB7B4	00	=		
XA543	TLD	B4	13B	( )	01		TW0B7D 27 13B	WRITE DATA 7 DRIVERS
XA443	TLD	F4	39A	( )	02	+	UWLC7D 80 39A	

DRAWING NUMBER 149015-860

UNIT ASSEMBLY NO. 149015 REV. C INDEX TWDB7D

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UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

FILE IDENT T39C1FC5 DATE 09-03-82 PAGE

CONNECTOR	CHECK TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM NATURE	FACTOR	COMMENT
XA543	TLD	B4	13B	TWDB7D	00 =		
XA543	TLD	B4	11B	(27)	01	TW04DTD TW0DFC 23 11B 25 12B	TWDB7B4 BUSS
XA518	TT3	C2	15B	TWDEEA	00 =		
XA518	TT3	C2	14B	(31)	01	TWDEPR TC70BQ TC73BP 29 14B 30 15A 34 16A	
XA438	PAR	E1	33A	TWDEPR	00 =		WRITE STROBE GENERATED
XA438	PAR	E1	29B	(68)	01	SPI013 SPI014 SPI015 TC83BQ TC84BQ SPI012 SPI003 SPI011 55 29B 59 30B 61 31B 63 32B 64 31A 62 30A 60 29A 57 28A	
XA438	PAR	E1	33B	( )	02 +	SPI008 65 33B	
XA511	TQ2	F1	37B	TWDEEO	00 =		
XA511	TQ2	F1	38B	(75)	01	TWDEEA SPI020 77 38B 79 39B	
XA511	TQ2	F2	34A	TWDDFO	00 =		
XA511	TQ2	F2	36A	(72)	01	TWDEEA SPI020 71 36A 73 36B	
XA506	TQ2	C1	19A	TWENCA	00 =		
XA506	TQ2	C1	19A	(38)	01	TWENCO SPI018 40 19A 42 20A	
XA516	TQ2	F1	37B	TWENCO	00 =		
XA516	TQ2	F1	38B	(75)	01	TWRITP TWRTEP 77 38B 79 39B	
XA515	TD4	B1	11B	TWENOA	00 =		
XA515	TD4	B1	12A	(23)	01	TWENCO TFPR10X TSNC1S TSCL1B 22 12A 24 13A 25 12B 26 14A	
XA507	TS8	A1	05B	TWENOR	00 =		
XA507	TS8	A1	02B	(11)	01	TWENOS TWEN1A TFPR10X TRDY10X TXRS1B SPI018 SPI003 SPI019 01 02B 04 04A 05 03B 06 05A 07 03A 08 06A 10 07A 13 06B	
XA512	TQ2	A1	05A	TWENOS	00 =		WRITE ENABLE F/F
XA512	TQ2	A1	06A	(06)	01	TWENOR TWEN0A 08 06A 10 07A	
XA524	TT3	C2	15B	TWEN1A	00 =		
XA524	TT3	C2	14B	(31)	01	TWENCA TSNC1S TSCL1B 29 14B 30 15A 34 16A	
XA524	TT3	C3	19B	TWEN2A	00 =		
XA524	TT3	C3	18B	(39)	01	TWENCO TFPR10X TSNC1S 33 16B 35 17B 37 18B	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TWEN30

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UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TRFM	DESIGNATOR	FACTOR	COMMENT
XA520	TQ2	E1	31A	TWEN3C	00	=		
XA520	TQ2	E1	32A	(166 )	01		TWEN2A TWENOR 68 32A 70 33A	
XA543	TLD	C4	19B	TWLRC04	00	=		
XA543	TLD	C4	17B	(39 )	01		TWLRC0 SPI029 35 17B 37 18B	
XA525	TD4	F1	37A	TWLRC0	00	=		
XA525	TD4	F1	37B	(76 )	01		TWEN0S TSNC2R TLCC3R SPI013 75 37B 77 38B 78 38A 79 39B	
XA512	TQ2	F2	34A	TWPE0A	00	=		
XA512	TQ2	F2	36A	(72 )	01		TWRITQ TWPE00 71 36A 73 36B	
XA511	TQ2	F3	35A	TWPE00	00	=		
XA511	TQ2	F3	34B	(69 )	01		TRTDPA TNSGJA 65 34B 74 35B	
XA522	TQ2	D4	27B	TWRGRA	00	=		
XA522	TQ2	D4	25B	(51 )	01		TWRG0S TWRG1R 47 25B 49 26B	
XA516	TQ2	E3	30B	TWRG0A	00	=		
XA516	TQ2	E3	28B	(57 )	01		TWRG00 TSTR90 53 28B 55 29B	
XA515	TD4	D1	25B	TWRGOR	00	=		
XA515	TD4	D1	26B	(47 )	01		TWRG0S TWRG1A UINT7A TXRS1B 49 26B 52 24A 54 25A 56 26A	
XA517	TQ2	E3	30B	TWRG0S	00	=		
XA517	TQ2	E3	28B	(57 )	01		TWRGOR TWRG0A 53 28B 55 29B	WRITE DELAY BIT 0
XA524	TT3	D3	27B	TWRG00	00	=		
XA524	TT3	D3	24B	(51 )	01		TWRITP TWRIEP THISPP 45 24B 47 25B 49 26B	
XA516	TQ2	F3	35A	TWRG1A	00	=		
XA516	TQ2	F3	34B	(69 )	01		TWRG2S TSCL3B 65 34B 74 35B	
XA516	TQ2	E4	33B	TWRG1R	00	=		
XA516	TQ2	E4	31B	(63 )	01		TWRG1S TWRG0S 59 31B 61 32B	



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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TWRG1S

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

FILE IDENT

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA517	TQ2	E4	33B	TWRG1S	00	=		WRITE DELAY BIT 1
XA517	TQ2	E4	31B	(63)	01		TWRG1R TWRG2A 59 31B 61 32B	
XA515	TD4	E1	31B	TWRG2A	00	=		
XA515	TD4	E1	32B	(59)	01		TWRG0S TCZR00 TCZR70 TSCL1B 61 32B 66 31A 68 32A 70 33A	
XA518	TT3	F1	36B	TWRG2R	00	=		
XA518	TT3	F1	37B	(73)	01		TWRG2S TWRG3A TXRS1B 75 37B 77 38B 79 39B	
XA410	TD4	F2	35A	TWRG2S	00	=		WRITE DELAY BIT 2
XA410	TD4	F2	36A	(69)	01		TWRG2R TWRG4A TWRG6A TWRG8A 71 36A 72 34A 73 36B 74 35B	
XA517	TQ2	F1	37B	TWRG3A	00	=		
XA517	TQ2	F1	38B	(75)	01		TWRG1R TSCL1B 77 38B 79 39B	
XA515	TD4	C2	16B	TWRG4A	00	=		
XA515	TD4	C2	15A	(33)	01		THISPQ TWRG1S T0800 TSCL1B 30 15A 31 15B 34 16A 36 17A	
XA515	TD4	C1	17B	TWRG6A	00	=		
XA515	TD4	C1	18B	(35)	01		TWR1T0 TWRG1S TC40BP TSCL1B 37 18B 38 18A 40 19A 42 20A	
XA524	TT3	D2	23B	TWRG8A	00	=		
XA524	TT3	D2	22B	(43)	01		TWRG1S TREG00 TSCL1B 41 22B 46 21A 48 22A	
				TWR1EI	00	=		
XA425	TDD	FI	16A	( )	01		TWRT20 34 16A	
				TWR1EN	00	=		
XA425	TDD	FN	15A	( )	01		TXDV1B 30 15A	
XA425	TDD	FP	16B	TWR1EP	00	=		
XA425	TDD	FP	17A	(33)	01		TRDCAB 36 17A	
XA425	TDD	FQ	15B	TWR1EQ	00	=		ERASE COMMAND F/E
XA425	TDD	FQ	14B	(31)	01		SPI006 29 14B	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TWRIRA

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

FILE IDENT T39CIFC5 DATE 09-03-82 PAGE 360

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TEST DESIGNATION	FACTOR	COMMENT
XA505	TQ2	E1	31A	TWRIRA	00 =		
XA505	TQ2	E1	32A	(66)	01	TWRI1S TWRI2R 68 32A 70 33A	
				TWRITI	00 =		
XA425	TDD	HI	22A	( )	01	TWRT10 48 22A	
				TWRITN	00 =		
XA425	TDD	HN	21A	( )	01	TXDV1B 46 21A	
XA425	TDD	HP	24B	TWRITP	00 =		
XA425	TDD	HP	23A	(45)	01	TRDCAB 50 23A	
XA425	TDD	HQ	23B	TWRITQ	00 =		WRITE COMMAND F/E
XA425	TDD	HQ	22B	(43)	01	SPI006 41 22B	
XA428	TQ2	D2	21A	TWRITO	00 =		
XA428	TQ2	D2	22A	(46)	01	TWRITP SPI010 48 22A 50 23A	
XA507	TS8	B1	11B	TWRIOR	00 =		
XA507	TS8	B1	09A	(23)	01	TWRI0S TREN1S TWRI1A SPI020 TXDPEA TXRS1B SPI019 SPI018 14 09A 18 10A 19 09B 20 11A 22 12A 24 13A 25 12B 26 14A	
XA512	TQ2	B1	12A	TWRI0S	00 =		WRITE COUNTER BIT 0
XA512	TQ2	B1	13A	(22)	01	TWRIOR TWRG6A 24 13A 26 14A	
XA504	TS8	E1	31B	TWRI1A	00 =		
XA504	TS8	E1	29B	(59)	01	TLCC0S TWRG2R TMWCBP TWRI1R TSCK1B SPI003 SPI012 SPI018 55 29B 60 28A 61 32B 62 29A 64 30A 66 31A 68 32A 70 33A	
XA510	TT3	D1	23A	TWRI1R	00 =		
XA510	TT3	D1	24A	(50)	01	TWRI1S TWRI3A TWRI0S 52 24A 54 25A 56 26A	
XA511	TQ2	D4	27B	TWRI1S	00 =		WRITE COUNTER BIT 1
XA511	TQ2	D4	25B	(51)	01	TWRI1R TWR12A 47 25B 49 26B	
XA508	TD4	E1	31B	TWRI2A	00 =		
XA508	TD4	E1	32B	(59)	01	TWRI0S TWRI2R TWRI3R TSCL3B 61 32B 66 31A 68 32A 70 33A	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TWRI2R

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

FILE IDENT T39C1FC5 DATE 09-03-82 PAGE

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TRM	RELATOR	FACTOR	COMMENT
XA512	TQ2	E1	31A	TWRI2R	00	=		
XA512	TQ2	E1	32A	(66)	01		TWRI2S TWRI1S 68 32A 70 33A	
XA511	TQ2	E1	31A	TWRI2S	00	=		WRITE COUNTER BIT 2
XA511	TQ2	E1	32A	(66)	01		TWRI2R TWRI4A 68 32A 70 33A	
XA512	TQ2	D4	27B	TWRI3A	00	=		
XA512	TQ2	D4	25B	(51)	01		TWRI3S TSCK3B 47 25B 49 26B	
XA510	TT3	D3	27B	TWRI3R	00	=		
XA510	TT3	D3	24B	(51)	01		TWRI3S TWRI5A TXRS2B 45 24B 47 25B 49 26B	
XA505	TQ2	D3	24B	TWRI3S	00	=		WRITE COUNTER BIT 3
XA505	TQ2	D3	22B	(45)	01		TWRI3R TWRI6A 41 22B 43 23B	
XA518	TT3	C3	19B	TWRI4A	00	=		
XA518	TT3	C3	16B	(39)	01		TWRI1S TCZR70 TSCL1B 33 16B 35 17B 37 18B	
XA511	TQ2	E2	28A	TWRI5A	00	=		
XA511	TQ2	E2	29A	(60)	01		TWRI2R TSCK1B 62 29A 64 30A	
XA510	TT3	D2	23B	TWRI6A	00	=		
XA510	TT3	D2	22B	(43)	01		TWRI2S T198U0 TSCK1B 41 22B 46 21A 48 22A	
XA425	TDD	LI	38B	TWRQCI	00	=		
XA425	TDD	LI		( )	01		SP1007 77 38B	
XA425	TDD	LN	39B	TWRQCN	00	=		
XA425	TDD	LN		( )	01		TWRQ4P 79 39B	
XA425	TDD	LP	37A	TWRQCP	00	=		
XA425	TDD	LP	37B	(76)	01		TWRQ5A 75 37B	
XA425	TDD	LQ	38A	TWRQCQ	00	=		WRITE REQUEST DELAY CONTROL
XA425	TDD	LQ	39A	(78)	01		TXRS2B 80 39A	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TWRQ0A

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UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERMINATION	DESIGNATION	FACTOR	COMMENT
XA507	TS8	C1	17B	TWRQ0A	00 =			
XA507	TS8	C1	15A	(35)	01		TWRITQ TLCCOR TWRQ3P TWRQ4Q TSCL3B TWRQOP TWRQ1P TWRQ2Q 30 15A 31 15B 34 16A 36 17A 37 18B 38 18A 40 19A 42 20A	
				TWRQ0I	00 =			
XA514	TDD	A1	06A	( )	01		TWRQ1P 08 06A	
				TWRQ0N	00 =			
XA514	TDD	AN	07A	( )	01		TSCK1B 10 07A	
XA514	TDD	AP	05B	TWRQOP	00 =			
XA514	TDD	AP	05A	(11)	01		TWRQCP 06 05A	
XA514	TDD	AQ	06B	TWRQOQ	00 =			WRITE REQUEST DELAY BIT 0
XA514	TDD	AQ	07B	(13)	01		SPI020 15 07B	
XA510	TT3	A1	04A	TWRQOR	00 =			
XA510	TT3	A1	05A	(04)	01		TWRQOS TWRQ1A TXRS1B 06 05A 08 06A 10 07A	
XA509	TT3	A1	04A	TWRQOS	00 =			
XA509	TT3	A1	05A	(04)	01		TWRQOR TWRQ6A TWRQ0A 06 05A 08 06A 10 07A	
XA511	TQ2	B3	10B	TWRQ1A	00 =			
XA511	TQ2	B3	08B	(21)	01		TWRQ1S TSCK3B 17 08B 19 09B	
				TWRQ1I	00 =			
XA513	TDD	A1	06A	( )	01		TWRQOQ 08 06A	
				TWRQ1N	00 =			
XA513	TDD	AN	07A	( )	01		TSCK1B 10 07A	
XA513	TDD	AP	05B	TWRQ1P	00 =			
XA513	TDD	AP	05A	(11)	01		TWRQCP 06 05A	
XA513	TDD	AQ	06B	TWRQ1Q	00 =			
XA513	TDD	AQ	07B	(13)	01		SPI020 15 07B	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
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DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TRWQ1R

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UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIG- NATOR	FACTOR	COMMENT
XA510	TT3	A2	03A	TWRQ1R	00	=		
XA510	TT3	AZ	02B	(107)	01		TWRQ1S TWRQ3A TXRS1B 01 02B 03 02A 05 03B	
XA516	TQ2	E1	31A	TWRQ1S	00	=		
XA516	TQ2	E1	32A	(66)	01		TWRQ1R TWRQ2A 68 32A 70 33A	
XA512	TQ2	B2	09A	TWRQ2A	00	=		
XA512	TQ2	B2	10A	(14)	01		TWRQ0S TSCL1B 18 10A 20 11A	
				TWRQ2I	00	=		
XA514	TDD	LI	38B	( )	01		TWRQ0A 77 38B	
				TWRQ2M	00	=		
XA514	TDD	LN	39B	( )	01		TWRQ1P 79 39B	
XA514	TDD	LP	37A	TWRQ2P	00	=		
XA514	TDD	LP	37B	(76)	01		TWRQCP 75 37B	
XA514	TDD	LQ	38A	TWRQ2Q	00	=		
XA514	TDD	LQ	39A	(78)	01		SPI019 80 39A	
XA510	TT3	C2	15B	TWRQ2R	00	=		
XA510	TT3	C2	14B	(31)	01		TWRQ2S TWRQ5A TMR50A 29 14B 30 15A 34 16A	
XA511	TQ2	C1	18A	TWRQ2S	00	=		
XA511	TQ2	C1	19A	(38)	01		TWRQ2R TXED3A 40 19A 42 20A	
XA512	TQ2	B3	10B	TWRQ3A	00	=		
XA512	TQ2	B3	08B	(21)	01		TWRQ0R TSCL1B 17 08B 19 09B	
				TWRQ3I	00	=		
XA513	TDD	BI	03B	( )	01		TWRQ4P 05 03B	
				TWRQ3N	00	=		
XA513	TDD	BN	02B	( )	01		TWRQ2P 01 02B	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TWRQ3P

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA513	TDD	BP	04B	TWRQ3P	00	=		
XA513	TDD	BP	04A	(09)	01		TWRQCP 04 04A	
XA513	TDD	BQ	03A	TWRQ3Q	00	=		
XA513	TDD	BQ	02A	(07)	01		SPI019 03 02A	
XA510	TT3	C3	19B	TWRQ3R	00	=		
XA510	TT3	C3	16B	(39)	01		TWRQ3S TWRQ7A TMR50A 33 16B 35 17B 37 18B	
XA512	TQ2	C1	18A	TWRQ3S	00	=		
XA512	TQ2	C1	19A	(38)	01		TWRQ3R TWRQ4A 40 19A 42 20A	
XA510	TT3	A3	07B	TWRQ4A	00	=		
XA510	TT3	A3	04B	(15)	01		TWRQ2S TMWC8P TSCK18 09 04B 11 05B 13 06B	
				TWRQ4I	00	=		
XA436	TDD	FI	16A	( )	01		TWRQ3Q 34 16A	
				TWRQ4N	00	=		
XA436	TDD	FN	15A	( )	01		TWRQ2P 30 15A	
XA436	TDD	FP	16B	TWRQ4P	00	=		
XA436	TDD	FP	17A	(33)	01		TWRQCP 36 17A	
XA436	TDD	FQ	15B	TWRQ4Q	00	=		
XA436	TDD	FQ	14B	(31)	01		SPI011 29 14B	WRITE REQUEST DELAY BIT 4
XA509	TT3	A2	03A	TWRQ5A	00	=		
XA509	TT3	A2	02B	(07)	01		TWRQ3S TSCK3B SPI020 01 02B 03 02A 05 03B	
XA511	TQ2	B4	13B	TWRQ6A	00	=		
XA511	TQ2	B4	11B	(27)	01		TWRITQ TSTR90 23 11B 25 12B	
XA511	TQ2	C2	15A	TWRQ7A	00	=		
XA511	TQ2	C2	16A	(30)	01		TWRQ2R TSCL1B 34 16A 36 17A	

DRAWING NUMBER 149015-860

UNIT ASSEMBLY NO. 149015 REV. C INDEX TWRQ9A

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

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CONNECTOR	UNIT ASSEMBLY	GROUP	TEST POINTS AND/OR	EQUATION	TERM DESIGNATOR	FACTOR	COMMENT
XA505	TQ2	F1	37B	TWRQ9A	00 =		
XA505	TQ2	F1	38B	(75)	01	TWRQ2Q SPI018 77 38B 79 39B	
XA426	TQ2	D4	27B	TWRT10	00 =		
XA426	TQ2	D4	25B	(51)	01	TXRAF7T TXRAF5T 47 25B 49 26B	
XA426	TQ2	C4	19B	TWRT20	00 =		
XA426	TQ2	C4	17B	(39)	01	TXRAF6T SPI007 35 17B 37 18B	
XA426	TQ2	C3	16B	TWRT30	00 =		
XA426	TQ2	C3	14B	(33)	01	TXRAF5T SPI007 29 14B 31 15B	
XA518	TT3	A1	04A	TWRT6A	00 =		
XA518	TT3	A1	05A	(04)	01	TWRT30 TXR2CS TXDV1B 06 05A 08 06A 10 07A	
XA518	TT3	A2	03A	TWRT7A	00 =		
XA518	TT3	A2	02B	(07)	01	TWRT30 TXR3CS TXDV1B 01 02B 03 02A 05 03B	
XA425	TDD	GI	25A	TWTSTI	00 =		
XA425	TDD	GI		( )	01	TWRT30 54 25A	
XA425	TDD	GN	26A	TWTSTN	00 =		
XA425	TDD	GN		( )	01	TXDV1B 56 26A	
XA425	TDD	GP	25B	TWTSTP	00 =		
XA425	TDD	GP	24A	(47)	01	TRDCAB 52 24A	
XA425	TDD	GQ	26B	TWTSTC	00 =		WRITE TEST COMMANDE/F
XA425	TDD	GQ	27B	(49)	01	SPI007 51 27B	
XA521	TS8	D1	25B	TWTOCA	00 =		WRITE COMMAND IOU TIMEOUT
XA521	TS8	D1	23B	(47)	01	TWRITQ TXONLO TLCCOR TMWCBP TMO2BP TMO3BQ TMO4BP TSCL3B 43 23B 46 21A 48 22A 49 26B 50 23A 52 24A 54 25A 56 26A	
XA423	MUX	D1	27B	TWO0DTA	00 =		WRITE DATA BIT 0 MULTIPLEXER
XA423	MUX	D1	25B	(55)	01	TWO1DTA TWO2DTA TWRI2R TC90BP 51 25B 53 26B 52 25A 49 24B	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TW00DTB

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

FILE IDENT T39C1FC5 DATE 09-03-82 PAGE 366

CONNECTOR	MUX	GROUP	TEST POINTS AND/OR	EQUATION	TERMINAL	RESISTOR	FACTOR	COMMENT
XA423	MUX	D2	31B	TW00DTB	00 =			
XA423	MUX	D2	29B	(61)	01		TW01DTB TW02DTB 57 29B 59 30B	
XA423	MUX	D3	28A	TW00DTC	00 =			
XA423	MUX	D3	26A	(60)	01		TW01DTC TW02DTC 54 26A 56 28B	
XA423	MUX	D4	31A	TW00DTD	00 =			
XA423	MUX	D4	29A	(63)	01		TW01DTD TW02DTD 62 29A 64 30A	
XA421	MUX	E1	34B	TW01DTA	00 =			
XA421	MUX	E1	32A	(73)	01		TRO0BQ TRO8BQ TXGN6A TC84BP 69 32A 71 33B 68 33A 66 32B	
XA421	MUX	E2	37B	TW01DTB	00 =			
XA421	MUX	E2	35B	(79)	01		TRO1BQ TRO9BQ 75 35B 77 36B	
XA421	MUX	E3	36A	TW01DTC	00 =			
XA421	MUX	E3	34A	(74)	01		TRO2BQ TR10BQ 70 34A 72 35A	
XA421	MUX	E4	38B	TW01DTD	00 =			
XA421	MUX	E4	37A	(80)	01		TRO3BQ TR11BQ 76 37A 78 38A	
XA422	MUX	E1	34B	TW02DTA	00 =			
XA422	MUX	E1	32A	(73)	01		TR16BQ TR24BQ TXGN6A TC84BP 69 32A 71 33B 68 33A 66 32B	
XA422	MUX	E2	37B	TW02DTB	00 =			
XA422	MUX	E2	35B	(79)	01		TR17BQ TR25BQ 75 35B 77 36B	
XA422	MUX	E3	36A	TW02DTC	00 =			
XA422	MUX	E3	34A	(74)	01		TR18BQ TR26BQ 70 34A 72 35A	
XA422	MUX	E4	38B	TW02DTD	00 =			
XA422	MUX	E4	37A	(80)	01		TR19BQ TR27BQ 76 37A 78 38A	
XA422	MUX	D1	27B	TW04DTA	00 =			WRITE DATA BIT 4 MULTIPLEXER
XA422	MUX	D1	25B	(55)	01		TW05DTA TW06DTA TWRI2R TC90BP 51 25B 53 26B 52 25A 49 24B	



DRAWING NUMBER 149015-860

UNIT ASSEMBLY NO. 149015 REV. C INDEX TW04DTB

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA422	MUX	D2	31B	TW04DTB	00	=		
XA422	MUX	D2	29B	(61)	01		TW05DTB TW06DTB 57 29B 59 30B	
XA422	MUX	D3	28A	TW04DTC	00	=		
XA422	MUX	D3	26A	(60)	01		TW05DTC TW06DTC 54 26A 56 28B	
XA422	MUX	D4	31A	TW04DTD	00	=		
XA422	MUX	D4	29A	(63)	01		TW05DTD TW06DTD 62 29A 64 30A	
XA423	MUX	E1	34B	TW05DTA	00	=		
XA423	MUX	E1	32A	(73)	01		TR04BQ TRI2BQ TXGN6A TC84BP 69 32A 71 33B 68 33A 66 32B	
XA423	MUX	E2	37B	TW05DTB	00	=		
XA423	MUX	E2	35B	(79)	01		TR05BQ TRI3BQ 75 35B 77 36B	
XA423	MUX	E3	36A	TW05DTC	00	=		
XA423	MUX	E3	34A	(74)	01		TR06BQ TRI4BQ 70 34A 72 35A	
XA423	MUX	E4	38B	TW05DTD	00	=		
XA423	MUX	E4	37A	(80)	01		TR07BQ TRI5BQ 76 37A 78 38A	
XA424	MUX	E1	34B	TW06DTA	00	=		
XA424	MUX	E1	32A	(73)	01		TR20BQ TR28BQ TXGN6A TC84BP 69 32A 71 33B 68 33A 66 32B	
XA424	MUX	E2	37B	TW06DTB	00	=		
XA424	MUX	E2	35B	(79)	01		TR21BQ TR29BQ 75 35B 77 36B	
XA424	MUX	E3	36A	TW06DTC	00	=		
XA424	MUX	E3	34A	(74)	01		TR22BQ TR30BQ 70 34A 72 35A	
XA424	MUX	E4	38B	TW06DTD	00	=		
XA424	MUX	E4	37A	(80)	01		TR23BQ TR31BQ 76 37A 78 38A	
XA438	PAR	C1	20A	TW07BPR	00	=		WRITE DATA PARITY GENERATOR
XA438	PAR	C1	15B	(42)	01		TW00DTA TW00DTB TW00DTC TW00DTD TW04DTA TW04DTB TW04DTC TW04DTD 31 15B 33 16B 35 17B 37 18B 38 18A 36 17A 34 16A 30 15A	
XA438	PAR	C1	19B	( )	02	+	TWRI2R 39 19B	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TXACM84

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

FILE IDENT

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DISCINATOR	FACTOR	COMMENT
				TXACMB4	00 =			
XA444	TLD	C2	15A	( )	01		TXACMD 30 15A	
XA443	TLD	C2	15A	( )	02 +		TXBCMD 30 15A	
XA444	TLD	C2	15A	TXACMD	00 =			TXACMB4 BUSS
XA444	TLD	C2	16A	(30 )	01		TXAIFO TXACMOX 34 16A 36 17A	
XA446	DCF	C5	31B	TXACMDX	00 =			TACMAB BUSS
XA446	DCF	C5	29A	(60 )	01		TXAOEA 52 29A	
XA446	DCF	C6	31A	TXACMOX	00 =			
XA446	DCF	C6	30A	(57 )	01		TXGNIA 54 30A	
XA441	TQ2	E4	33B	TXADEA	00 =			
XA441	TQ2	E4	31B	(63 )	01		TXADE04 SPI017 59 31B 61 32B	
XA441	TQ2	E3	30B	TXADRD	00 =			I/O ADDRESS FOR THIS CONTROLLER
XA441	TQ2	E3	28B	(57 )	01		TXAD0A TXAD1A 53 28B 55 29B	
XA439	TQ2	F3	35A	TXAD0A	00 =			ADDRESS 0 DECODE
XA439	TQ2	F3	34B	(69 )	01		TXROCS TXADE04 65 34B 74 35B	
XA439	TQ2	F4	39A	TXAD1A	00 =			ADDRESS 1 DECODE
XA439	TQ2	F4	37A	(80 )	01		TXR1CS TXADEA 76 37A 78 38A	
				TXAENB4	00 =			
XA444	TLD	C3	16B	( )	01		TXAEND 33 16B	
XA443	TLD	C3	16B	( )	02 +		TXBEND 33 16B	
XA444	TLD	C3	16B	TXAEND	00 =			TXAENB4 BUSS
XA444	TLD	C3	14B	(33 )	01		TXAIFO TXAENOX 29 14B 31 15B	
XA446	DCF	C7	25A	TXAENDX	00 =			TAENAB BUSS
XA446	DCF	C7	29A	(43 )	01		TXAOEA 52 29A	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TXAENOX

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

FILE IDENT

T39CIFC5

DATE 09-03-82

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CONNECTOR	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATION	FACTOR	COMMENT
XA446	DCF	C8 26A	TXAENOX	00 =			
XA446	DCF	C8 28A	(48)	01		TXGN1A 50 28A	
XA440	TQ2	F1 37B	TXAIEA	00 =			PORT A INPUT ENABLE
XA440	TQ2	F1 38B	(75)	01		TXASLOX TXXDRA 77 38B 79 39B	
XA441	TQ2	F1 37B	TXAIE0	00 =			
XA441	TQ2	F1 38B	(75)	01		TXAIEA SPI017 77 38B 79 39B	
XA441	TQ2	F2 34A	TXAIFO	00 =			
XA441	TQ2	F2 36A	(72)	01		TXAIEA SPI017 71 36A 73 36B	
XA446	DCF	C3 30B	TXAINDX	00 =			TAINAR BUSS
XA446	DCF	C3 29A	(55)	01		TXAOEA 52 29A	
XA446	DCF	C4 29B	TXAINOX	00 =			
XA446	DCF	C4 28B	(56)	01		TXO8IO 51 28B	
XA444	TLD	C1 18A	TXAPCB4	00 =			
XA444	TLD	C1 18A	( )	01		TXAPCD 38 18A	
XA443	TLD	C1 18A	( )	02 +		TXBPCD 38 18A	
XA444	TLD	C1 19A	TXAPCD	00 =			TXAPCB4 BUSS
XA444	TLD	C1 19A	(38)	01		TXAIFO TXAPCOX 40 19A 42 20A	
XA446	DCF	C1 25B	TXAPCDX	00 =			TAOPAR BUSS
XA446	DCF	C1 29A	(46)	01		TXAOEA 52 29A	
XA446	DCF	C2 26B	TXAPCOX	00 =			
XA446	DCF	C2 27B	(47)	01		TXDSBPR 49 27B	
XA433	T04	B1 11B	TXARQA	00 =			
XA433	T04	B1 12A	(23)	01		TXASLOX TXINHR DEVINH TXXROQ 22 12A 24 13A 25 12B 26 14A	
XA440	TQ2	F3 35A	TXARQ0	00 =			
XA440	TQ2	F3 34B	(69)	01		TXARQA SPI017 65 34B 74 35B	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TXARSA

LOGIC

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UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
			AND OR	OR					
XA431	TT3	A1	04A		TXARSA	00	=		
XA431	TT3	A1	05A		(04)	01		TXACMOX TXAENOX TXASLOX 06 05A 08 06A 10 07A	PORT A RESET
XA444	TLD	D1	24A		TXAR0D1	00	=		
XA444	TLD	D1	25A		(52)	01		TXADE04 TXARQO 54 25A 56 26A	
XA444	TLD	D2	21A		TXAR1D1	00	=		
XA444	TLD	D2	22A		(46)	01		TXADEA TXARQO 48 22A 50 23A	
XA441	TQ2	E1	31A		TXASLA	00	=		
XA441	TQ2	E1	32A		(66)	01		TXASLOX SPI017 68 32A 70 33A	
XA446	DCF	D5	38B		TXASLDX	00	=		
XA446	DCF	D5	36A		(80)	01		SPI018 72 36A	PORT A SELECT DRI/RCVR
XA446	DCF	D6	38A		TXASLOX	00	=		
XA446	DCF	D6	37A		(76)	01		SPI013 74 37A	
XA445	DCF	D5	38B		TXASTDX4	00	=		
XA445	DCF	D5	36A		(80)	01		TXGN1A 72 36A	PORT A STATUS DRI/RCVR
XA445	DCF	D6	36A		TXASTOX	00	=		
XA445	DCF	D6	37A		(76)	01		TXASLOX 74 37A	
					TXA0CB4	00	=		
XA444	TLD	A1	05A		( )	01		TXA0CD 06 05A	
XA443	TLD	A1	05A		( )	02	+	TXB0CD 06 05A	
XA444	TLD	A1	05A		TXA0CD	00	=		
XA444	TLD	A1	06A		(06)	01		TXA1E0 TXA0COX 08 06A 10 07A	TXA0CB4 BUSS
XA446	DCF	A1	02B		TXA0CDX	00	=		
XA446	DCF	A1	05A		(07)	01		TXA0EA 06 05A	TA00AB BUSS
XA446	DCF	A2	03B		TXA0COX	00	=		
XA446	DCF	A2	04B		(09)	01		TXS031U 11 04B	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TXA0EA

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

FILE IDENT T39C1FC5 DATE 09-03-82 PAGE 371

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA433	TD4	A1	05B	TXA0EA	00	=		PORT A OUTPUT ENABLE
XA433	TD4	A1	05A	(11)	01		TXASLOX TXINHR DEVINH TXXC50 06 05A 08 06A 10 07A 13 06B	
				TXA1CB4	00	=		
XA444	TLD	A2	02B	( )	01		TXA1CD 01 02B	
XA443	TLD	A2	02B	( )	02	+	TXB1CD 01 02B	
XA444	TLD	A2	02B	TXA1CD	00	=		TXA1CB4 BUSS
XA444	TLD	A2	04A	(01)	01		TXA1E0 TXA1COX 04 04A 05 03B	
XA446	DCF	A3	07B	TXA1CDX	00	=		TA01AB BUSS
XA446	DCF	A3	05A	(17)	01		TXA0EA 06 05A	
XA446	DCF	A4	06B	TXA1COX	00	=		
XA446	DCF	A4	05B	(15)	01		TXS032U 13 05B	
				TXA2CB4	00	=		
XA444	TLD	A3	04B	( )	01		TXA2CD 09 04B	
XA443	TLD	A3	04B	( )	02	+	TXB2CD 09 04B	
XA444	TLD	A3	04B	TXA2CD	00	=		TXA2CB4 BUSS
XA444	TLD	A3	02A	(09)	01		TXA1E0 TXA2COX 03 02A 07 03A	
XA446	DCF	A5	08B	TXA2CDX	00	=		TA02AB BUSS
XA446	DCF	A5	05A	(14)	01		TXA0EA 06 05A	
XA446	DCF	A6	07A	TXA2COX	00	=		
XA446	DCF	A6	06A	(10)	01		TXS033U 08 06A	
				TXA3CB4	00	=		
XA444	TLD	A4	07B	( )	01		TXA3CD 15 07B	
XA443	TLD	A4	07B	( )	02	+	TXB3CD 15 07B	
XA444	TLD	A4	07B	TXA3CD	00	=		TXA3CB4 BUSS
XA444	TLD	A4	05B	(15)	01		TXA1E0 TXA3COX 11 05B 13 06B	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TXA3CDX

LOGIC

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UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
			AND	OR					
XA446	DCF	A7	02A		TXA3CDX	00 =			
XA446	DCF	A7	05A		(01)	01	TXA0EA 06 05A	TA03RB BUSS	
XA446	DCF	A8	03A		TXA3COX	00 =			
XA446	DCF	A8	04A		(03)	01	TXS034U 04 04A		
					TXA4CB4	00 =			
XA444	TLD	B1	12A		( )	01	TXA4CD 22 12A		
XA443	TLD	B1	12A		( )	02 +	TXB4CD 22 12A		
XA444	TLD	B1	12A		TXA4CD	00 =			
XA444	TLD	B1	13A		(22)	01	TXA1EO TXA4COX 24 13A 26 14A	TXA4CB4 BUSS	
XA446	DCF	B1	10B		TXA4CDX	00 =			
XA446	DCF	B1	13A		(27)	01	TXA0EA 36 13A	TA04AB BUSS	
XA446	DCF	B2	11B		TXA4COX	00 =			
XA446	DCF	B2	12B		(29)	01	TXS471U 31 12B		
					TXA5CB4	00 =			
XA444	TLD	B2	09A		( )	01	TXA5CD 14 09A		
XA443	TLD	B2	09A		( )	02 +	TXB5CD 14 09A		
XA444	TLD	B2	09A		TXA5CD	00 =			
XA444	TLD	B2	10A		(14)	01	TXA1EO TXA5COX 18 10A 20 11A	TXA5CB4 BUSS	
XA446	DCF	B3	15B		TXA5CDX	00 =			
XA446	DCF	B3	13A		(37)	01	TXA0EA 36 13A	TA05AB BUSS	
XA446	DCF	B4	14B		TXA5COX	00 =			
XA446	DCF	B4	13B		(35)	01	TXS472U 33 13B		
					TXA6CB4	00 =			
XA444	TLD	B3	10B		( )	01	TXA6CD 21 10B		
XA443	TLD	B3	10B		( )	02 +	TXB6CD 21 10B		

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TXA6CD

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

FILE IDENT T39C1FC5 DATE 09-03-82 PAGE

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CONNECTOR	UNIT	GROUP	TEST POINTS AND OR	EQUATION	TR	DESIGNATOR	FACTOR	COMMENT
XA444	TLD	B3	10B	TXA6CD	00	=		
XA444	TLD	B3	08B	(21)	01		TXA6CB4 BUSS TXA1E0 TXA6COX 17 08B 19 09B	
XA446	DCF	B5	16A	TXA6CDX	00	=		
XA446	DCF	B5	13A	(41)	01		TA06AB BUSS TXA0EA 36 13A	
XA446	DCF	B6	15A	TXA6COX	00	=		
XA446	DCF	B6	14A	(40)	01		TXS473U 38 14A	
				TXA7CB4	00	=		
XA444	TLD	B4	13B	( )	01		TXA7CD 27 13B	
XA443	TLD	B4	13B	( )	02	+	TXB7CD 27 13B	
XA444	TLD	B4	13B	TXA7CD	00	=		
XA444	TLD	B4	11B	(27)	01		TXA7CB4 BUSS TXA1E0 TXA7COX 23 11B 25 12B	
XA446	DCF	B7	10A	TXA7CDX	00	=		
XA446	DCF	B7	13A	(23)	01		TA07AB BUSS TXA0EA 36 13A	
XA446	DCF	B8	11A	TXA7COX	00	=		
XA446	DCF	B8	12A	(30)	01		TXS474U 34 12A	
XA443	TLD	C2	15A	TXBCMD	00	=		
XA443	TLD	C2	16A	(30)	01		TXACMB4 BUSS TXB1F0 TXBCMOX 34 16A 36 17A	
XA445	DCF	C5	31B	TXBCMDX	00	=		
XA445	DCF	C5	29A	(60)	01		TACMB BUSS TXB0EA 52 29A	
XA445	DCF	C6	31A	TXBCMOX	00	=		
XA445	DCF	C6	30A	(57)	01		TXGN1A 54 30A	
XA443	TLD	C3	16B	TXBEND	00	=		
XA443	TLD	C3	14B	(33)	01		TXAENB4 BUSS TXB1F0 TXBENOX 29 14B 31 15B	
XA445	DCF	C7	25A	TXBENDX	00	=		
XA445	DCF	C7	29A	(43)	01		TAENB BUSS TXB0EA 52 29A	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TXBENOX

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

FILE IDENT T39CIFC5 DATE 09-03-82 PAGE 374

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESIGNATION	FACTOR	COMMENT
			AND	OR					
XA445	DCF	C8	26A		TXBENOX	00	=		
XA445	DCF	C8	28A		(48)	01		TXGN1A 50 28A	
XA440	TQ2	F2	34A		TXBIEA	00	=		
XA440	TQ2	F2	36A		(72)	01		TXBSLOX TXXDRA 71 36A 73 36B	PORT B INPUT ENABLE
XA441	TQ2	F3	35A		TXBIEO	00	=		
XA441	TQ2	F3	34B		(69)	01		TXBIEA SPI017 65 34B 74 35B	
XA441	TQ2	F4	39A		TXBIFO	00	=		
XA441	TQ2	F4	37A		(80)	01		TXBIEA SPI017 76 37A 78 38A	
XA445	DCF	C3	30B		TXBINDX	00	=		
XA445	DCF	C3	29A		(55)	01		TXBOEA 52 29A	TAINBB BUSS
XA445	DCF	C4	29B		TXBINOX	00	=		
XA445	DCF	C4	28B		(56)	01		TXDBIO 51 28B	
XA443	TLD	C1	18A		TXBPCD	00	=		
XA443	TLD	C1	19A		(38)	01		TXBIFO TXBPCOX 40 19A 42 20A	TXAPCB4 BUSS
XA445	DCF	C1	25B		TXBPCDX	00	=		
XA445	DCF	C1	29A		(46)	01		TXBOEA 52 29A	TAOPBB BUSS
XA445	DCF	C2	26B		TXBPCOX	00	=		
XA445	DCF	C2	27B		(47)	01		TXDSBPR 49 27B	
XA433	TD4	B2	10B		TXBRQA	00	=		
XA433	TD4	B2	09A		(21)	01		TXBSLOX TXINHR DEVINH TXXROO 14 09A 18 10A 19 09B 20 11A	
XA440	TQ2	F4	39A		TXBRQO	00	=		
XA440	TQ2	F4	37A		(80)	01		TXBRQA SPI017 76 37A 78 38A	
XA431	TT3	A2	03A		TXBRSA	00	=		
XA431	TT3	A2	02B		(07)	01		TXBCMOX TXBENOX TXBSLOX 01 02B 03 02A 05 03B	PORT B RESET



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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TXBROD1

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

FILE IDENT T39C1FC5 DATE 09-03-82 PAGE 375

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	Y	DISC. POSITION	FACTOR	COMMENT
XA444	TLD	03	24B	TXBR0D1	00 =			
XA444	TLD	03	22B	(45)	01		TXADE04 TXBRQ0 41 22B 43 23B	
XA444	TLD	04	27B	TXBR1D1	00 =			
XA444	TLD	04	25B	(51)	01		TXADEA TXBRQ0 47 25B 49 26B	
XA441	TQ2	E2	28A	TXBSLA	00 =			
XA441	TQ2	E2	29A	(60)	01		TXBSLOX SPI017 62 29A 64 30A	
XA446	DCF	D7	33A	TXBSLDX	00 =			PORT B SELECT DRI/RCVR
XA446	DCF	D7	36A	(61)	01		SPI018 72 36A	
XA446	DCF	D8	34A	TXBSLOX	00 =			
XA446	DCF	D8	35A	(68)	01		SPI015 70 35A	
XA445	DCF	D7	33A	TXBSTDX4	00 =			PORT B STATUS DRI/RCVR
XA445	DCF	D7	36A	(61)	01		YXGNIA 72 36A	
XA445	DCF	D8	34A	TXBSTOX	00 =			
XA445	DCF	D8	35A	(68)	01		TXBSLOX 70 35A	
XA443	TLD	A1	05A	TXB0CD	00 =			TXA0CB4 BUSS
XA443	TLD	A1	06A	(06)	01		TXB1E0 TXB0COX 08 06A 10 07A	
XA445	DCF	A1	02B	TXB0CDX	00 =			TA00BB BUSS
XA445	DCF	A1	05A	(07)	01		TXB0EA 06 05A	
XA445	DCF	A2	03B	TXB0COX	00 =			
XA445	DCF	A2	04B	(09)	01		TXS031U 11 04B	
XA433	TD4	A2	04B	TXB0EA	00 =			PORT B OUTPUT ENABLE
XA433	TD4	A2	02B	(09)	01		TXBSLOX TXINHR DEVINH TXXC50 01 02B 04 04A 05 03B 07 03A	
XA443	TLD	A2	02B	TXB1CD	00 =			TXA1CB4 BUSS
XA443	TLD	A2	04A	(01)	01		TXB1E0 TXB1COX 04 04A 05 03B	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TXB1CDX

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

FILE IDENT T39C1FC5 DATE 09-03-82 PAGE 376

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA445	DCF	A3	07B	TXB1CDX	00	=		
XA445	DCF	A3	05A	(17)	01		TXB0EA 06 05A	TA01BB BUSS
XA445	DCF	A4	06B	TXB1COX	00	=		
XA445	DCF	A4	05B	(15)	01		TXS032U 13 05B	
XA443	TLD	A3	04B	TXB2CD	00	=		
XA443	TLD	A3	02A	(09)	01		TXB1E0 TXB2COX 03 02A 07 03A	TXA2CB4 BUSS
XA445	DCF	A5	08B	TXB2CDX	00	=		
XA445	DCF	A5	05A	(14)	01		TXB0EA 06 05A	TA02BB BUSS
XA445	DCF	A6	07A	TXB2COX	00	=		
XA445	DCF	A6	06A	(10)	01		TXS033U 08 06A	
XA443	TLD	A4	07B	TXB3CD	00	=		
XA443	TLD	A4	05B	(15)	01		TXB1E0 TXB3COX 11 05B 13 06B	TXA3CB4 BUSS
XA445	DCF	A7	02A	TXB3CDX	00	=		
XA445	DCF	A7	05A	(01)	01		TXB0EA 06 05A	TA03BB BUSS
XA445	DCF	A8	03A	TXB3COX	00	=		
XA445	DCF	A8	04A	(03)	01		TXS034U 04 04A	
XA443	TLD	B1	12A	TXB4CD	00	=		
XA443	TLD	B1	13A	(22)	01		TXB1E0 TXB4COX 24 13A 26 14A	TXA4CB4 BUSS
XA445	DCF	B1	10B	TXB4CDX	00	=		
XA445	DCF	B1	13A	(27)	01		TXB0EA 36 13A	TA04BB BUSS
XA445	DCF	B2	11B	TXB4COX	00	=		
XA445	DCF	B2	12B	(29)	01		TXS471U 31 12B	
XA443	TLD	B2	09A	TXB5CD	00	=		
XA443	TLD	B2	10A	(14)	01		TXB1E0 TXB5COX 18 10A 20 11A	TXA5CB4 BUSS

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TXB5CDX

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

FILE IDENT T39C1FC5

DATE 09-03-82 PAGE

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERY	DESIGNATOR	FACTOR	COMMENT
XA445	DCF	B3	15B	TXB5CDX	00	=		TA05BB BUSS
XA445	DCF	B3	13A	(37)	01		TXB0EA 36 13A	
XA445	DCF	B4	14B	TXB5COX	00	=		
XA445	DCF	B4	13B	(35)	01		TXS472U 33 13B	
XA443	TLD	B3	10B	TXB6CD	00	=		TXA6CB4 BUSS
XA443	TLD	B3	08B	(21)	01		TXB1E0 TXB6COX 17 08B 19 09B	
XA445	DCF	B5	16A	TXB6CDX	00	=		TA06BB BUSS
XA445	DCF	B5	13A	(41)	01		TXB0EA 36 13A	
XA445	DCF	B6	15A	TXB6COX	00	=		
XA445	DCF	B6	14A	(40)	01		TXS473U 38 14A	
XA443	TLD	B4	13B	TXB7CD	00	=		TXA7CB4 BUSS
XA443	TLD	B4	11B	(27)	01		TXB1E0 TXB7COX 23 11B 25 12B	
XA445	DCF	B7	10A	TXB7CDX	00	=		TA07BB BUSS
XA445	DCF	B7	13A	(23)	01		TXB0EA 36 13A	
XA445	DCF	B8	11A	TXB7COX	00	=		
XA445	DCF	B8	12A	(30)	01		TXS474U 34 12A	
XA433	TD4	E1	31B	TXCA0A	00	=		
XA433	TD4	E1	32B	(59)	01		TXADRO TXRCMS TXX050 TXROPA 61 32B 66 31A 68 32A 70 33A	
XA437	TS8	D1	25B	TXCA1A	00	=		
XA437	TS8	D1	23B	(47)	01		TXCMAS TXXB10 TXXAOP TXXA1Q TXRPCS TXROCS TXR1CR TXR2CR 43 23B 46 21A 48 22A 49 26B 50 23A 52 24A 54 25A 56 26A	
XA426	TQ2	E1	31A	TXCA10	00	=		
XA426	TQ2	E1	32A	(66)	01		TXCA1A SPI007 68 32A 70 33A	
XA433	TD4	E2	30B	TXCMAR	00	=		
XA433	TD4	E2	29B	(57)	01		TXCMAS TXXB2A TXX04A TXRS0B 55 29B 60 28A 62 29A 64 30A	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TXCMAS

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

FILE IDENT T39C1FC5 DATE 09-03-82 PAGE

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CONNECTOR	ENCLAVE TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERMINAL	DISC. NAME	FACTOR	COMMENT
XA432	TQ2	E1	31A	TXCMAS	00	=		
XA432	TQ2	E1	32A	(66)	01		TXCMAR TXCA0A 68 32A 70 33A	ADDRESS STORE E/F
XA412	TQ2	F3	35A	TXCP1A	00	=		
XA412	TQ2	F3	34B	(69)	01		TX1MAP TX1MBQ 65 34B 74 35B	CLOCK PHASE 1
XA411	TQ2	B3	10B	TXCP10	00	=		
XA411	TQ2	B3	08B	(21)	01		TXCP1A SPI001 17 08B 19 09B	
XA412	TQ2	F4	39A	TXCP3A	00	=		
XA412	TQ2	F4	37A	(80)	01		TX1MAQ TX1MBP 76 37A 78 38A	CLOCK PHASE 3
XA411	TQ2	B4	13B	TXCP30	00	=		
XA411	TQ2	B4	11B	(27)	01		TXCP3A SPI001 23 11B 25 12B	
XA430	TT3	E1	30A	TXDBIA	00	=		
XA430	TT3	E1	31A	(64)	01		TXXDSQ TXXDOP TXXDIQ 66 31A 68 32A 70 33A	
XA431	TT3	E1	30A	TXDBIO	00	=		
XA431	TT3	E1	31A	(64)	01		TXXCIP TXXDIP TXDBIA 66 31A 68 32A 70 33A	
XA424	MUX	D1	27B	TXDBOTA	00	=		
XA424	MUX	D1	25B	(55)	01		TBOT10X TXDB01X TXXCIO TXXDIP 51 25B 53 26B 52 25A 49 24B	INPUT MULTIPLEXER BITS 0-3
XA424	MUX	D2	31B	TXDBOTB	00	=		
XA424	MUX	D2	29B	(61)	01		TEOT10X TXDB11X 57 29B 59 30B	
XA424	MUX	D3	28A	TXDBOTC	00	=		
XA424	MUX	D3	26A	(60)	01		TS26CQ TXDB21X 54 26A 56 28B	
XA424	MUX	D4	31A	TXDBOTD	00	=		
XA424	MUX	D4	29A	(63)	01		TRDY1A TXDB31X 62 29A 64 30A	
XA421	MUX	A1	02A	TXDB01X	00	=		
XA421	MUX	A1	02B	(04)	01		TR00BQ TR24BQ TR08BQ TR16BQ TTS1B5 TTS4B5 TTS2B5 TTS3B5 03 02B 05 03B 07 04B 09 05B 11 06B 15 07B 18 09A 17 08B	INPUT MUX BIT 0

DRAWING NUMBER 149015-860

UNIT ASSEMBLY NO. 149015 REV. C INDEX TXDB02X

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

FILE IDENT T39C1FC5 DATE 09-03-82 PAGE

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DISSEMINATOR	FACTOR	COMMENT
XA421	MUX	A2	03A	TXDB02X	00	=		
XA421	MUX	A2	06A	(06)	01		TXXD1Q TXXD0Q TXXDSQ TXGN5A 14 06A 10 05A 08 04A 13 07A	
XA421	MUX	B1	10A	TXDB11X	00	=		
XA421	MUX	B1	09B	(20)	01		TR01BQ TR25BQ TR09BQ TR17BQ TRDY10X URDY40X URDY20X URDY30X 19 09B 21 10B 23 11B 25 12B 29 13B 31 14B 34 15B 33 16A	
XA421	MUX	B2	11A	TXDB12X	00	=		
XA421	MUX	B2	14A	(22)	01		TXXD1Q TXXD0Q TXXDSQ TXGN5A 27 14A 26 13A 24 12A 30 15A	
XA422	MUX	A1	02A	TXDB21X	00	=		
XA422	MUX	A1	02B	(04)	01		TR02BQ TR26BQ TR10BQ TR18BQ TWDBSA UFPR40X TADSA0X UFPR30X 03 02B 05 03B 07 04B 09 05B 11 06B 15 07B 18 09A 17 08B	
XA422	MUX	A2	03A	TXDB22X	00	=		
XA422	MUX	A2	06A	(06)	01		TXXD1Q TXXD0Q TXXDSQ TXGN5A 14 06A 10 05A 08 04A 13 07A	
XA422	MUX	B1	10A	TXDB31X	00	=		
XA422	MUX	B1	09B	(20)	01		TR03BQ TR27BQ TR11BQ TR19BQ TREW10X UREW40X TADS80X UREW30X 19 09B 21 10B 23 11B 25 12B 29 13B 31 14B 34 15B 33 16A	
XA422	MUX	B2	11A	TXDB32X	00	=		
XA422	MUX	B2	14A	(22)	01		TXXD1Q TXXD0Q TXXDSQ TXGN5A 27 14A 26 13A 24 12A 30 15A	
XA421	MUX	D1	27B	TXDB4TA	00	=		INPUT MULTIPLEXER BITS 4-7
XA421	MUX	D1	25B	(55)	01		TS2890 TXDB41X TXXC1Q TXXDIP 51 25B 53 26B 52 25A 49 24B	
XA421	MUX	D2	31B	TXDB4TB	00	=		
XA421	MUX	D2	29B	(61)	01		TS29CQ TXDB51X 57 29B 59 30B	
XA421	MUX	D3	28A	TXDB4TC	00	=		
XA421	MUX	D3	26A	(60)	01		TS30CQ TXDB61X 54 26A 56 28B	
XA421	MUX	D4	31A	TXDB4TD	00	=		
XA421	MUX	D4	29A	(63)	01		TS31CQ TXDB71X 62 29A 64 30A	
XA423	MUX	A1	02A	TXDB41X	00	=		
XA423	MUX	A1	02B	(04)	01		TR04BQ TR28BQ TR12BQ TR20BQ TBOT10X UBOT40X TADSC0X UBOT30X 03 02B 05 03B 07 04B 09 05B 11 06B 15 07B 18 09A 17 08B	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TXDB42X

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

FILE IDENT T39C1FC5 DATE 09-03-82 PAGE 380

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM DESIGNATION	FACTOR	COMMENT
XA423	MUX	A2	03A	TXDB42X	00 =		
XA423	MUX	A2	06A	(06)	01	TXXD1Q TXXD0Q TXXDSQ TXGN5A 14 06A 10 05A 08 04A 13 07A	
XA423	MUX	B1	10A	TXDB51X	00 =		
XA423	MUX	B1	09B	(20)	01	TR05BQ TR29BQ TR13BQ TR21BQ TE0T10X UE0T40X TADSD0X UE0T30X 19 09B 21 10B 23 11B 25 12B 29 13B 31 14B 34 15B 33 16A	
XA423	MUX	B2	11A	TXDB52X	00 =		
XA423	MUX	B2	14A	(22)	01	TXXD1Q TXXD0Q TXXDSQ TXGN5A 27 14A 26 13A 24 12A 30 15A	
XA424	MUX	A1	02A	TXDB61X	00 =		
XA424	MUX	A1	02B	(04)	01	TR06BQ TR30BQ TR14BQ TR22BQ TXGN1A TS30BQ TS14BQ TSBZY0 03 02B 05 03B 07 04B 09 05B 11 06B 15 07B 18 09A 17 08B	
XA424	MUX	A2	03A	TXDB62X	00 =		
XA424	MUX	A2	06A	(06)	01	TXXD1Q TXXD0Q TXXDSQ TXGN5A 14 06A 10 05A 08 04A 13 07A	
XA424	MUX	B1	10A	TXDB71X	00 =		
XA424	MUX	B1	09B	(20)	01	TR07BQ TR31BQ TR15BQ TR23BQ TXGN1A TS31BQ TS1590 TS23BQ 19 09B 21 10B 23 11B 25 12B 29 13B 31 14B 34 15B 33 16A	INPUT MIX BIT 7
XA424	MUX	B2	11A	TXDB72X	00 =		
XA424	MUX	B2	14A	(22)	01	TXXD1Q TXXD0Q TXXDSQ TXGN5A 27 14A 26 13A 24 12A 30 15A	
XA439	TQ2	E1	31A	TXDEVA	00 =		
XA439	TQ2	E1	32A	(66)	01	TXR3CS SPI011 68 32A 70 33A	*
XA429	TD4	B2	10B	TXDEVR	00 =		
XA429	TD4	B2	09A	(21)	01	TXDEVS TXX80A TXXB3A TXRS0B 14 09A 18 10A 19 09B 20 11A	
XA426	TQ2	E4	33B	TXDEVS	00 =		
XA426	TQ2	E4	31B	(63)	01	TXDEVR TXDVOA 59 31B 61 32B	DEVICE COMMAND STORE E/F
XA426	TQ2	E2	28A	TXDEVO	00 =		
XA426	TQ2	E2	29A	(60)	01	TXDEVA SPI007 62 29A 64 30A	
XA428	TQ2	E2	28A	TXDIRA	00 =		
XA428	TQ2	E2	29A	(60)	01	TXXD1Q TXXCS0 62 29A 64 30A	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TXDPEA

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DIS- NATOR	FACTOR	COMMENT
XA429	TD4	E1	31B	TXDPEA	00	=		RECEIVE DATA PARITY ERROR
XA429	TD4	E1	32B	(59)	01		TWRITO TXENAS TXROPPR TXXA50 61 32B 66 31A 68 32A 70 33A	
XA438	PAR	B1	14A	TXDSRPR	00	=		
XA438	PAR	B1	09B	(25)	01		TXS031U TXS032U TXS033U TXS034U TXS471U TXS472U TXS473U TXS474U 17 09B 19 10B 21 11B 23 12B 24 12A 22 11A 20 10A 18 09A	
XA438	PAR	B1	13B	( )	02	+	TXXCIQ 27 13B	
XA426	TQ2	A1	05A	TXDVCO	00	=		
XA426	TQ2	A1	06A	(06)	01		TXR090T SPI007 08 06A 10 07A	
XA429	TD4	B1	11B	TXDVSR	00	=		
XA429	TD4	B1	12A	(23)	01		TXDVSS TXXB0A TXXB3A TXRSOB 22 12A 24 13A 25 12B 26 14A	
XA426	TQ2	E3	30B	TXDVSS	00	=		DEV COMMAND STOP STORE F/F
XA426	TQ2	E3	28B	(57)	01		TXDVSR TXDV5A 53 28B 55 29B	
XA429	TD4	A1	05B	TXDV0A	00	=		
XA429	TD4	A1	05A	(11)	01		TXCA10 TXDEVO TXDVCO TBUSYA 06 05A 08 06A 10 07A 13 06B	
XA437	TS8	C1	17B	TXDV1A	00	=		
XA437	TS8	C1	15A	(35)	01		TXDEVS TXXB20 TXXAOP TXXA1Q TXROPA TXR097T SPI006 SPI011 30 15A 31 15B 34 16A 36 17A 37 18B 38 18A 40 19A 42 20A	
				TXDV1B	00	=		
XA439	TQ2	D1	24A	( )	01		TXDV10 TXDV20 TXDV30 TXDV40 52 24A 46 21A 45 24B 51 27B	
XA439	TQ2	D1	24A	TXDV10	00	=		TXDV1B BUSS
XA439	TQ2	D1	25A	(52)	01		TXDV1A SPI011 54 25A 56 26A	
XA439	TQ2	D2	21A	TXDV20	00	=		TXDV1B BUSS
XA439	TQ2	D2	22A	(46)	01		TXDV1A SPI011 48 22A 50 23A	
XA439	TQ2	D3	24B	TXDV30	00	=		TXDV1B BUSS
XA439	TQ2	D3	22B	(45)	01		TXDV1A SPI011 41 22B 43 23B	
XA439	TQ2	D4	27B	TXDV40	00	=		TXDV1B BUSS
XA439	TQ2	D4	25B	(51)	01		TXDV1A SPI011 47 25B 49 26B	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TXDV5A

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA430	TT3	A1	04A	TXDV5A	00	=		
XA430	TT3	A1	05A	(04)	01		TXCA10 TXDEVO TXDVCO 06 05A 08 06A 10 07A	
XA433	TD4	D1	25B	TXEA0A	00	=		
XA433	TD4	D1	26B	(47)	01		TXADRO TXRENS TXX050 TXROPA 49 26B 52 24A 54 25A 56 26A	
XA432	TQ2	C1	18A	TXEA00	00	=		
XA432	TQ2	C1	19A	(38)	01		TXEA0A SPI010 40 19A 42 20A	
XA430	TT3	A3	07B	TXEBOA	00	=		
XA430	TT3	A3	04B	(15)	01		TXCA10 TXDEVA TXE0B0 09 04B 11 05B 13 06B	
				TXEBOJ	00	=		
XA436	TDD	EI	19A	( )	01		TXGN1A 40 19A	
				TXEBOH	00	=		
XA436	TDD	EN	20A	( )	01		TXEB1A 42 20A	
XA436	TDD	EP	17B	TXEDOP	00	=		
XA436	TDD	EP	18A	(35)	01		TXRS0B 38 18A	
XA436	TDD	EQ	18B	TXEBOQ	00	=		
XA436	TDD	EQ	19B	(37)	01		TXEBOA 39 19B	EOB SYNC COUNTER BIT 0
XA428	TQ2	A2	02B	TXEB1A	00	=		
XA428	TQ2	A2	04A	(01)	01		TXEB10 TXCP30 04 04A 05 03B	
				TXEB1I	00	=		
XA435	TDD	GI	25A	( )	01		TXEBOQ 54 25A	
				TXEB1N	00	=		
XA435	TDD	GN	26A	( )	01		TXCP10 56 26A	
XA435	TDD	GP	25B	TXEB1P	00	=		
XA435	TDD	GP	24A	(47)	01		TXRS0B 52 24A	



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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TXEB1Q

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATION	FACTOR	COMMENT
XA435	TDD	GQ	26B	TXEB1Q	00 =			EOB SYNC COUNTER BIT 1
XA435	TDD	GQ	27B	(49 )	01	SPI011 51 27B		
XA429	TD4	C1	17B	TXED0A	00 =			RECEIVE DATA STROBE BYTE 0
XA429	TD4	C1	18B	(35 )	01	TWRIT0 TXENAS TXXB10 TXXA50 37 18B 38 18A 40 19A 42 20A		
XA429	TD4	C2	16B	TXED1A	00 =			RECEIVE DATA STROBE BYTE 1
XA429	TD4	C2	15A	(33 )	01	TWRIT0 TXENAS TXXB20 TXXA50 30 15A 31 15B 34 16A 36 17A		
XA429	TD4	D1	25B	TXED2A	00 =			RECEIVE DATA STROBE BYTE 2
XA429	TD4	D1	26B	(47 )	01	TWRIT0 TXENAS TXXB30 TXXA50 49 26B 52 24A 54 25A 56 26A		
XA429	TD4	D2	24B	TXED3A	00 =			RECEIVE DATA STROBE BYTE 3
XA429	TD4	D2	23B	(45 )	01	TWRIT0 TXENAS TXXB40 TXXA50 43 23B 46 21A 48 22A 50 23A		
XA433	TD4	D2	24B	TXENAR	00 =			
XA433	TD4	D2	23B	(45 )	01	TXENAS TXXB5A TXX04A TXRS0B 43 23B 46 21A 48 22A 50 23A		
XA432	TQ2	D1	24A	TXENAS	00 =			ENABLE STORE F/F
XA432	TQ2	D1	25A	(52 )	01	TXENAR TXEA0A 54 25A 56 26A		
XA426	TQ2	A3	04B	TXE0B0	00 =			
XA426	TQ2	A3	02A	(09 )	01	TXR092T SPI007 03 02A 07 03A		
XA441	TQ2	D1	24A	TXGN1A	00 =			
XA441	TQ2	D1	25A	(52 )	01	SPI012 SPI017 54 25A 56 26A		
XA506	TQ2	C3	16B	TXGN2A	00 =			
XA506	TQ2	C3	14B	(33 )	01	SPI018 SPI019 29 14B 31 15B		
XA441	TQ2	D2	21A	TXGN4A	00 =			
XA441	TQ2	D2	22A	(46 )	01	T0SCEA4 SPI017 48 22A 50 23A		
XA441	TQ2	D3	24B	TXGN5A	00 =			
XA441	TQ2	D3	22B	(45 )	01	SPI017 SPI012 41 22B 43 23B		

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TXGN6A

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATION	FACTOR	COMMENT
XA441	TQ2	D4	27B	TXGN6A	00	=		
XA441	TQ2	D4	25B	(51)	01		SPI017 SPI012 47 25B 49 26B	
XA426	TQ2	A2	02B	TXHST0	00	=		
XA426	TQ2	A2	04A	(01)	01		TXR091T SPI007 04 04A 05 03B	
XA430	TT3	B1	11A	TXH50A	00	=		
XA430	TT3	B1	12A	(20)	01		TXCA10 TXDEVA TXHST0 22 12A 24 13A 26 14A	
XA426	TQ2	F4	39A	TXINHR	00	=		
XA426	TQ2	F4	37A	(80)	01		TXINHS TXINOA 76 37A 78 38A	
XA430	TT3	F1	36B	TXINHS	00	=		
XA430	TT3	F1	37B	(73)	01		TXINHR TXONL0 TXRS0B 75 37B 77 38B 79 39B	OFF LINE STORE F/E
XA430	TT3	F2	35B	TXINOA	00	=		
XA430	TT3	F2	34B	(74)	01		TXRS0B TXRCMS TXONL0 65 34B 71 36A 72 34A	
XA426	TQ2	B1	12A	TXIRCO	00	=		
XA426	TQ2	B1	13A	(22)	01		TXR094T SPI007 24 13A 26 14A	
XA430	TT3	A2	03A	TXIROA	00	=		
XA430	TT3	A2	02B	(07)	01		TXCA10 TXDEVA TXIRCO 01 02B 03 02A 05 03B	
				TXIROI	00	=		
XA436	TDD	OI	10A	( )	01		TXGN1A 18 10A	
				TXIRON	00	=		
XA436	TDD	ON	09A	( )	01		TXIR1A 14 09A	
XA436	TDD	DP	10B	TXIROP	00	=		
XA436	TDD	DP	11A	(21)	01		TXRS0B 20 11A	
XA436	TDD	DQ	09B	TXIROQ	00	=		
XA436	TDD	DQ	08B	(19)	01		TXIROA 17 08B	IIR SYNC COUNTER BIT 0

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015

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UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERMINAL DESIGNATION	FACTOR	COMMENT
XA428	TQ2	A1	05A	TXIR1A	00 =		
XA428	TQ2	A1	06A	(06)	01	TXIR1Q TXCP30 08 06A 10 07A	
				TXIR1I	00 =		
XA434	TDD	HI	22A	( )	01	TXIROQ 48 22A	
				TXIR1N	00 =		
XA434	TDD	HN	21A	( )	01	TXCP10 46 21A	
XA434	TDD	HP	24B	TXIR1P	00 =		
XA434	TDD	HP	23A	(45)	01	TXRS0B 50 23A	
XA434	TDD	HQ	23B	TXIR1Q	00 =		ITR SYNC COUNTER BIT 1
XA434	TDD	HQ	22B	(43)	01	SPI006 41 22B	
XA428	TQ2	B4	13B	TXPRSA	00 =		
XA428	TQ2	B4	11B	(27)	01	TXPRSOX SPI007 23 11B 25 12B	
XA446	DCF	D3	37B	TXPRSDX	00 =		PORT RESET DRI/RCVR
XA446	DCF	D3	36A	(78)	01	SPI018 72 36A	
XA446	DCF	D4	36B	TXPRSOX	00 =		
XA446	DCF	D4	35B	(75)	01	SPI008 73 35B	
XA427	DBC	B1	12A	TXRAF0T	00 =		DEV COMMAND DECODER A TO F
XA427	DBC	B1	10A	(24)	01	TXR7CS 20 10A	
XA427	DBC	B2	13A	TXRAF1T	00 =		
XA427	DBC	B2	11A	(26)	01	TXR6CS 22 11A	
XA427	DBC	B3	14A	TXRAF2T	00 =		
XA427	DBC	B3	09B	(27)	01	TXR5CS 19 09B	
XA427	DBC	B4	15A	TXRAF3T	00 =		
XA427	DBC	B4	10B	(30)	01	TXR4CR 21 10B	

DRAWING NUMBER 149015-860

UNIT ASSEMBLY NO. 149015 REV. C INDEX TXRAF4T

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UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

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CONNECTOR	PRELIM TYPE	GROUP	TEST POINTS AND/OR	EQUATION	ITER	DESIGNATOR	FACTOR	COMMENT
XA427	DBC	B5	16A	TXRAF4T	00	=		
XA427	DBC	B5		(33)	01		SPA 4T	
XA427	DBC	B6	11B	TXRAF5T	00	=		
XA427	DBC	B6		(23)	01		SPA 5T	
XA427	DBC	B7	12B	TXRAF6T	00	=		
XA427	DBC	B7		(25)	01		SPA 6T	
XA427	DBC	B8	13B	TXRAF7T	00	=		
XA427	DBC	B8		(29)	01		SPA 7T	
XA427	DBC	B9	14B	TXRAF8T	00	=		
XA427	DBC	B9		(31)	01		SPA 8T	
XA427	DBC	B0	15B	TXRAF9T	00	=		
XA427	DBC	B0		(34)	01		SPA 9T	
XA440	TQ2	C2	15A	TXRCMR	00	=		
XA440	TQ2	C2	16A	(30)	01		TXRCMS TXRRSA 34 16A 36 17A	
XA441	TQ2	C2	15A	TXRCMS	00	=		
XA441	TQ2	C2	16A	(30)	01		TXRCMR TXACMB4 34 16A 36 17A	I/O INPUT REG COMMAND BIT
XA440	TQ2	C3	16B	TXRENR	00	=		
XA440	TQ2	C3	14B	(33)	01		TXRENS TXRRSA 29 14B 31 15B	
XA441	TQ2	C3	16B	TXRENS	00	=		
XA441	TQ2	C3	14B	(33)	01		TXRENR TXAENB4 29 14B 31 15B	I/O INPUT REG ENABLE BIT
XA440	TQ2	C1	18A	TXRPCR	00	=		
XA440	TQ2	C1	19A	(38)	01		TXRPCS TXRRSA 40 19A 42 20A	
XA441	TQ2	C1	18A	TXRPCS	00	=		
XA441	TQ2	C1	19A	(38)	01		TXRPCR TXAPCB4 40 19A 42 20A	I/O INPUT REG PARITY BIT

DRAWING NUMBER 149015-860

UNIT ASSEMBLY NO. 149015 REV. C INDEX TXRRSA

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UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERMINAL	DESIGNATION	FACTOR	COMMENT
XA441	TQ2	C4	19B	TXRRSA	00	=		I/O INPUT REG RESET
XA441	TQ2	C4	17B	(39)	01		TXRRS0 SPI017 35 17B 37 18B	
XA440	TQ2	C4	19B	TXRRS0	00	=		
XA440	TQ2	C4	17B	(39)	01		TXXA6A TXRS0B 35 17B 37 18B	
XA428	TQ2	F4	39A	TXRSAA	00	=		TXRS2B BUSS
XA428	TQ2	F4	37A	(80)	01		TXRS00 SPI010 76 37A 78 38A	
XA432	TQ2	E2	28A	TXRS0A	00	=		TXRS0B BUSS
XA432	TQ2	E2	29A	(60)	01		TXRS00 SPI010 62 29A 64 30A	
				TXRS0B	00	=		
XA432	TQ2	E2	28A	( )	01		TXRS0A TXRS1A TXRS2A 60 28A 57 30B 63 33B	
XA437	TS8	B1	11B	TXRS00	00	=		CONTROLLER MASTER RESET
XA437	TS8	B1	09A	(23)	01		TXST1A TXARSA TXBRSA TXPRSA DEVINH SPI003 SPI006 SPI011 14 09A 18 10A 19 09B 20 11A 22 12A 24 13A 25 12B 26 14A	
XA432	TQ2	E3	30B	TXRS1A	00	=		TXRS0B BUSS
XA432	TQ2	E3	28B	(57)	01		TXRS00 SPI010 53 28B 55 29B	
				TXRS1B	00	=		
XA432	TQ2	F1	37B	( )	01		TXRS3A TXRS4A TXRS5A TXRS6A 75 37B 72 34A 69 35A 80 39A	
XA432	TQ2	E4	33B	TXRS2A	00	=		TXRS0B BUSS
XA432	TQ2	E4	31B	(63)	01		TXRS00 SPI010 59 31B 61 32B	
				TXRS2B	00	=		
XA428	TQ2	F1	37B	( )	01		TXRS7A TXRS8A TXRS9A TXRSAA 75 37B 72 34A 69 35A 80 39A	
XA432	TQ2	F1	37B	TXRS3A	00	=		TXRS1B BUSS
XA432	TQ2	F1	38B	(75)	01		TXRS00 SPI010 77 38B 79 39B	
XA432	TQ2	F2	34A	TXRS4A	00	=		TXRS1B BUSS
XA432	TQ2	F2	36A	(72)	01		TXRS00 SPI010 71 36A 73 36B	

DRAWING NUMBER 149015-860

UNIT ASSEMBLY NO. 149015 REV. C INDEX TRRS5A

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

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CONNECTOR	CPL UNIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA432	TQ2	F3	35A	TXRS5A	00	=		
XA432	TQ2	F3	34B	(69)	01		TXRS00 SPI010 65 34B 74 35B	TXRS1B BUSS
XA432	TQ2	F4	39A	TXRS6A	00	=		
XA432	TQ2	F4	37A	(80)	01		TXRS00 SPI010 76 37A 78 38A	TXRS1B BUSS
XA428	TQ2	F1	37B	TXRS7A	00	=		
XA428	TQ2	F1	38B	(75)	01		TXRS00 SPI010 77 38B 79 39B	TXRS2B BUSS
XA428	TQ2	F2	34A	TXRS8A	00	=		
XA428	TQ2	F2	36A	(72)	01		TXRS00 SPI010 71 36A 73 36B	TXRS2B BUSS
XA428	TQ2	F3	35A	TXRS9A	00	=		
XA428	TQ2	F3	34B	(69)	01		TXRS00 SPI010 65 34B 74 35B	TXRS2B BUSS
XA440	TQ2	A1	05A	TXROCR	00	=		
XA440	TQ2	A1	06A	(06)	01		TXROCS TXR RSA 08 06A 10 07A	
XA441	TQ2	A1	05A	TXROCS	00	=		
XA441	TQ2	A1	06A	(06)	01		TXROCR TXAOCB4 08 06A 10 07A	I/O INPUT REG DATA BIT 0
XA432	TQ2	A2	02B	TXROPA	00	=		
XA432	TQ2	A2	04A	(01)	01		TXROPPR SPI010 04 04A 05 03B	
XA438	PAR	A1	07A	TXROPPR	00	=		
XA438	PAR	A1	03B	(13)	01		TXROCS TXR1CS TXR2CS TXR3CS TXR4CS TXR5CS TXR6CS TXR7CS 03 03B 05 04B 07 05B 09 06B 10 05A 08 04A 06 03A 04 02A	INPUT REG PARITY CHECKER
XA438	PAR	A1	07B	( )	02	+	TXRPCS 11 07B	
XA427	DBC	A1	04A	TXR090T	00	=		
XA427	DBC	A1	02A	(08)	01		TXR7CS 04 02A	DEV COMMAND DECODER 0 TO 9
XA427	DBC	A2	05A	TXR091T	00	=		
XA427	DBC	A2	03A	(10)	01		TXR6CS 06 03A	
XA427	DBC	A3	06A	TXR092T	00	=		
XA427	DBC	A3	02B	(14)	01		TXR5CS 03 02B	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TXR093I

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

LOGIC

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CONNECTOR	CONNECTOR TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DISC	FACTOR	COMMENT
XA427	DBC	A4	07A	TXR093T	00	=		
XA427	DBC	A4	03B	(13)	01		TXR4CS 05 03B	
XA427	DBC	A5	08B	TXR094T	00	=		
XA427	DBC	A5		(17)	01		SPA 4T	
XA427	DBC	A6	04B	TXR095T	00	=		
XA427	DBC	A6		(07)	01		SPA 5T	
XA427	DBC	A7	05B	TXR096T	00	=		
XA427	DBC	A7		(09)	01		SPA 6T	
XA427	DBC	A8	06B	TXR097T	00	=		
XA427	DBC	A8		(11)	01		SPA 7T	
XA427	DBC	A9	07B	TXR098T	00	=		
XA427	DBC	A9		(15)	01		SPA 8T	
XA427	DBC	A0	09A	TXR099T	00	=		
XA427	DBC	A0		(18)	01		SPA 9T	
XA440	TQ2	A2	02B	TXR1CR	00	=		
XA440	TQ2	A2	04A	(01)	01		TXR1CS TXR RSA 04 04A 05 03B	
XA441	TQ2	A2	02B	TXR1CS	00	=		I/O INPUT REG DATA BIT 1
XA441	TQ2	A2	04A	(01)	01		TXR1CR TXA1CB4 04 04A 05 03B	
XA440	TQ2	A3	04B	TXR2CR	00	=		
XA440	TQ2	A3	02A	(09)	01		TXR2CS TXR RSA 03 02A 07 03A	
XA441	TQ2	A3	04B	TXR2CS	00	=		I/O INPUT REG DATA BIT 2
XA441	TQ2	A3	02A	(09)	01		TXR2CR TXA2CB4 03 02A 07 03A	
XA440	TQ2	A4	07B	TXR3CR	00	=		
XA440	TQ2	A4	05B	(15)	01		TXR3CS TXR RSA 11 05B 13 06B	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TXR3CS

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OH	EQUATION	TERM	DESIGNATION	FACTOR	COMMENT
XA441	TQ2	A4	07B	TXR3CS	00	=		
XA441	TQ2	A4	05B	(15)	01		TXR3CR TXA3CB4 11 05B 13 06B	I/O INPUT REG DATA BIT 3
XA440	TQ2	B1	12A	TXR4CR	00	=		
XA440	TQ2	B1	13A	(22)	01		TXR4CS TXR4CS 24 13A 26 14A	
XA441	TQ2	B1	12A	TXR4CS	00	=		
XA441	TQ2	B1	13A	(22)	01		TXR4CR TXA4CB4 24 13A 26 14A	I/O INPUT REG DATA BIT 4
XA440	TQ2	B2	09A	TXR5CR	00	=		
XA440	TQ2	B2	10A	(14)	01		TXR5CS TXR5CS 18 10A 20 11A	
XA441	TQ2	B2	09A	TXR5CS	00	=		
XA441	TQ2	B2	10A	(14)	01		TXR5CR TXA5CB4 18 10A 20 11A	I/O INPUT REG DATA BIT 5
XA440	TQ2	B3	10B	TXR6CR	00	=		
XA440	TQ2	B3	08B	(21)	01		TXR6CS TXR6CS 17 08B 19 09B	
XA441	TQ2	B3	10B	TXR6CS	00	=		
XA441	TQ2	B3	08B	(21)	01		TXR6CR TXA6CB4 17 08B 19 09B	I/O INPUT REG DATA BIT 6
XA440	TQ2	B4	13B	TXR7CR	00	=		
XA440	TQ2	B4	11B	(27)	01		TXR7CS TXR7CS 23 11B 25 12B	
XA441	TQ2	B4	13B	TXR7CS	00	=		
XA441	TQ2	B4	11B	(27)	01		TXR7CR TXA7CB4 23 11B 25 12B	I/O INPUT REG DATA BIT 7
XA428	TQ2	B2	09A	TXSK0A	00	=		
XA428	TQ2	B2	10A	(14)	01		TXXC20 TXXC3P 18 10A 20 11A	
XA432	TQ2	C3	16B	TXSK00	00	=		
XA432	TQ2	C3	14B	(33)	01		TXSK0A SPI010 29 14B 31 15B	STATUS REG CLOCK BITS 0 TO 3
XA426	TQ2	B3	10B	TXSST0	00	=		
XA426	TQ2	B3	08B	(21)	01		TXR097T SPI007 17 08B 19 09B	



DRAWING NUMBER 149015-860

UNIT ASSEMBLY NO. 149015 REV. C INDEX TXSS0A

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

FILE IDENT T39C1FC5 DATE 09-03-82 PAGE

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA437	T5B	E1	31B	TXSS0A	00	=		
XA437	T5B	E1	29B	(59)	01		TXDVSS TXXB20 TXXAOP TXXA1Q TXSSTO TXROPA SPI011 SPI006 55 29B 60 28A 61 32B 62 29A 64 30A 66 31A 68 32A 70 33A	
XA436	TDD	LI	38B	TXST0I	00	=		
				( )	01		TXGNIA 77 38B	
XA436	TDD	LN	39B	TXSTON	00	=		
				( )	01		TXST1A 79 39B	
XA436	TDD	LP	37A	TXSTOP	00	=		
XA436	TDD	LP	37B	(76)	01		SPI011 75 37B	
XA436	TDD	LQ	38A	TXST0Q	00	=		STOP SYNC COUNTER BIT 0
XA436	TDD	LQ	39A	(78)	01		TXST2A 80 39A	
XA439	TQ2	F1	37B	TXST1A	00	=		
XA439	TQ2	F1	38B	(75)	01		TXST1Q TXCP30 77 38B 79 39B	
XA435	TDD	LI	38B	TXST1I	00	=		
				( )	01		TXST0Q 77 38B	
XA435	TDD	LN	39B	TXST1N	00	=		
				( )	01		TXCP10 79 39B	
XA435	TDD	LP	37A	TXST1P	00	=		
XA435	TDD	LP	37B	(76)	01		SPI011 75 37B	
XA435	TDD	LQ	38A	TXST1Q	00	=		STOP SYNC COUNTER BIT 1
XA435	TDD	LQ	39A	(78)	01		SPI006 80 39A	
XA439	TQ2	F2	34A	TXST2A	00	=		
XA439	TQ2	F2	36A	(72)	01		TXST20 SPI011 71 36A 73 36B	
XA432	TQ2	D4	27B	TXST20	00	=		
XA432	TQ2	D4	25B	(51)	01		TXH50A TXSS0A 47 25B 49 26B	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TXSO31U

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

FILE IDENT

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DATE 09-03-82

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESIG. NOTOR	FACTOR	COMMENT
			AND	OR					
XA427	DBC	C1	18A		TXS031U	00	=		
XA427	DBC	C1	18B		(38 )	01		TXDB0TA TXDB0TB TXDB0TC TXDB0TD TXGN4A 39 18B 41 19B 43 22B 45 23B 50 24A	IOU INPUT DATA REGBITS 0-3
XA427	DBC	C2	19A		TXS032U	00	=		
XA427	DBC	C2	17B		(40 )	01		TXSK00 37 17B	
XA427	DBC	C3	20A		TXS033U	00	=		
XA427	DBC	C3	23A		(42 )	01		TXGN5A 47 23A	
XA427	DBC	C4	21A		TXS034U	00	=		
XA427	DBC	C4	22A		(46 )	01		TXGN6A 48 22A	
XA427	DBC	C5	17A		TXS035U	00	=		
XA427	DBC	C5	16B		(36 )	01		SPI007 35 16B	
XA427	DBC	D1	26A		TXS471U	00	=		
XA427	DBC	D1	26B		(54 )	01		TXDB4TA TXDB4TB TXDB4TC TXDB4TD TXGN4A 53 26B 55 27B 57 29B 59 30B 63 31A	IOU INPUT DATA REGBITS 4-7
XA427	DBC	D2	28B		TXS472U	00	=		
XA427	DBC	D2	25B		(56 )	01		TXSK00 51 25B	
XA427	DBC	D3	28A		TXS473U	00	=		
XA427	DBC	D3	31B		(60 )	01		TXGN5A 61 31B	
XA427	DBC	D4	29A		TXS474U	00	=		
XA427	DBC	D4	30A		(62 )	01		TXGN6A 64 30A	
XA427	DBC	D5	25A		TXS475U	00	=		
XA427	DBC	D5	24B		(52 )	01		SPI007 49 24B	
XA432	TQ2	A4	07B		TXXACA	00	=		
XA432	TQ2	A4	05B		(15 )	01		TXXACO SPI010 11 05B 13 06B	
					TXXACI	00	=		
XA436	TDD	MI	36A		( )	01		SPI006 71 36A	

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DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

UNIT ASSEMBLY NO. 149015 REV. C INDEX TXXACN

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

FILE IDENT

T39CIFC5

DATE 09-03-82

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERMINATION	DESIGNATION	FACTOR	COMMENT
XA436	TDD	MN	34A	TXXACN ( )	00 =			
					01 =	TXXA3P 72 34A		
XA436	TDD	MP	35A	TXXACP	00 =			
XA436	TDD	MP	36B	(69 )	01 =	TXXADA 73 36B		
XA436	TDD	MQ	35B	TXXACQ	00 =			I/O STATE COUNTER CONTROL E/E
XA436	TDD	MQ	34B	(74 )	01 =	TXRS08 65 34B		
XA437	TS8	A1	05B	TXXACO	00 =			
XA437	TS8	A1	02B	(11 )	01 =	TXR0CR TXR1CR TXR2CR TXR3CR TXR4CR TXR5CR TXR6CR TXR7CR 01 02B 04 04A 05 03B 06 05A 07 03A 08 06A 10 07A 13 06B		
XA432	TQ2	B1	12A	TXXADA	00 =			I/O STATE COUNTER START
XA432	TQ2	B1	13A	(22 )	01 =	TXXADO SPI010 24 13A 26 14A		
XA433	TD4	C1	17B	TXXADO	00 =			
XA433	TD4	C1	18B	(35 )	01 =	TXRCMR TXRENK TXRPCR TXXACA 37 18B 38 18A 40 19A 42 20A		
				TXXA0I	00 =			
XA434	TDD	AI	06A	( )	01 =	TXXA3P 08 06A		
				TXXA0N	00 =			
XA434	TDD	AN	07A	( )	01 =	T16MHO 10 07A		
XA434	TDD	AP	05B	TXXAOP	00 =			
XA434	TDD	AP	05A	(11 )	01 =	TXXACP 06 05A		
XA434	TDD	AQ	06B	TXXA0Q	00 =			I/O STATE COUNTER BIT 0
XA434	TDD	AQ	07B	(13 )	01 =	SPI010 15 07B		
				TXXA1I	00 =			
XA435	TDD	AI	06A	( )	01 =	TXXA0Q 08 06A		
				TXXA1N	00 =			
XA435	TDD	AN	07A	( )	01 =	T16MHO 10 07A		

DRAWING NUMBER 149015-860

UNIT ASSEMBLY NO. 149015 REV. C INDEX TXXA1P

LOGIC

UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA435	TDD	AP	05B	TXXA1P	00	=		
XA435	TDD	AP	05A	(11)	01		TXXACP 06 05A	
XA435	TDD	AQ	06B	TXXA1Q	00	=		I/O STATE COUNTER BIT 1
XA435	TDD	AQ	07B	(13)	01		SPI011 15 07B	
XA434	TDD	BI	03B	TXXA2I	00	=		
XA434	TDD	BI	03B	( )	01		TXXA1Q 05 03B	
XA434	TDD	BN	02B	TXXA2N	00	=		
XA434	TDD	BN	02B	( )	01		T16MHO 01 02B	
XA434	TDD	BP	04B	TXXA2P	00	=		
XA434	TDD	BP	04A	(09)	01		TXXACP 04 04A	
XA434	TDD	BQ	03A	TXXA2Q	00	=		I/O STATE COUNTER BIT 2
XA434	TDD	BQ	02A	(07)	01		SPI006 03 02A	
XA435	TDD	BI	03B	TXXA3I	00	=		
XA435	TDD	BI	03B	( )	01		TXXA2Q 05 03B	
XA435	TDD	BN	02B	TXXA3N	00	=		
XA435	TDD	BN	02B	( )	01		T16MHO 01 02B	
XA435	TDD	BP	04B	TXXA3P	00	=		
XA435	TDD	BP	04A	(09)	01		TXXACP 04 04A	
XA435	TDD	BQ	03A	TXXA3Q	00	=		I/O STATE COUNTER BIT 3
XA435	TDD	BQ	02A	(07)	01		SPI006 03 02A	
XA439	TQ2	A1	05A	TXXA4A	00	=		I/O STATE COUNTER STATE 4
XA439	TQ2	A1	06A	(06)	01		TXXA0Q TXXA3Q 08 06A 10 07A	
XA440	TQ2	D1	24A	TXXA40	00	=		
XA440	TQ2	D1	25A	(52)	01		TXXA4A SPI011 54 25A 56 26A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TRYS	DELTA FACTOR	FACTOR	COMMENT
XA439	TQ2	A2	02B	TXXA5A	00	=		I/O STATE COUNTER STATE 5
XA439	TQ2	A2	04A	(01)	01		TXXA0P TXXA1Q 04 04A 05 03B	
XA440	TQ2	D2	21A	TXXA50	00	=		
XA440	TQ2	D2	22A	(46)	01		TXXA5A SPI011 48 22A 50 23A	
XA439	TQ2	A3	04B	TXXA6A	00	=		I/O STATE COUNTER STATE 6
XA439	TQ2	A3	02A	(09)	01		TXXA1P TXXA2Q 03 02A 07 03A	
XA432	TQ2	A3	04B	TXXBCA	00	=		I/O BYTE COUNTER RESET
XA432	TQ2	A3	02A	(09)	01		TXXBC0 SPI010 03 02A 07 03A	
XA431	TT3	B1	11A	TXXBC0	00	=		
XA431	TT3	B1	12A	(20)	01		TXRCMR TXRENr TXRS0B 22 12A 24 13A 26 14A	
XA406	TQ2	E4	33B	TXXBK0	00	=		I/O BYTE COUNTER CLOCK
XA406	TQ2	E4	31B	(63)	01		TXXA3Q SPI001 59 31B 61 32B	
XA439	TQ2	A4	07B	TXXB0A	00	=		I/O BYTE COUNTER STATE 0
XA439	TQ2	A4	05B	(15)	01		TXXB0P TXXB2P 11 05B 13 06B	
XA436	TDD	CI	13A	TXXB0I	00	=		
XA436	TDD	CI		( )	01		TXXB2P 24 13A	
XA436	TDD	CN	14A	TXXB0N	00	=		
XA436	TDD	CN		( )	01		TXXBK0 26 14A	
XA436	TDD	CP	11B	TXXB0P	00	=		
XA436	TDD	CP	12A	(23)	01		TXXBCA 22 12A	
XA436	TDD	CQ	12B	TXXB0Q	00	=		I/O BYTE COUNTER BIT 0
XA436	TDD	CQ	13B	(25)	01		SPI011 27 13B	
XA440	TQ2	D3	24B	TXXB00	00	=		
XA440	TQ2	D3	22B	(45)	01		TXXB0A SPI011 41 22B 43 23B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
			AND	OR					
XA439	TQ2	B1	12A		TXXB1A	00	=		I/O BYTE COUNTER STATE 1
XA439	TQ2	B1	13A		( 22 )	01		TXXB0Q TXXB1P 24 13A 26 14A	
					TXXB1I	00	=		
XA435	TDD	FI	16A		( )	01		TXXB0Q 34 16A	
					TXXB1N	00	=		
XA435	TDD	FN	15A		( )	01		TXXBKO 30 15A	
XA435	TDD	FP	16B		TXXB1P	00	=		
XA435	TDD	FP	17A		( 33 )	01		TXXBCA 36 17A	
XA435	TDD	FQ	15B		TXXB1Q	00	=		I/O BYTE COUNTER BIT 1
XA435	TDD	FQ	14B		( 31 )	01		SPI011 29 14B	
XA440	TQ2	D4	27B		TXXB1O	00	=		
XA440	TQ2	O4	25B		( 51 )	01		TXXB1A SPI011 47 25B 49 26B	
XA439	TQ2	B2	09A		TXXB2A	00	=		I/O BYTE COUNTER STATE 2
XA439	TQ2	B2	10A		( 14 )	01		TXXB1Q TXXB2P 18 10A 20 11A	
					TXXB2I	00	=		
XA434	TDD	GI	25A		( )	01		TXXB1Q 54 25A	
					TXXB2N	00	=		
XA434	TDD	GN	26A		( )	01		TXXBKO 56 26A	
XA434	TDD	GP	25B		TXXB2P	00	=		
XA434	TDD	GP	24A		( 47 )	01		TXXBCA 52 24A	
XA434	TDD	GQ	26B		TXXB2Q	00	=		I/O BYTE COUNTER BIT 2
XA434	TDD	GQ	27B		( 49 )	01		SPI011 51 27B	
XA440	TQ2	E1	31A		TXXB2O	00	=		
XA440	TQ2	E1	32A		( 66 )	01		TXXB2A SPI011 68 32A 70 33A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA439	TQ2	B3	10B	TXXB3A	00	=		I/O BYTE COUNTER STATE 3
XA439	TQ2	B3	08B	(21)	01		TXXB0Q TXXB2Q 17 08B 19 09B	
XA440	TQ2	E2	28A	TXXB30	00	=		
XA440	TQ2	E2	29A	(60)	01		TXXB3A SPI017 62 29A 64 30A	
XA439	TQ2	B4	13B	TXXB4A	00	=		I/O BYTE COUNTER STATE 4
XA439	TQ2	B4	11B	(27)	01		TXXB0P TXXB1Q 23 11B 25 12B	I/O BYTE COUNTER STATE 4
XA440	TQ2	E3	30B	TXXB40	00	=		
XA440	TQ2	E3	28B	(57)	01		TXXB4A SPI017 53 28B 55 29B	
XA439	TQ2	C1	19A	TXXB5A	00	=		I/O BYTE COUNTER STATE 5
XA439	TQ2	C1	19A	(38)	01		TXXB1P TXXB2Q 40 19A 42 20A	
XA440	TQ2	E4	33B	TXXB50	00	=		
XA440	TQ2	E4	31B	(63)	01		TXXB5A SPI017 59 31B 61 32B	
XA428	TQ2	D4	27B	TXXC1A	00	=		
XA428	TQ2	D4	25B	(51)	01		TXXC1O SPI010 47 25B 49 26B	
XA435	TDD	MI	36A	TXXC1I	00	=		
				( )	01		TXGN4A 71 36A	
XA435	TDD	MN	34A	TXXC1N	00	=		
				( )	01		TXXC4P 72 34A	
XA435	TDD	MP	35A	TXXC1P	00	=		
XA435	TDD	MP	36B	(69)	01		TXRS0B 73 36B	
XA435	TDD	MQ	35B	TXXC1Q	00	=		DEV/OPER INDICATOR CONTROL E/E
XA435	TDD	MQ	34B	(74)	01		TXXC1A 65 34B	
XA429	TD4	F1	37A	TXXC1O	00	=		INDICATOR INPUTS
XA429	TD4	F1	37B	(76)	01		TSYN1A TTASLA TXSS0A TX0D3A 75 37B 77 38B 78 38A 79 39B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA429	T04	F2	35A	TXXCRO	00	=		
XA429	T04	F2	36A	( 169 )	01		TXXCIP TXXDDP TXXDIP TXXDSP 71 36A 72 34A 73 36B 74 35B	
XA428	TQ2	B3	10B	TXXCSA	00	=		INPUT STROBE COUNT 5 TO 7
XA428	TQ2	B3	08B	( 21 )	01		TXXC2Q TXXC4Q 17 08B 19 09B	
XA432	TQ2	D2	21A	TXXCS0	00	=		
XA432	TQ2	D2	22A	( 146 )	01		TXXCSA SPI010 48 22A 50 23A	
				TXXC0I	00	=		
XA434	T0D	FI	16A	( )	01		TXXC4P 34 16A	
				TXXCON	00	=		
XA434	T0D	FN	15A	( )	01		T16M10 30 15A	
XA434	T0D	FP	16B	TXXCOP	00	=		
XA434	T0D	FP	17A	( 133 )	01		TXXCRO 36 17A	
XA434	T0D	FQ	15B	TXXCOQ	00	=		
XA434	T0D	FQ	14B	( 31 )	01		SPI010 29 14B	
				TXXC1I	00	=		
XA435	T0D	CI	13A	( )	01		TXXC0Q 24 13A	
				TXXC1N	00	=		
XA435	T0D	CN	14A	( )	01		T16M10 26 14A	
XA435	T0D	CP	11B	TXXC1P	00	=		
XA435	T0D	CP	12A	( 23 )	01		TXXCRO 22 12A	
XA435	T0D	CQ	12B	TXXC1Q	00	=		INPUT STROBE COUNTER BIT 1
XA435	T0D	CQ	13B	( 25 )	01		SPI011 27 13B	
				TXXC2I	00	=		
XA434	T0D	OI	10A	( )	01		TXXC1Q 18 10A	



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
				TXXC2N	00	=		
XA434	TDD	DN	09A	( )	01		T16M10 14 09A	
XA434	TDD	DP	10B	TXXC2P	00	=		
XA434	TDD	DP	11A	(21)	01		TXXCRO 20 11A	
XA434	TDD	DQ	09B	TXXC2Q	00	=		INPUT STROBE COUNTER BIT 2
XA434	TDD	DQ	08B	(19)	01		SPI006 17 08B	
				TXXC3I	00	=		
XA435	TDD	DI	10A	( )	01		TXXC2Q 18 10A	
				TXXC3N	00	=		
XA435	TDD	DN	09A	( )	01		T16M10 14 09A	
XA435	TDD	DP	10B	TXXC3P	00	=		
XA435	TDD	DP	11A	(21)	01		TXXCRO 20 11A	
XA435	TDD	DQ	09B	TXXC3Q	00	=		INPUT STROBE COUNTER BIT 3
XA435	TDD	DQ	08B	(19)	01		SPI006 17 08B	
				TXXC4I	00	=		
XA539	TDD	DI	10A	( )	01		TXXC3Q 18 10A	
				TXXC4N	00	=		
XA539	TDD	DN	09A	( )	01		T16M10 14 09A	
XA539	TDD	DP	10B	TXXC4P	00	=		
XA539	TDD	DP	11A	(21)	01		TXXCRO 20 11A	
XA539	TDD	DQ	09B	TXXC4Q	00	=		INPUT STROBE COUNTER BIT 4
XA539	TDD	DQ	08B	(19)	01		SPI026 17 08B	
XA432	TQ2	C2	15A	TXXDDA	00	=		
XA432	TQ2	C2	16A	(30)	01		TXXDDO SPI010 34 16A 36 17A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM. DESIGNATOR	FACTOR	COMMENT
XA435	TDD	EI	19A	TXXDDI ( )	00 = 01	TXGN4A 40 19A	
XA435	TDD	EN	20A	TXXDDN ( )	00 = 01	TXXD1P 42 20A	
XA435	TDD	EP	17B	TXXDDP (35 )	00 = 01	TXRSOB 38 18A	
XA435	TDD	EQ	18B	TXXDDQ (37 )	00 = 01	TXXDDA 39 19B	INPUT DATA CONTROL E/F
XA428	TQ2	A4	07B	TXXD00 (15 )	00 = 01	TRTDEA TLTEOA 11 05B 13 06B	
XA428	TQ2	B1	12A	TXXDIA (22 )	00 = 01	TXXDIS TXEA00 24 13A 26 14A	
XA434	TDD	CI	13A	TXXDII ( )	00 = 01	TXGN4A 24 13A	
XA434	TDD	CN	14A	TXXDIN ( )	00 = 01	TXXD1P 26 14A	
XA434	TDD	CP	11B	TXXDIP (23 )	00 = 01	TXRSOB 22 12A	
XA434	TDD	CQ	12B	TXXDIQ (25 )	00 = 01	TXXDIA 27 13B	INPUT STROBE COUNTER BIT 0
XA431	TT3	D3	27B	TXXDIR (51 )	00 = 01	TXXDIS TXDIRA TXINHR 45 24B 47 25B 49 26B	
XA430	TT3	D3	24B	TXXDIS (51 )	00 = 01	TXXDIR TINT2A TINT6A 45 24B 47 25B 49 26B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	ELIGIBLE FACTOR	FACTOR	COMMENT
XA432	TQ2	A1	05A	TXXDRA	00	=		
XA432	TQ2	A1	06A	(06)	01		TXXDRO SPI010 08 06A 10 07A	
XA431	TT3	A3	07B	TXXDRO	00	=		
XA431	TT3	A3	04B	(15)	01		TXXDDP TXXDIP TXXDSP 09 04B 11 05B 13 06B	
XA434	TDD	MI	36A	TXXDSI	00	=		
				( )	01		TXGN4A 71 36A	
XA434	TDD	MN	34A	TXXDSN	00	=		
				( )	01		TXXDIP 72 34A	
XA434	TDD	MP	35A	TXXDSP	00	=		
XA434	TDD	MP	36B	(69)	01		TXRS0B 73 36B	
XA434	TDD	MQ	35B	TXXDSQ	00	=		INPUT STATUS CONTROL F/F
XA434	TDD	MQ	34B	(74)	01		TXIR1A 65 34B	
XA434	TDD	EI	19A	TXXDOI	00	=		
				( )	01		TXXDIP 40 19A	
XA434	TDD	EN	20A	TXXDON	00	=		
				( )	01		TXXC3P 42 20A	
XA434	TDD	EP	17B	TXXDOP	00	=		
XA434	TDD	EP	18A	(35)	01		TXXCRO 38 18A	
XA434	TDD	EQ	18B	TXXDOQ	00	=		INPUT BYTE COUNTER BIT 0
XA434	TDD	EQ	19B	(37)	01		SPI011 39 19B	
XA436	TDD	BI	03B	TXXDII	00	=		
				( )	01		TXXD0Q 05 03B	
XA436	TDD	BN	02B	TXXDIN	00	=		
				( )	01		TXXC3P 01 02B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
			AND	OR					
XA436	TDD	BP	04B		TXXD1P	00	=		
XA436	TDD	BP	04A		(09)	01		TXXCRO 04 04A	
XA436	TDD	BQ	03A		TXXD1Q	00	=		
XA436	TDD	BQ	02A		(07)	01		TXXDIA 03 02A	INPUT BYTE COUNTER BIT 1
XA428	TQ2	A3	04B		TXXRCA	00	=		
XA428	TQ2	A3	02A		(09)	01		TXXRCO TXXREP 03 02A 07 03A	
					TXXRCI	00	=		
XA539	TDD	AI	06A		( )	01		TXGN4A 08 06A	
					TXXRCN	00	=		
XA539	TDD	AN	07A		( )	01		TXXR2P 10 07A	
XA539	TDD	AP	05B		TXXRCP	00	=		
XA539	TDD	AP	05A		(11)	01		TXRS0B 06 05A	
XA539	TDD	AQ	06B		TXXRCQ	00	=		
XA539	TDD	AQ	07B		(13)	01		TXXRCA 15 07B	REQUEST CONTROL F/F
XA418	TS8	E1	31B		TXXRCO	00	=		
XA418	TS8	E1	29B		(59)	01		TREQ4A TWRQ1A TLPT5A TNSGCA TNSGEA TFST6A TINT2A TINT6A 55 29B 60 28A 61 32B 62 29A 64 30A 66 31A 68 32A 70 33A	REQUEST INPUTS
					TXXREI	00	=		
XA436	TDD	JI	32A		( )	01		TXGN4A 68 32A	
					TXXREN	00	=		
XA436	TDD	JN	33A		( )	01		TXEA0A 70 33A	
XA436	TDD	JP	31B		TXXREP	00	=		
XA436	TDD	JP	31A		(59)	01		TXRS0B 66 31A	
XA436	TDD	JQ	32B		TXXREQ	00	=		
XA436	TDD	JQ	33B		(61)	01		TXXROP 63 33B	REQUEST ENABLE F/F

CONNECTOR	CPU/OUT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TRM	DESIG- NATOR	FACTOR	COMMENT
XA435	TDD	KI	29A	TXXR01 ( )	00	=	TXXR2P 62 29A	
XA435	TDD	KN	28A	TXXRON ( )	00	=	T16MH0 60 28A	
XA435	TDD	KP	30B	TXXROP (57 )	00	=		
XA435	TDD	KP	30A		01		TXXRCQ 64 30A	
XA435	TDD	KQ	29B	TXXROQ (55 )	00	=		REQUEST STROBE COUNTER BIT 0
XA435	TDD	KQ	28B		01		SP1011 53 28B	
XA436	TDD	KI	29A	TXXR1I ( )	00	=	TXXR0Q 62 29A	
XA436	TDD	KN	28A	TXXR1N ( )	00	=	T16MH0 60 28A	
XA436	TDD	KP	30B	TXXR1P (57 )	00	=		
XA436	TDD	KP	30A		01		TXXRCQ 64 30A	
XA436	TDD	KQ	29B	TXXR1Q (55 )	00	=		REQUEST STROBE COUNTER BIT 1
XA436	TDD	KQ	28B		01		SP1011 53 28B	
XA434	TDD	LI	38B	TXXR2I ( )	00	=	TXXR1Q 77 38B	
XA434	TDD	LN	39B	TXXR2N ( )	00	=	T16MH0 79 39B	
XA434	TDD	LP	37A	TXXR2P (76 )	00	=		
XA434	TDD	LP	37B		01		TXXRCQ 75 37B	
XA434	TDD	LQ	38A	TXXR2Q (78 )	00	=		REQUEST STROBE COUNTER BIT 2
XA434	TDD	LQ	39A		01		SP1011 80 39A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA439	TQ2	C2	15A	TXX04A	00	=		
XA439	TQ2	C2	16A	( 30 )	01		TXXB00 TXXA40 34 16A 36 17A	
XA433	TD4	C2	16B	TXX05A	00	=		
XA433	TD4	C2	15A	( 33 )	01		TXXB0P TXXB2P TXXA0P TXXA1Q 30 15A 31 15B 34 16A 36 17A	
XA439	TQ2	C3	16B	TXX050	00	=		I/O-BYTE COUNT=0, STATE=5
XA439	TQ2	C3	14B	( 33 )	01		TXX05A SPI011 29 14B 31 15B	
XA431	TT3	B3	13B	TX0DEA	00	=		DEF RECEIVE DATA PARITY ERROR
XA431	TT3	B3	10B	( 27 )	01		TXOFRS TXROPPR TXXA50 21 10B 23 11B 25 12B	
XA432	TQ2	B2	09A	TX0DRA	00	=		DEF RESET STROBE
XA432	TQ2	B2	10A	( 14 )	01		TXOFRS TXXB10 18 10A 20 11A	
XA430	TT3	C1	17A	TX0D0A	00	=		DEF RECEIVE DATA STROBE BYTE 0
XA430	TT3	C1	18A	( 36 )	01		TXOFRS TXXB20 TXXA50 38 18A 40 19A 42 20A	
XA430	TT3	C2	15B	TX0D1A	00	=		DEF RECEIVE DATA STROBE BYTE 1
XA430	TT3	C2	14B	( 31 )	01		TXOFRS TXXB30 TXXA50 29 14B 30 15A 34 16A	
XA430	TT3	C3	19B	TX0D2A	00	=		DEF RECEIVE DATA STROBE BYTE 2
XA430	TT3	C3	16B	( 39 )	01		TXOFRS TXXB40 TXXA50 33 16B 35 17B 37 18B	
XA430	TT3	D1	23A	TX0D3A	00	=		DEF RECEIVE DATA STROBE BYTE 3
XA430	TT3	D1	24A	( 50 )	01		TXOFRS TXXB50 TXXA50 52 24A 54 25A 56 26A	
XA430	TT3	B2	09B	TX0FRR	00	=		
XA430	TT3	B2	09A	( 19 )	01		TXOFRS TXXB0A TXRS0B 14 09A 17 08B 18 10A	
XA426	TQ2	F1	37B	TX0FRS	00	=		DEF COMMAND STORE F/F
XA426	TQ2	F1	38B	( 75 )	01		TXOFRR TXOR0A 77 38B 79 39B	
XA426	TQ2	B4	13B	TX0FRO	00	=		
XA426	TQ2	B4	11B	( 27 )	01		TXR098T SPI007 23 11B 25 12B	

CONNECTOR	CPCUT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERMIN	DESIGNATOR	FACTOR	COMMENT
XA439	TQ2	C4	19B	TXONLO	00	=		
XA439	TQ2	C4	17B	(39)	01		TXASLA TXBSLA 35 17B 37 18B	
XA429	TD4	A2	04B	TXOR0A	00	=		
XA429	TD4	A2	02B	(09)	01		TXCA10 TXDEVA TXOFRO TBUSYA 01 02B 04 04A 05 03B 07 03A	
				TX1MAI	00	=		
XA416	TDD	KI	29A	( )	01		TX1MBP 62 29A	
				TX1MAN	00	=		
XA416	TDD	KN	28A	( )	01		T04MHJ 60 28A	
XA416	TDD	KP	30B	TX1MAP	00	=		
XA416	TDD	KP	30A	(57)	01		SPI006 64 30A	
XA416	TDD	KQ	29B	TX1MAQ	00	=		SYNC CLOCK BIT 0
XA416	TDD	KQ	28B	(55)	01		SPI003 53 28B	
				TX1MBI	00	=		
XA417	TDD	KI	29A	( )	01		TX1MAQ 62 29A	
				TX1MBN	00	=		
XA417	TDD	KN	28A	( )	01		T04MHJ 60 28A	
XA417	TDD	KP	30B	TX1MBP	00	=		
XA417	TDD	KP	30A	(57)	01		SPI006 64 30A	
XA417	TDD	KQ	29B	TX1MBQ	00	=		SYNC CLOCK BIT 1
XA417	TDD	KQ	28B	(55)	01		SPI003 53 28B	
XA519	TQ2	A3	04B	T002MA	00	=		MAIN COUNT 100MS
XA519	TQ2	A3	02A	(09)	01		TC318P TC328Q 03 02A 07 03A	
XA520	TQ2	B1	12A	T002M0	00	=		
XA520	TQ2	B1	13A	(22)	01		T002MA SPI013 24 13A 26 14A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERN	DEFINITION	FACTOR	COMMENT
XA519	TQ2	A1	05A	T003MA	00	=		MAIN COUNT 3MS
XA519	TQ2	A1	06A	(06)	01		TC32BP TC33BQ 08 06A 10 07A	
XA520	TQ2	A1	05A	T003M0	00	=		
XA520	TQ2	A1	06A	(06)	01		T003MA SPI021 08 06A 10 07A	
XA412	TQ2	A4	07B	T009SA	00	=		MAIN COUNT 9S
XA412	TQ2	A4	05B	(15)	01		TC63BQ TC64BP 11 05B 13 06B	
XA519	TQ2	A2	02B	T030MA	00	=		MAIN COUNT 50MS
XA519	TQ2	A2	04A	(01)	01		TC42BP TC43BQ 04 04A 05 03B	
XA520	TQ2	A2	02B	T030M0	00	=		
XA520	TQ2	A2	04A	(01)	01		T030MA SPI021 04 04A 05 03B	
XA433	TD4	F2	35A	T033MA	00	=		MAIN COUNT 62MS
XA433	TD4	F2	36A	(69)	01		TC425P TC32BP TC33BQ SPI010 71 36A 72 34A 73 36B 74 35B	
XA520	TQ2	A3	04B	T033M0	00	=		
XA520	TQ2	A3	02A	(09)	01		T033MA SPI021 03 02A 07 03A	
XA525	TD4	D2	24B	T066MA	00	=		WRITE COUNT 37US
XA525	TD4	D2	23B	(45)	01		TC40BQ TC41BP TC30BQ TC31BP 43 23B 46 21A 48 22A 50 23A	
XA519	TQ2	B2	09A	T080UA	00	=		MAIN COUNT 30US
XA519	TQ2	B2	10A	(14)	01		TC14BP TC13BQ 18 10A 20 11A	
XA520	TQ2	B2	09A	T080U0	00	=		
XA520	TQ2	B2	10A	(14)	01		T080UA SPI021 18 10A 20 11A	
XA525	TD4	F2	35A	T082MA	00	=		MAIN COUNT 15MS
XA525	TD4	F2	36A	(69)	01		TC42BQ TC31BP TC43BP SPI014 71 36A 72 34A 73 36B 74 35B	
XA522	TQ2	F2	34A	T082M0	00	=		
XA522	TQ2	F2	36A	(72)	01		T082MA SPI022 71 36A 73 36B	



CONNECTOR	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA527	TS8	F1 37A	T198UA	00	=		WRITE COUNT 158US
XA527	TS8	F1 36A	(76)	01		TC90BP TC91BQ TC83BQ TC84BP TC72BQ TC73BP SPI025 SPI024 71 36A 72 34A 73 36B 74 35B 75 37B 77 38B 78 38A 79 39B	
XA522	TQ2	F1 37B	T198U0	00	=		
XA522	TQ2	F1 38B	(75)	01		T198UA SPI022 77 38B 79 39B	
XA407	TQ2	D3 24B	T200MA	00	=		MAIN COUNT 500MS
XA407	TQ2	D3 22B	(45)	01		TC51BP TC52BQ 41 22B 43 23B	
XA520	TQ2	A4 07B	T200M0	00	=		
XA520	TQ2	A4 05B	(15)	01		T200MA SPI021 11 05B 13 06B	
XA519	TQ2	A4 07B	T300MA	00	=		MAIN COUNT 300MS
XA519	TQ2	A4 05B	(15)	01		TC52BP TC53BQ 11 05B 13 06B	
XA527	TS8	E1 31B	T399UA	00	=		WRITE COUNT 319US
XA527	TS8	E1 29B	(59)	01		TC90BQ TC91BP TC83BQ TC84BP TC73BQ TC74BP SPI024 SPI022 55 29B 60 28A 61 32B 62 29A 64 30A 66 31A 68 32A 70 33A	
XA523	TQ2	F3 35A	T399UC	00	=		
XA523	TQ2	F3 34B	(69)	01		T399UA SPI022 65 34B 74 35B	
XA505	TQ2	A1 05A	UBSY1A	00	=		
XA505	TQ2	A1 06A	(06)	01		TDIRSQ TEOTOS 08 06A 10 07A	
XA545	DCF	A5 08B	UB0T3DX4	00	=		ROT 3 RECEIVER
XA545	DCF	A5 05A	(14)	01		SPI029 06 05A	
XA545	DCF	A6 07A	UB0T30X	00	=		
XA545	DCF	A6 06A	(10)	01		SPI013 08 06A	
XA545	DCF	A7 02A	UB0T4DX4	00	=		ROT 4 RECEIVER
XA545	DCF	A7 05A	(01)	01		SPI029 06 05A	
XA545	DCF	A8 03A	UB0T40X	00	=		
XA545	DCF	A8 04A	(03)	01		SPI015 04 04A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERM	DEFC FACTOR	FACTOR	COMMENT
XA430	TT3	D2	23B	UEBOCA	00	=		
XA430	TT3	D2	22B	(43)	01		TTAS10 TXR0CR TXDV1B 41 22B 46 21A 48 22A	
XA430	TT3	E2	29B	UEB1CA	00	=		
XA430	TT3	E2	28B	(55)	01		TTAS10 TXR1CR TXDV1B 53 28B 60 28A 62 29A	
XA430	TT3	E3	33B	UEB2CA	00	=		
XA430	TT3	E3	30B	(63)	01		TTAS10 TXR2CR TXDV1B 57 30B 59 31B 61 32B	
XA430	TT3	F3	39A	UEB3CA	00	=		
XA430	TT3	F3	35A	(80)	01		TTAS10 TXR3CR TXDV1B 69 35A 76 37A 78 38A	
XA524	TT3	B2	09B	UEOT1R	00	=		
XA524	TT3	B2	09A	(19)	01		UEOT1S UEOT3A TEOTOS 14 09A 17 08B 18 10A	
XA523	TQ2	B2	09A	UEOT1S	00	=		EOT COUNTER BIT 1
XA523	TQ2	B2	10A	(14)	01		UEOT1R UEOT2A 18 10A 20 11A	
XA524	TT3	B3	13B	UEOT2A	00	=		
XA524	TT3	B3	10B	(27)	01		TEOTOS UEOT2R TSCL1B 21 10B 23 11B 25 12B	
XA522	TQ2	B4	13B	UEOT2R	00	=		
XA522	TQ2	B4	11B	(27)	01		UEOT2S TEOTOS 23 11B 25 12B	
XA523	TQ2	B4	13B	UEOT2S	00	=		EOT COUNTER BIT 2
XA523	TQ2	B4	11B	(27)	01		UEOT2R UEOT4A 23 11B 25 12B	
XA522	TQ2	B1	12A	UEOT3A	00	=		
XA522	TQ2	B1	13A	(22)	01		UEOT2S TSCL1B 24 13A 26 14A	
XA545	DCF	B5	16A	UEOT3DX4	00	=		EOT 3 RECEIVER
XA545	DCF	B5	13A	(41)	01		SPI029 36 13A	
XA545	DCF	B6	15A	UEOT30X	00	=		
XA545	DCF	B6	14A	(40)	01		SPI013 38 14A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERM	DESIGNATOR	FACTOR	COMMENT
XA522	TQ2	B2	09A	UEOT4A	00	=		
XA522	TQ2	B2	10A	(14)	01		UEOT1S TSCL3B 18 10A 20 11A	
XA545	DCF	B7	10A	UEOT4DX4	00	=		EQT 4 RECEIVER
XA545	DCF	B7	13A	(23)	01		SPI029 36 13A	
XA545	DCF	B8	11A	UEOT4OX	00	=		
XA545	DCF	B8	12A	(30)	01		SPI015 34 12A	
XA424	MUX	C1	17A	UFPRX1X	00	=		FILE PROTECT MULTIPLEXER
XA424	MUX	C1	16B	(36)	01		TFPR1OX TADSAOX UFPR3OX SPI006 UFPR4OX SPI007 SPI008 SPI003 35 16B 37 17B 39 18B 41 19B 43 22B 45 23B 47 23A 50 24A	
XA424	MUX	C2	18A	UFPRX2X	00	=		
XA424	MUX	C2	21A	(38)	01		TTS2BS TTS3BS TTS4BS TXGN6A 46 21A 42 20A 40 19A 48 22A	
XA545	DCF	C5	31B	UFPR3DX4	00	=		FILE PROTECT 3 RECEIVER
XA545	DCF	C5	29A	(60)	01		SPI029 52 29A	
XA545	DCF	C6	31A	UFPR3OX	00	=		
XA545	DCF	C6	30A	(57)	01		SPI013 54 30A	
XA545	DCF	C7	25A	UFPR4DX4	00	=		FILE PROTECT 4 RECEIVER
XA545	DCF	C7	29A	(43)	01		SPI029 52 29A	
XA545	DCF	C8	26A	UFPR4OX	00	=		
XA545	DCF	C8	28A	(48)	01		SPI015 50 28A	
XA509	TT3	E1	30A	UIN7A	00	=		
XA509	TT3	E1	31A	(64)	01		TXGN2A TDIRIS TSCL3B 66 31A 68 32A 70 33A	
XA446	DCF	D1	32B	UMALFDX	00	=		MALEFUNCTION RECEIVER
XA446	DCF	D1	36A	(65)	01		SPI018 72 36A	
XA446	DCF	D2	33B	UMALFOX	00	=		
XA446	DCF	D2	34B	(69)	01		SPI012 71 34B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TRN	RESIGNATOR	FACTOR	COMMENT
XA546	DCF	C5	31B	URDBSDX4	00	=		READ STATUS RECEIVER
XA546	DCF	C5	29A	(60)	01		SPI029 52 29A	
XA546	DCF	C6	31A	URDBSOX	00	=		
XA546	DCF	C6	30A	(57)	01		SPI013 54 30A	
XA423	MUX	C1	17A	URDYX1X	00	=		READY MULTIPLEXER
XA423	MUX	C1	16B	(36)	01		TRDY10X URDY20X URDY30X SPI003 URDY40X SPI008 SPI006 SPI007 35 16B 37 17B 39 18B 41 19B 43 22B 45 23B 47 23A 50 24A	
XA423	MUX	C2	18A	URDYX2X	00	=		
XA423	MUX	C2	21A	(38)	01		TTS2BS TTS3BS TTS4BS TXGN6A 46 21A 42 20A 40 19A 48 22A	
XA546	DCF	D3	37B	URDY2DX4	00	=		READY 2 RECEIVER
XA546	DCF	D3	36A	(78)	01		SPI029 72 36A	
XA546	DCF	D4	36B	URDY20X	00	=		
XA546	DCF	D4	35B	(75)	01		SPI030 73 35B	
XA546	DCF	D5	38B	URDY3DX4	00	=		READY 3 RECEIVER
XA546	DCF	D5	36A	(80)	01		SPI029 72 36A	
XA546	DCF	D6	38A	URDY30X	00	=		
XA546	DCF	D6	37A	(76)	01		SPI013 74 37A	
XA546	DCF	D7	33A	URDY4DX4	00	=		READY 4 RECEIVER
XA546	DCF	D7	36A	(61)	01		SPI029 72 36A	
XA546	DCF	D8	34A	URDY40X	00	=		
XA546	DCF	D8	35A	(68)	01		SPI015 70 35A	
XA545	DCF	D5	38B	UREW3DX4	00	=		REWINDING 3 RECEIVER
XA545	DCF	D5	36A	(80)	01		SPI029 72 36A	
XA545	DCF	D6	38A	UREW30X	00	=		
XA545	DCF	D6	37A	(76)	01		SPI013 74 37A	

CONNECTOR	UNIT TYPE	GROUP	TEST POINTS	EQUATION	TERM	DESIGN FACTOR	FACTOR	COMMENT
XA545	DCF	D7	33A	UREW4DX4	00 =			
XA545	DCF	D7	36A	(61)	01		SP1029 72 36A	REWINDING & RECEIVER
XA545	DCF	D8	34A	UREW40X	00 =			
XA545	DCF	D8	35A	(68)	01		SP1015 70 35A	
XA517	TQ2	A3	04B	URRS00	00 =			
XA517	TQ2	A3	02A	(09)	01		UWRT8A TXRS2B 03 02A 07 03A	
XA517	TQ2	A4	07B	URRS10	00 =			
XA517	TQ2	A4	05B	(15)	01		UWRT9A TXRS2B 11 05B 13 06B	
XA506	TQ2	A3	04B	URUN1A	00 =			
XA506	TQ2	A3	02A	(09)	01		TDIRIS TE0T10X 03 02A 07 03A	
XA522	TQ2	A1	05A	URW1CA	00 =			
XA522	TQ2	A1	05A	(06)	01		TT51B5 TREW10X 08 06A 10 07A	
XA522	TQ2	A2	02B	URW2CA	00 =			
XA522	TQ2	A2	04A	(01)	01		TT52B5 TADSBOX 04 04A 05 03B	
XA522	TQ2	A3	04B	URW3CA	00 =			
XA522	TQ2	A3	02A	(09)	01		TT53B5 UREW30X 03 02A 07 03A	
XA522	TQ2	A4	07B	URW4CA	00 =			
XA522	TQ2	A4	05B	(15)	01		TT54B5 UREW40X 11 05B 13 06B	
XA511	TQ2	F4	39A	USTP50	00 =			
XA511	TQ2	F4	37A	(80)	01		TSNC05 TBUSYS 76 37A 78 38A	
XA546	DCF	C7	25A	UWDBSDX4	00 =			
XA546	DCF	C7	29A	(43)	01		SP1029 52 29A	WRITE STATUS RECEIVER
XA546	DCF	C8	26A	UWDBSOX	00 =			
XA546	DCF	C8	28A	(48)	01		SP1015 50 28A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS		EQUATION	TERMS	DESIGNATOR	FACTOR	COMMENT
			AND	OR					
XA512	TQ2	F3	35A		UWDDGO	00	=		
XA512	TQ2	F3	34B		(69)	01		UW4BCA UW5BCA 65 34B 74 35B	
XA512	TQ2	F4	39A		UWDDHO	00	=		
XA512	TQ2	F4	37A		(80)	01		UW4BCA UW5BCA 76 37A 78 38A	
XA443	TLD	D4	27B		UWLCPD	00	=		TWDBPB4 BUSS
XA443	TLD	D4	25B		(51)	01		UWDDHO UWLCPQ 47 25B 49 26B	
XA540	EOR	B3	14A		UWLCPER	00	=		
XA540	EOR	B3	13A		(25)	01		TW07BPR UWLCPQ 26 13A 24 12A	
					UWLCPI	00	=		
XA539	TDD	CI	13A		( )	01		UWLCPER 24 13A	
					UWLCPN	00	=		
XA539	TDD	CN	14A		( )	01		UWLK00 26 14A	
XA539	TDD	CP	11B		UWLCPP	00	=		
XA539	TDD	CP	12A		(23)	01		SP1031 22 12A	
XA539	TDD	CQ	12B		UWLCPQ	00	=		
XA539	TDD	CQ	13B		(25)	01		UWLS0A 27 13B	
XA443	TLD	E1	31A		UWLCOD	00	=		TWDB0B4 BUSS
XA443	TLD	E1	32A		(66)	01		UWDDGO UWLCOQ 68 32A 70 33A	
XA540	EOR	B4	11A		UWLCOER	00	=		
XA540	EOR	B4	10A		(22)	01		TW00DTA UWLCOQ 20 10A 18 09A	
					UWLCOI	00	=		
XA539	TDD	EI	19A		( )	01		UWLCOER 40 19A	
					UWLCON	00	=		
XA539	TDD	EN	20A		( )	01		UWLK00 42 20A	

H78-16 409

DATA SYSTEMS DIVISION  
LITTON SYSTEMS INC.  
LITTON INDUSTRIES

DRAWING NUMBER 149015-860  
UNIT ASSEMBLY NAME CARD CAGE ASSEMBLY IFCU

TM 11-5895-856-34-10/EE640-CA-MMI-100/E154 CPU/TO 31W2-2T-122-10

LOGIC UNIT ASSEMBLY NO. 149015 REV. C INDEX UWLCOP  
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CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERMINAL	DESIGNATOR	FACTOR	COMMENT
XA539	TDD	EP	17B	UWLCOP	00	=		
XA539	TDD	EP	18A	(35)	01	=	SPI031 38 18A	
XA539	TDD	EQ	18B	UWLC0Q	00	=		
XA539	TDD	EQ	19B	(37)	01	=	UWL50A 39 19B	
XA443	TLD	E2	28A	UWLC1D	00	=		
XA443	TLD	E2	29A	(60)	01	=	UWDDG0 UWLC1Q 62 29A 64 30A	TWDB184 BUSS
XA540	EOR	C1	16B	UWLC1E	00	=		
XA540	EOR	C1	15B	(33)	01	=	TW00DTB UWLC1Q 31 15B 29 14B	
				UWLC1I	00	=		
XA539	TDD	FI	16A	( )	01	=	UWLC1E 34 16A	
				UWLC1N	00	=		
XA539	TDD	FN	15A	( )	01	=	UWLK00 30 15A	
XA539	TDD	FP	16B	UWLC1P	00	=		
XA539	TDD	FP	17A	(33)	01	=	SPI031 36 17A	
XA539	TDD	FQ	15B	UWLC1Q	00	=		
XA539	TDD	FQ	14B	(31)	01	=	UWL50A 29 14B	
XA443	TLD	E3	30B	UWLC2D	00	=		
XA443	TLD	E3	28B	(57)	01	=	UWDDG0 UWLC2Q 53 28B 55 29B	TWDB284 BUSS
XA540	EOR	C2	19B	UWLC2E	00	=		
XA540	EOR	C2	18B	(39)	01	=	TW00DTC UWLC2Q 37 18B 35 17B	
				UWLC2I	00	=		
XA539	TDD	GI	25A	( )	01	=	UWLC2E 54 25A	
				UWLC2N	00	=		
XA539	TDD	GN	26A	( )	01	=	UWLK00 56 26A	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TRF#	DESIGNATOR	FACTOR	COMMENT
XA539	TDD	GP	25B	UWLC2P	00	=		
XA539	TDD	GP	24A	(47)	01		SPI031 52 24A	
XA539	TDD	GQ	26B	UWLC2Q	00	=		
XA539	TDD	GQ	27B	(49)	01		UWLS0A 51 27B	
XA443	TLD	E4	33B	UWLC3D	00	=		TWDB3B4 BUSS
XA443	TLD	E4	31B	(63)	01		UWDDG0 UWLC3Q 59 31B 61 32B	
XA540	EOR	C3	20A	UWLC3ER	00	=		
XA540	EOR	C3	19A	(42)	01		TW00D0D UWLC3Q 40 19A 38 18A	
				UWLC3I	00	=		
XA539	TDD	HI	22A	( )	01		UWLC3ER 48 22A	
				UWLC3N	00	=		
XA539	TDD	HN	21A	( )	01		UWLK00 46 21A	
XA539	TDD	HP	24B	UWLC3P	00	=		
XA539	TDD	HP	23A	(45)	01		SPI031 50 23A	
XA539	TDD	HQ	23B	UWLC3Q	00	=		
XA539	TDD	HQ	22B	(43)	01		UWLS0A 41 22B	
XA443	TLD	F1	37B	UWLC4D	00	=		TWDB4B4 BUSS
XA443	TLD	F1	38B	(75)	01		UWDDH0 UWLC4Q 77 38B 79 39B	
XA540	EOR	C4	17A	UWLC4ER	00	=		
XA540	EOR	C4	16A	(36)	01		TW04D0TA UWLC4Q 34 16A 30 15A	
				UWLC4I	00	=		
XA539	TDD	JI	32A	( )	01		UWLC4ER 68 32A	
				UWLC4N	00	=		
XA539	TDD	JN	33A	( )	01		UWLK10 70 33A	



CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND/OR	EQUATION	TERN	DESIGNATOR	FACTOR	COMMENT
XA539	TDD	JP	31B	UWLC4P	00	=		
XA539	TDD	JP	31A	(59)	01		SPI031 66 31A	
XA539	TDD	JQ	32B	UWLC4Q	00	=		
XA539	TDD	JQ	33B	(61)	01		UWLS0A 63 33B	
XA443	TLD	F2	34A	UWLC5D	00	=		
XA443	TLD	F2	36A	(72)	01		UW00H0 UWLC5Q 71 36A 73 36B	TW085B4 BUSS
XA540	EOR	D1	24B	UWLC5ER	00	=		
XA540	EOR	D1	23B	(45)	01		TW040YB UWLC5Q 43 23B 41 22B	
				UWLC5I	00	=		
XA539	TDD	KI	29A	( )	01		UWLC5ER 62 29A	
				UWLC5N	00	=		
XA539	TDD	KN	28A	( )	01		UWLK10 60 28A	
XA539	TDD	KP	30B	UWLC5P	00	=		
XA539	TDD	KP	30A	(57)	01		SPI031 64 30A	
XA539	TDD	KQ	29B	UWLC5Q	00	=		
XA539	TDD	KQ	28B	(55)	01		UWLS0A 53 28B	
XA443	TLD	F3	35A	UWLC6D	00	=		
XA443	TLD	F3	34B	(69)	01		UW00H0 UWLC6Q 65 34B 74 35B	TW086B4 BUSS
XA540	EOR	D2	27B	UWLC6ER	00	=		
XA540	EOR	D2	26B	(51)	01		TW040TC UWLC6Q 49 26B 47 25B	
				UWLC6I	00	=		
XA539	TDD	LI	38B	( )	01		UWLC6ER 77 38B	
				UWLC6N	00	=		
XA539	TDD	LN	39B	( )	01		UWLK10 79 39B	

CONNECTOR	CIRCUIT TYPE	GROUP	TEST POINTS AND OR	EQUATION	TERMINAL	DESIGNATOR	FACTOR	COMMENT
XA539	TDD	LP	37A	UWLC6P	00	=		
XA539	TDD	LP	37B	(76)	01		SPI031 75 37B	
XA539	TDD	LQ	38A	UWLC6Q	00	=		
XA539	TDD	LQ	39A	(78)	01		UWLS0A 80 39A	
XA443	TLD	F4	39A	UWLC7D	00	=		
XA443	TLD	F4	37A	(80)	01		UWDDH0 UWLC7Q 76 37A 78 38A	TWDB7B4 BUSS
XA540	EOR	D3	26A	UWLC7ER	00	=		
XA540	EOR	D3	25A	(56)	01		TW040TD UWLC7Q 54 25A 52 24A	
				UWLC7I	00	=		
XA539	TDD	MI	36A	( )	01		UWLC7ER 71 36A	
				UWLC7N	00	=		
XA539	TDD	MN	34A	( )	01		UWLK10 72 34A	
XA539	TDD	MP	35A	UWLC7P	00	=		
XA539	TDD	MP	36B	(69)	01		SPI031 73 36B	
XA539	TDD	MQ	35B	UWLC7Q	00	=		
XA539	TDD	MQ	34B	(74)	01		UWLS0A 65 34B	
XA516	TQ2	D4	27B	UWLK0A	00	=		
XA516	TQ2	D4	25B	(51)	01		TWRI2S TWDBCO 47 25B 49 26B	
XA517	TQ2	C3	16B	UWLK00	00	=		
XA517	TQ2	C3	14B	(33)	01		UWLK0A SPI021 29 14B 31 15B	
XA517	TQ2	C4	19B	UWLK10	00	=		
XA517	TQ2	C4	17B	(39)	01		UWLK0A SPI021 35 17B 37 18B	
XA517	TQ2	C2	15A	UWLS0A	00	=		
XA517	TQ2	C2	16A	(30)	01		UWLS00 SPI021 34 16A 36 17A	

CONNECTOR	CHRYLOT TYPE	GROUP	TEST POINTS	EQUATION	TIME	DESIGN VARIABLE	FACTOR	COMMENT
XA516	TQ2	D3	24B	UWLS00	00	=		
XA516	TQ2	D3	22B	(45)	01		TWENOA SPI021 41 22B 43 23B	
XA518	TT3	F3	39A	UWRICO	00	=		
XA518	TT3	F3	35A	(80)	01		TWRI2R UWRIOA TLCC1R 69 35A 76 37A 78 38A	
XA512	TQ2	E2	28A	UWRIOA	00	=		
XA512	TQ2	E2	29A	(60)	01		TWRITQ TWRG1S 62 29A 64 30A	
XA518	TT3	A3	07B	UWRT8A	00	=		
XA518	TT3	A3	04B	(15)	01		TWRT30 TXR2CR TXDV1B 09 04B 11 05B 13 06B	
XA518	TT3	B1	11A	UWRT9A	00	=		
XA518	TT3	B1	12A	(20)	01		TWRT30 TXR3CR TXDV1B 22 12A 24 13A 26 14A	
XA518	TT3	E2	29B	UW48CA	00	=		
XA518	TT3	E2	28B	(55)	01		TWRITQ TWRG1S TXGN2A 53 28B 60 28A 62 29A	
XA524	TT3	F1	36B	UW58CA	00	=		
XA524	TT3	F1	37B	(73)	01		TWRITQ TLCC2S TXGN2A 75 37B 77 38B 79 39B	
XA522	TQ2	F4	39A	U078UC	00	=		
XA522	TQ2	F4	37A	(80)	01		SPI031 SPI022 76 37A 78 38A	
XA502	TS8	E1	31B	U1658A	00	=		WRITE COUNT 36-38US
XA502	TS8	E1	29B	(59)	01		TC90BP TC91BQ TC80BP TC84BP TC70BQ TC73BP SPI012 SPI018 55 29B 60 28A 61 32B 62 29A 64 30A 66 31A 68 32A 70 33A	
XA523	TQ2	F4	39A	U16580	00	=		
XA523	TQ2	F4	37A	(80)	01		U1658A SPI022 76 37A 78 38A	
XA527	TS8	D1	25B	U3618A	00	=		WRITE COUNT 16-18US
XA527	TS8	D1	23B	(47)	01		TC90BQ TC91BP TC80BP TC81BQ TC70BQ TC73BP SPI022 SPI024 43 23B 46 21A 48 22A 49 26B 50 23A 52 24A 54 25A 56 26A	
XA523	TQ2	F2	34A	U36180	00	=		
XA523	TQ2	F2	36A	(72)	01		U3618A SPI022 71 36A 73 36B	

By Order of the Secretaries of the Army, the Navy and the Air Force.

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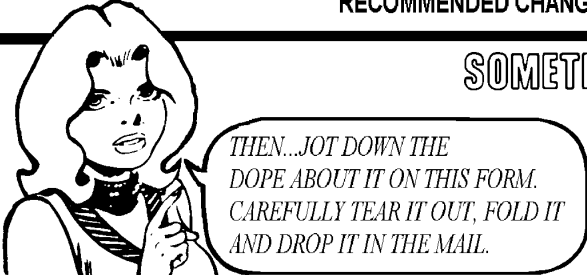
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## The Metric System and Equivalents

### Linear Measure

1 centimeter = 10 millimeters = .39 inch  
 1 decimeter = 10 centimeters = 3.94 inches  
 1 meter = 10 decimeters = 39.37 inches  
 1 dekameter = 10 meters = 32.8 feet  
 1 hectometer = 10 dekameters = 328.08 feet  
 1 kilometer = 10 hectometers = 3,280.8 feet

### Weights

1 centigram = 10 milligrams = .15 grain  
 1 decigram = 10 centigrams = 1.54 grains  
 1 gram = 10 decigrams = .035 ounce  
 1 decagram = 10 grams = .35 ounce  
 1 hectogram = 10 decagrams = 3.52 ounces  
 1 kilogram = 10 hectograms = 2.2 pounds  
 1 quintal = 100 kilograms = 220.46 pounds  
 1 metric ton = 10 quintals = 1.1 short tons

### Liquid Measure

1 centiliter = 10 milliliters = .34 fl. ounce  
 1 deciliter = 10 centiliters = 3.38 fl. ounces  
 1 liter = 10 deciliters = 33.81 fl. ounces  
 1 dekaliter = 10 liters = 2.64 gallons  
 1 hectoliter = 10 dekaliters = 26.42 gallons  
 1 kiloliter = 10 hectoliters = 264.18 gallons

### Square Measure

1 sq. centimeter = 100 sq. millimeters = .155 sq. inch  
 1 sq. decimeter = 100 sq. centimeters = 15.5 sq. inches  
 1 sq. meter (centare) = 100 sq. decimeters = 10.76 sq. feet  
 1 sq. dekameter (are) = 100 sq. meters = 1,076.4 sq. feet  
 1 sq. hectometer (hectare) = 100 sq. dekameters = 2.47 acres  
 1 sq. kilometer = 100 sq. hectometers = .386 sq. mile

### Cubic Measure

1 cu. centimeter = 1000 cu. millimeters = .06 cu. inch  
 1 cu. decimeter = 1000 cu. centimeters = 61.02 cu. inches  
 1 cu. meter = 1000 cu. decimeters = 35.31 cu. feet

## Approximate Conversion Factors

<i>To change</i>	<i>To</i>	<i>Multiply by</i>	<i>To change</i>	<i>To</i>	<i>Multiply by</i>
inches	centimeters	2.540	ounce-inches	Newton-meters	.007062
feet	meters	.305	centimeters	inches	.394
yards	meters	.914	meters	feet	3.280
miles	kilometers	1.609	meters	yards	1.094
square inches	square centimeters	6.451	kilometers	miles	.621
square feet	square meters	.093	square centimeters	square inches	.155
square yards	square meters	.836	square meters	square feet	10.764
square miles	square kilometers	2.590	square meters	square yards	1.196
acres	square hectometers	.405	square kilometers	square miles	.386
cubic feet	cubic meters	.028	square hectometers	acres	2.471
cubic yards	cubic meters	.765	cubic meters	cubic feet	35.315
fluid ounces	milliliters	29.573	cubic meters	cubic yards	1.308
pints	liters	.473	milliliters	fluid ounces	.034
quarts	liters	.946	liters	pints	2.113
gallons	liters	3.785	liters	quarts	1.057
ounces	grams	28.349	liters	gallons	.264
pounds	kilograms	.454	grams	ounces	.035
short tons	metric tons	.907	kilograms	pounds	2.205
pound-feet	Newton-meters	1.356	metric tons	short tons	1.102
pound-inches	Newton-meters	.11296			

### Temperature (Exact)

°F	Fahrenheit temperature	5/9 (after subtracting 32)	Celsius temperature	°C
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PIN:054153-000